Dear Sir/Madam,

Below are some comments and questions on the hash function requirement and evaluation.

1. comments on software: B.2 together with C.2.1 and C.2.2

   B.2 An ANSI C source language reference implementation and an optimized implementation. The optimized code will be used to compare software performance and memory requirements to the implementations of other submitted algorithms.

   C.2.1 Computational efficiency:

   C.2.2 Memory requirements:

There may be some confusion on the use of the term "an optimized implementation". The code optimized for software throughput may not be the same as the code optimized for memory and code size. Thus do we need two optimized implementations, one optimized for the computational efficiency evaluation, and another one optimized for the memory requirement evaluation?

The problem may become more complicated: an optimized implementation on which platform? The optimized code on the 32-bit platform may be different from the optimized code on the 64-bit platform.

2. comments on the security requirement

Is it possible to provide some numerical data to clearly specify the security requirements so as to eliminate the ambiguity? For example, for the hash function with 256-bit output, the collision resistance is with complexity $2^{128}$; (first- and second-) preimage resistance is with complexity $2^{256}.$
One more question: For a hash function with 256-bit output, it is supposed to use this hash function in the environment where no one can carry out $2^{128}$ computations. So what would be the meaningful requirement on the preimage resistance of such hash function?

Best Regards,
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