

# Side Channel Analysis of the SHA-3 Finalists



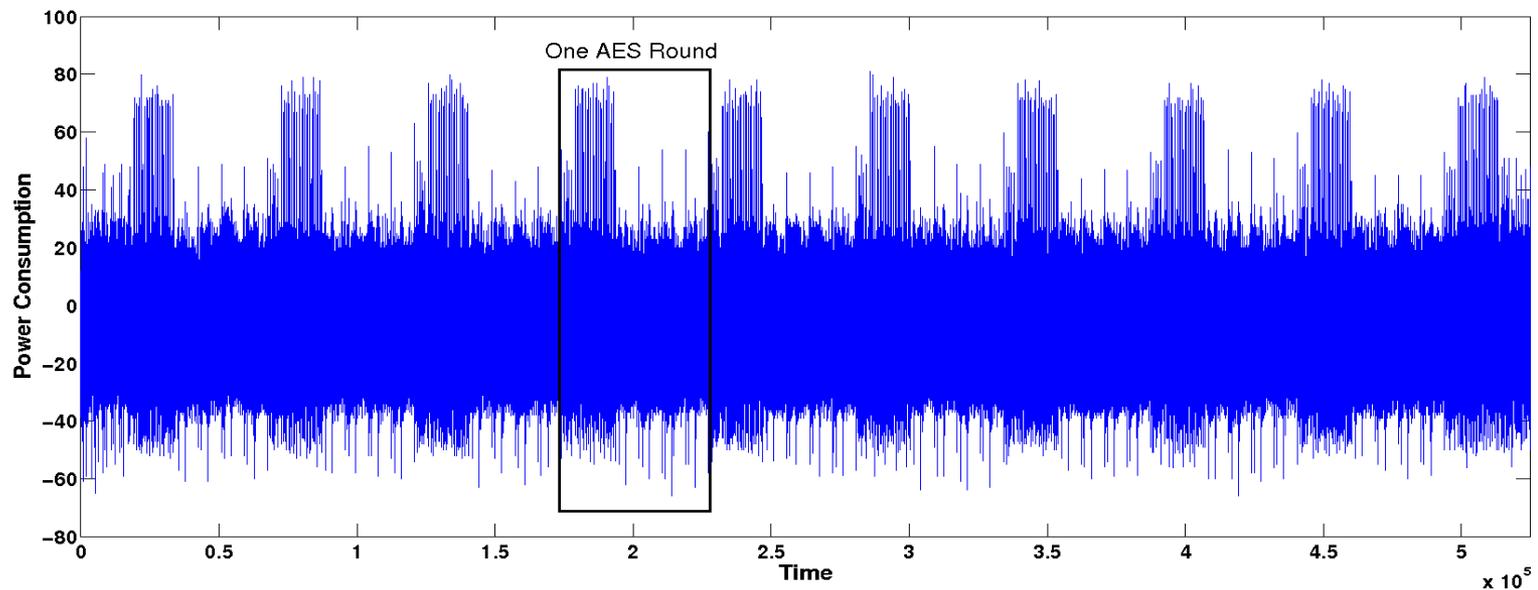
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# Side Channel Analysis - Power Analysis



- ◆ Power Analysis is based on the dependency of the power consumption on the processed data

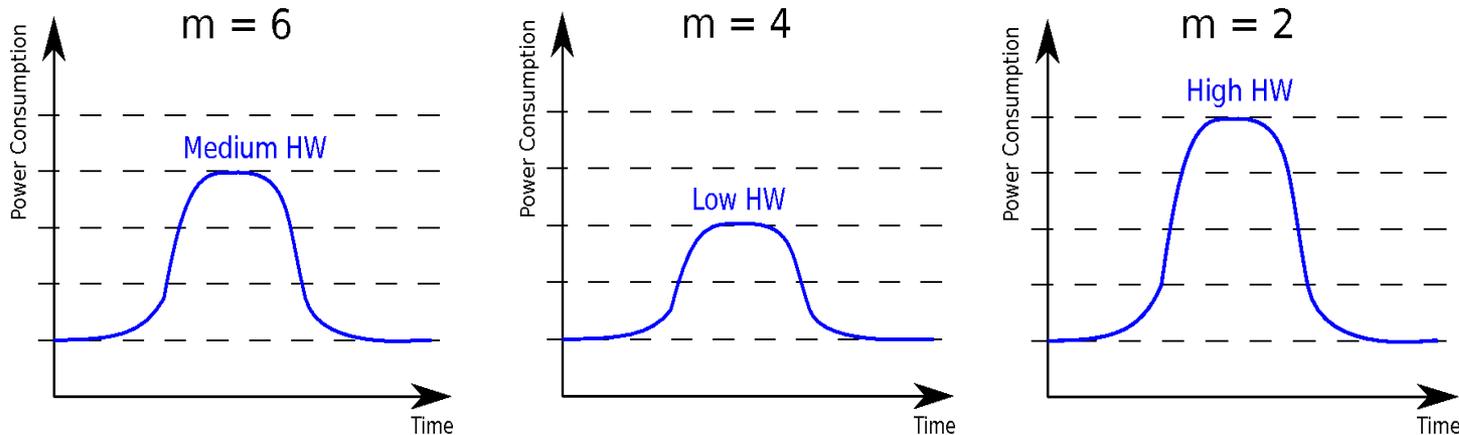


AES Power Trace

# Differential Power Analysis (DPA)



Device processes  $m \oplus \text{key}$

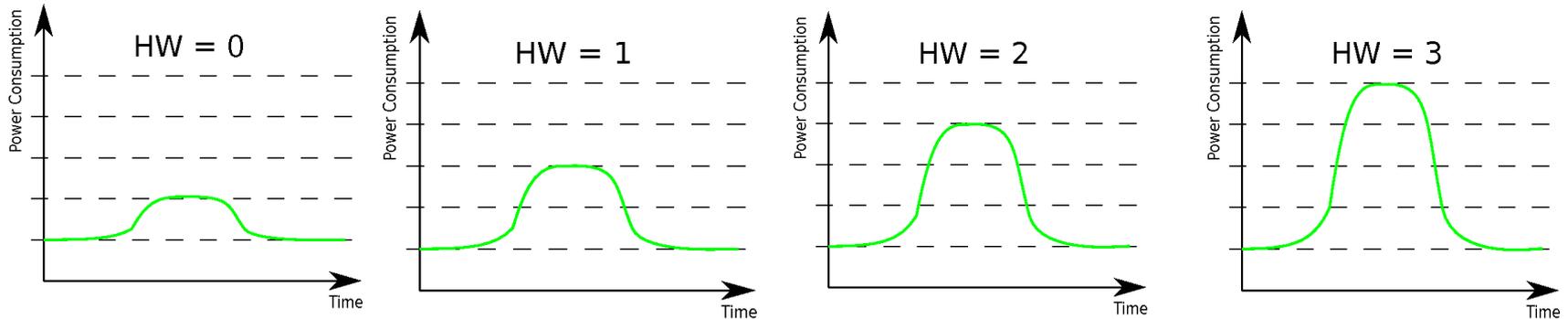


$$\text{HW}(4 \oplus \text{key}) < \text{HW}(6 \oplus \text{key}) < \text{HW}(2 \oplus \text{key})$$

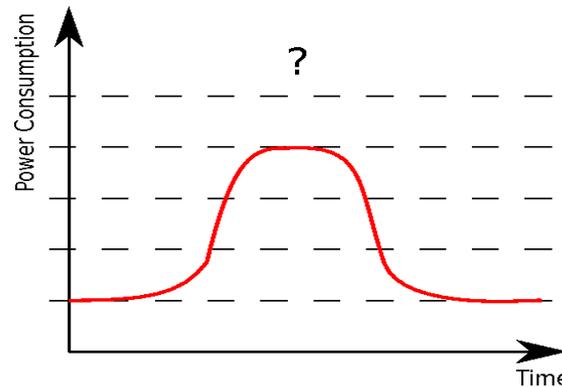
$$\Rightarrow \text{key} = 5$$

# Profiling Based Attacks

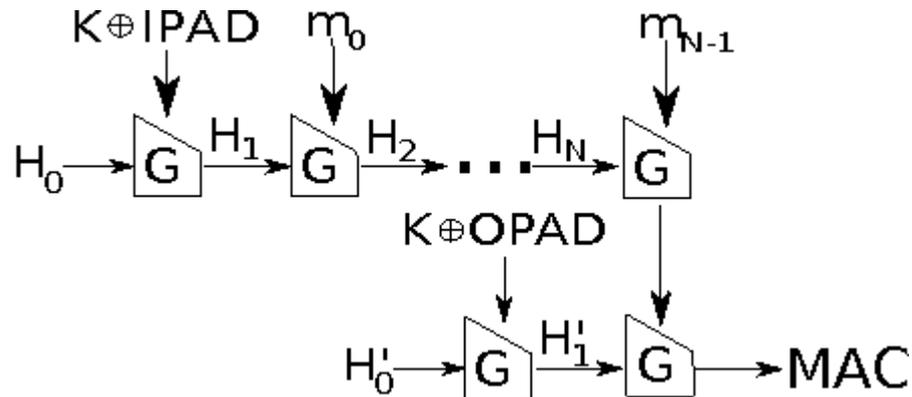
- ◆ First phase: profile the power consumption on a fully controllable device



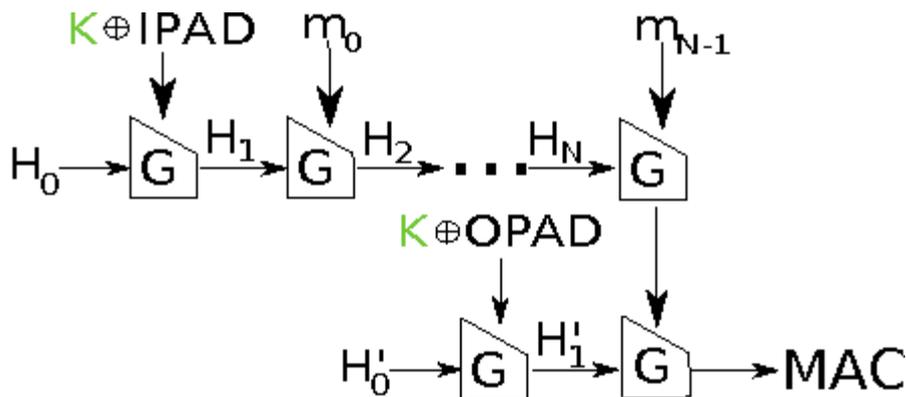
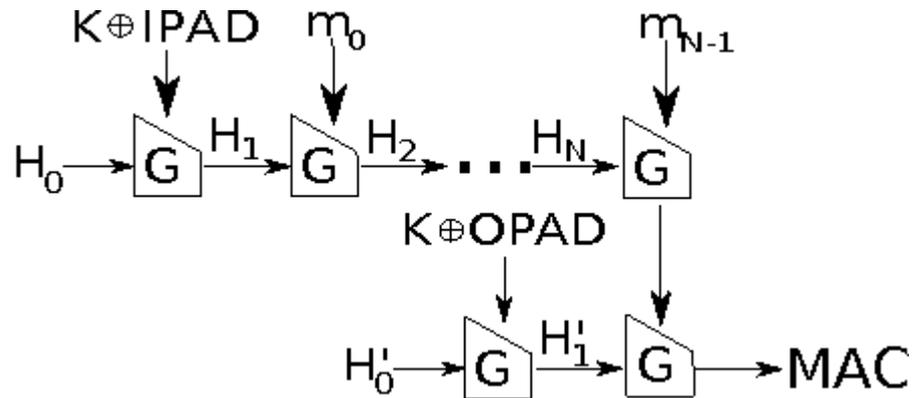
- ◆ Second phase: compare profiles to power consumption of attacked device



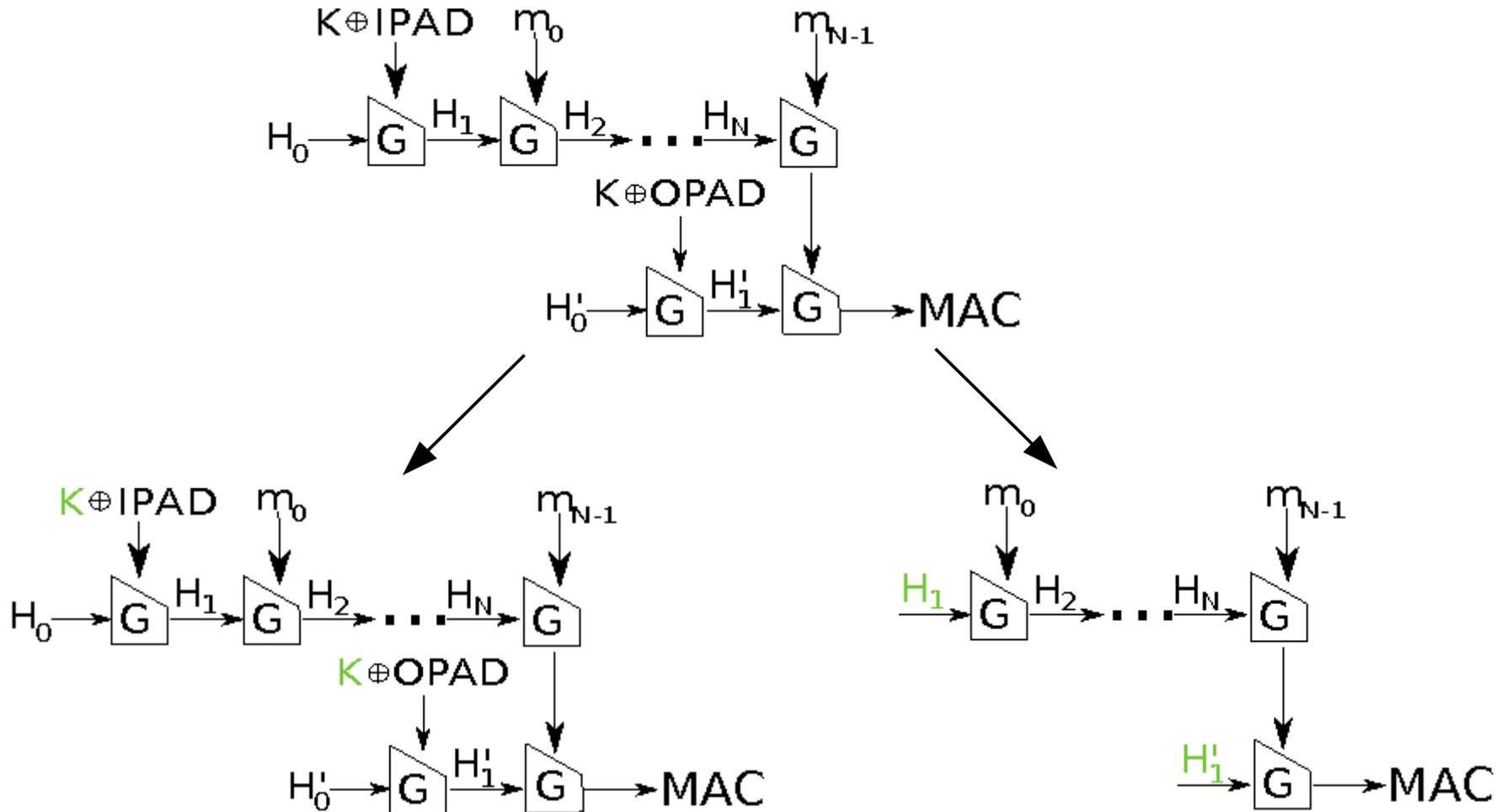
# Side Channel Attacks on MAC Functions



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# Side Channel Attacks on MAC Functions



# Side Channel Attacks against the SHA-3 Finalists



	Benoît et al. (DPA)
<b>BLAKE</b>	MAC Forgery
<b>Grøstl</b>	MAC Forgery
<b>JH</b>	-
<b>Keccak</b>	-
<b>Skein</b>	-

# Background for this Work



- ♦ We use the same power consumption model as Benoît et al., namely the Hamming weight model
- ♦ We analyzed:
  - ♦ Grøstl-MAC (Envelope MAC)
  - ♦ JH-HMAC
  - ♦ Keccak-MAC (built in MAC function)
  - ♦ Skein-MAC (built in MAC function)
- ♦ The attacks were verified on:
  - ♦ ATMega 256-1 microcontroller (8 bit register)
  - ♦ AVR Cortex M3 (32 bit register)

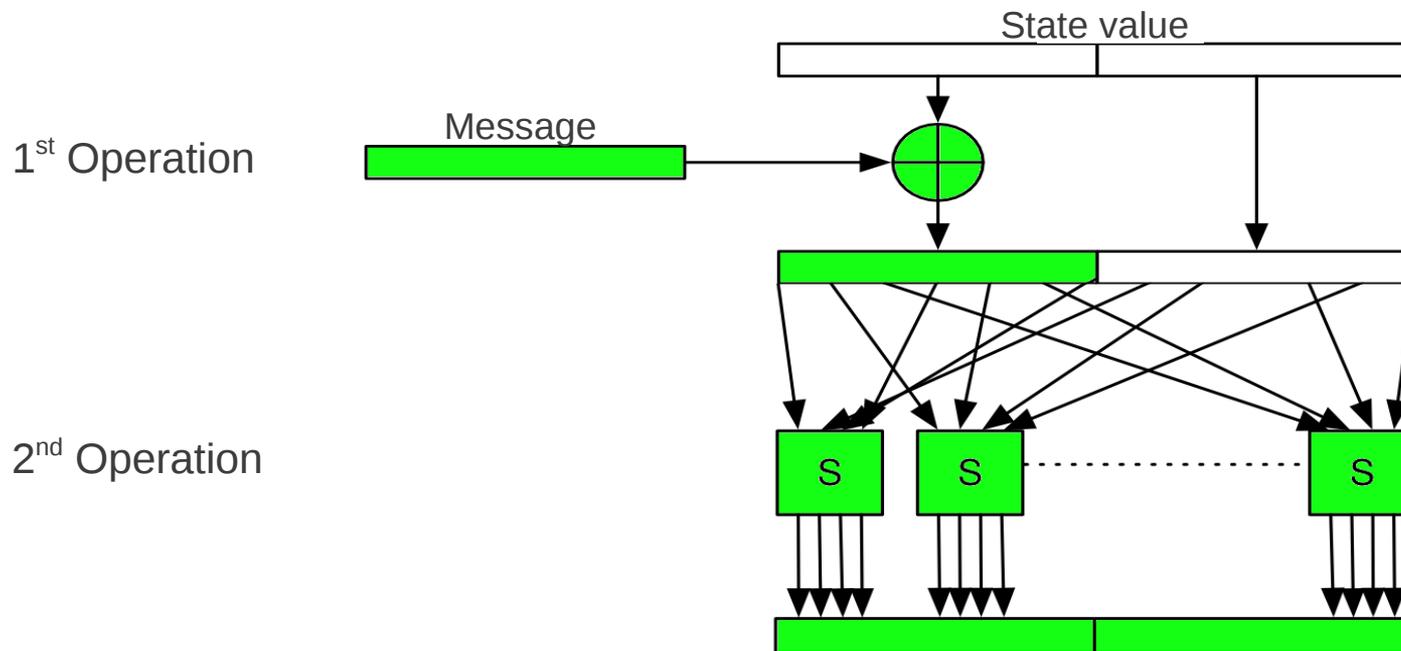
# Analysis of Grøstl



- ♦ Grøstl-MAC computes a MAC by hashing  $(\overline{K} \parallel \overline{M} \parallel K)$
- ♦ The attack, suggested by Benoît et al., can be altered to fit Grøstl-MAC
- ♦ A successful DPA is able to recover the processed key, since the last key  $K$  is processed with variable data

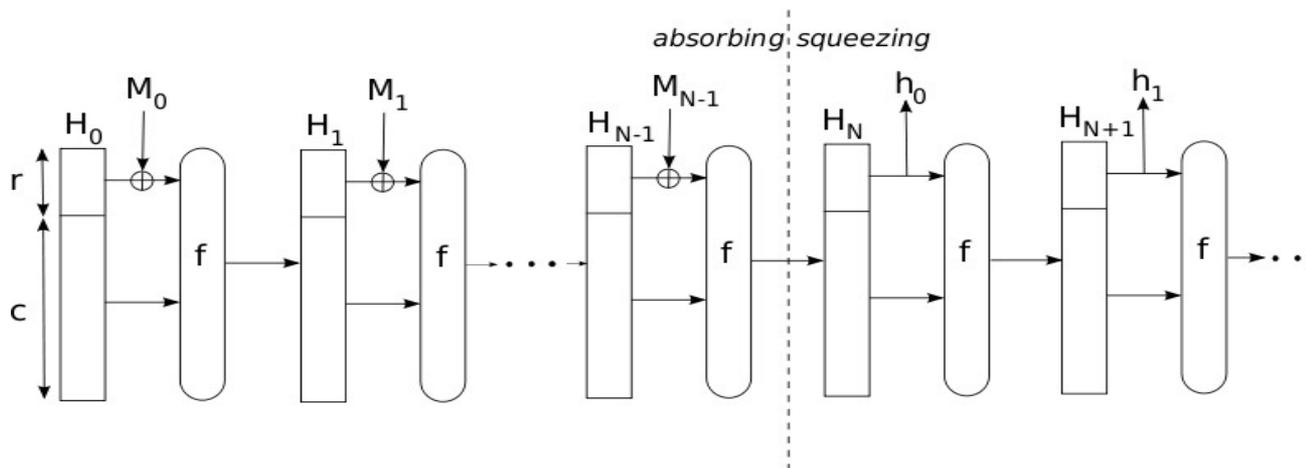
# Analysis of JH

- ♦ Two state values are needed for inner and outer hash function call
- ♦ For each state value, two operations have to be exploited



# Analysis of Keccak (1)

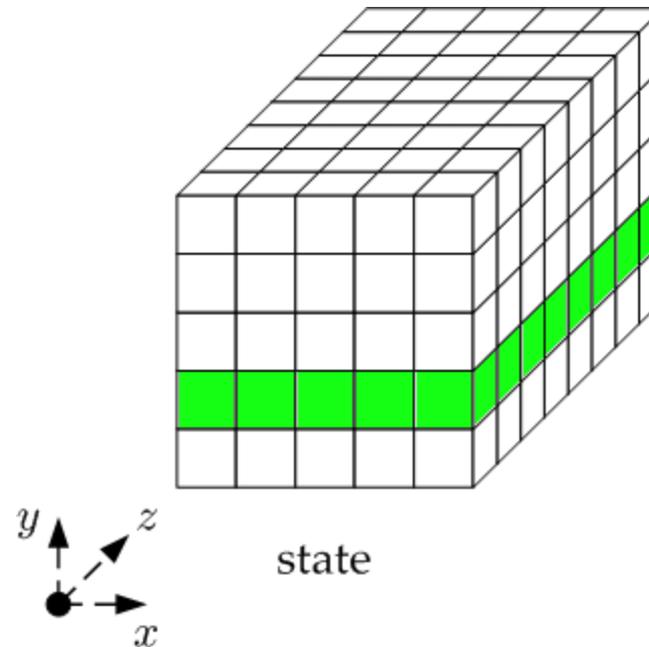
- ◆ Keccak-MAC hashes ( $K \parallel M$ )
- ◆ First exploit the XOR between the bitrate and the message



The Sponge Construction based on a permutation  $f$

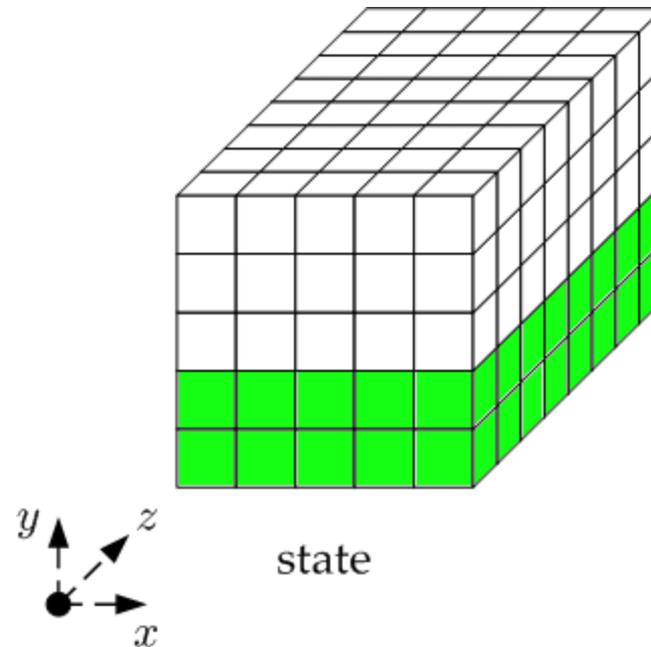
# Analysis of Keccak (2)

- ◆ Secondly exploit the XOR of the columns during  $\theta$  until all values are known
- ◆ If the key is only few bits long, a key recovery is possible



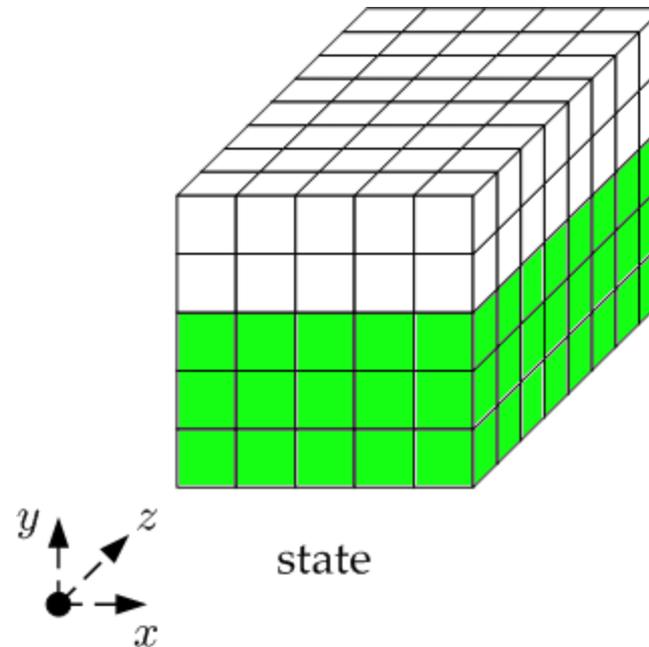
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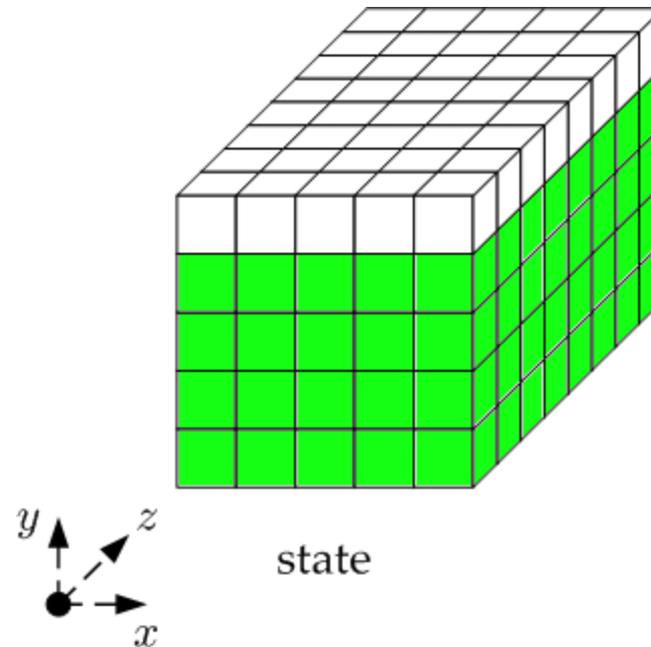
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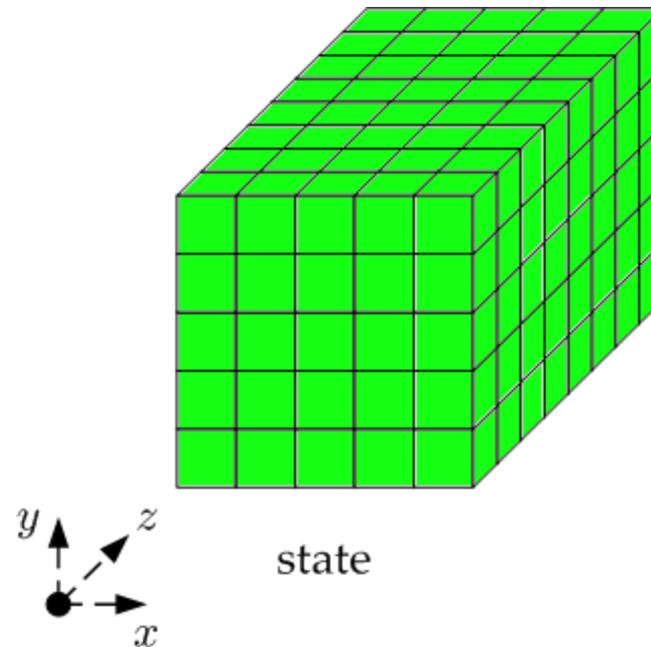
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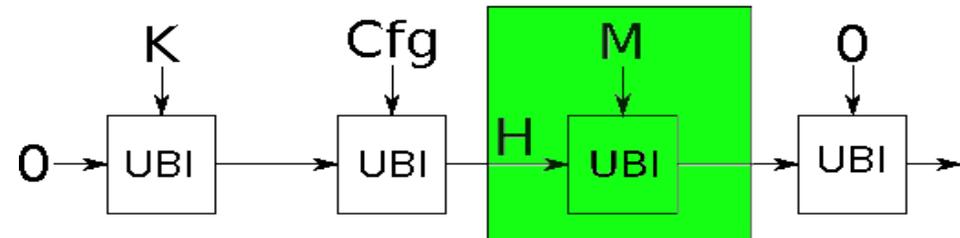
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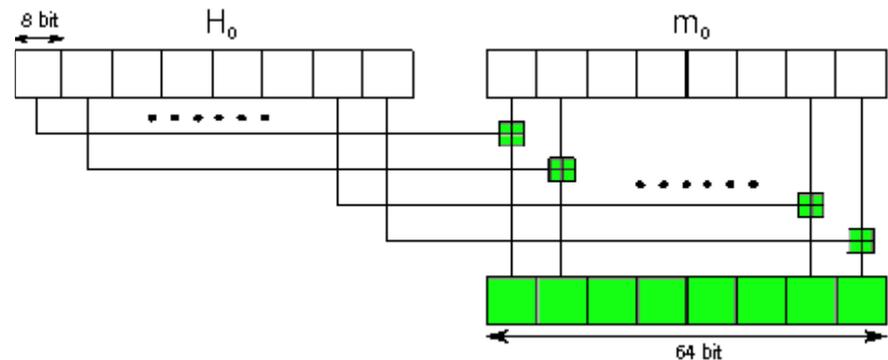


# Analysis of Skein

- ♦ Target the modular addition between the state value and the message
- ♦ Recover the key by dividing each 64 bit addition in eight 8 bit additions and attack them independently



Attacked UBI call



Split the 64 bit modular addition into 8 bit blocks and attack them independently

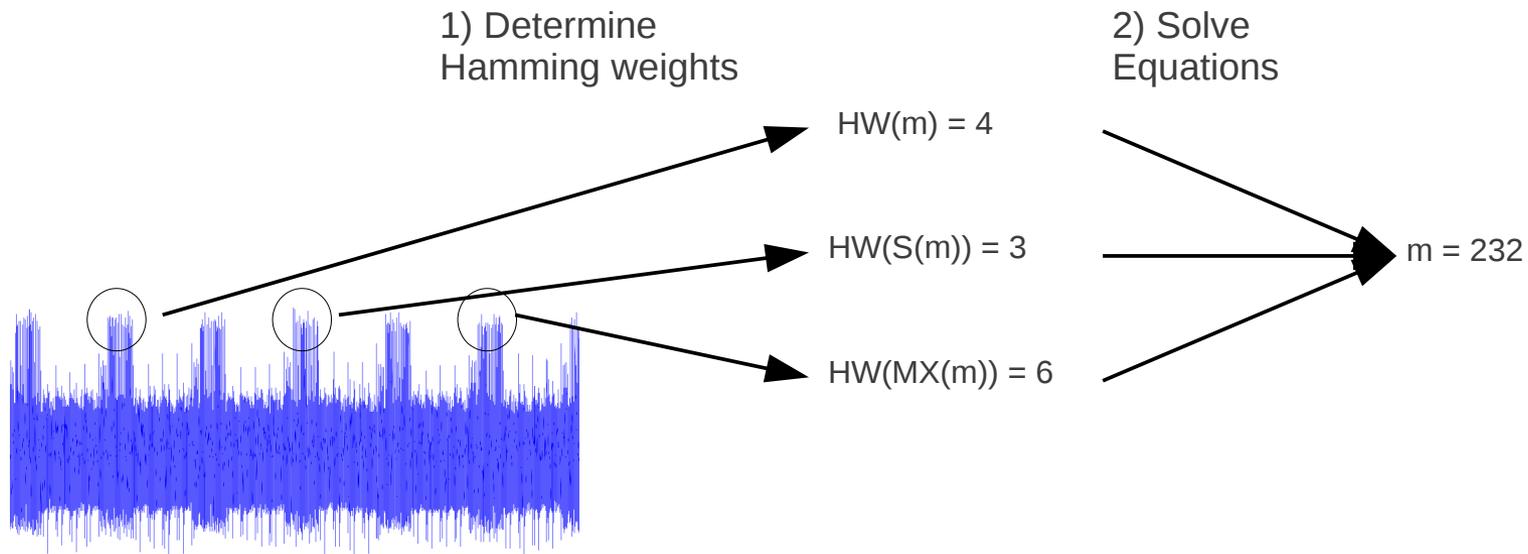
# Side Channel Attacks against the SHA-3 Finalists



	Benoît et al. (DPA)	This work (DPA)
<b>BLAKE</b>	MAC Forgery	-
<b>Grøstl</b>	MAC Forgery	Key Recovery
<b>JH</b>	-	MAC Forgery
<b>Keccak</b>	-	MAC Forgery (Key Recovery)
<b>Skein</b>	-	MAC Forgery

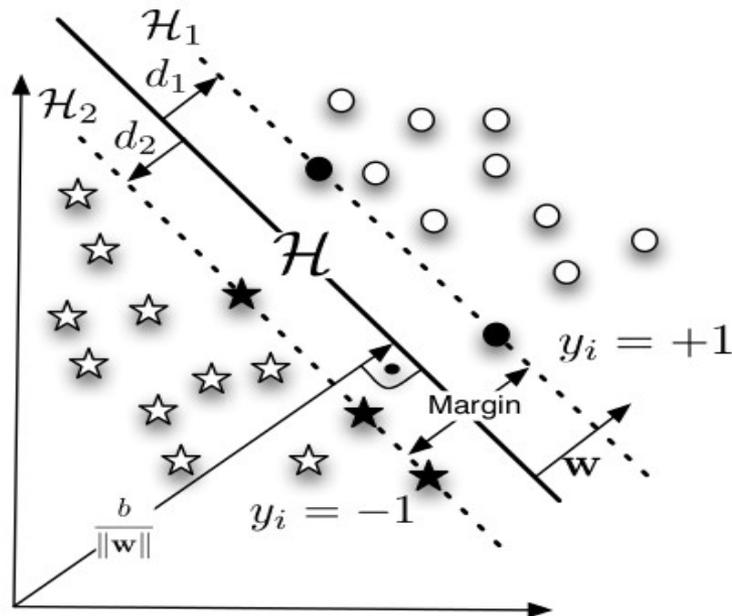
# Analysis of Grøstl

- Use algebraic side-channel analysis to recover the hashed message



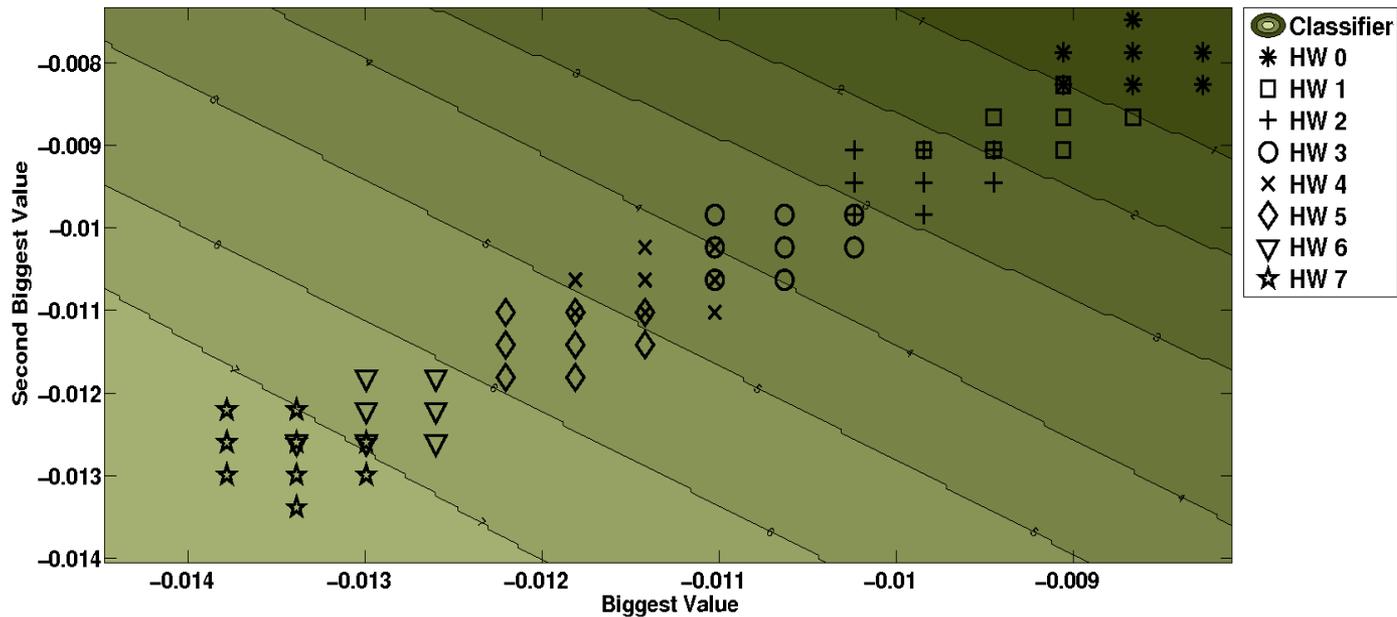
# 1) Determine Hamming weight (1)

- Support Vector Machines (SVM) are used for binary classification



# 1) Determine Hamming weight (2)

- ◆ Profiling Hamming weights using Support Vector Machines



## 2) Solve Equations



- ♦ The variables in the equation system are composed of:
  - HW of the input
  - HW of the S-box input
  - HW of the S-box output
  - HW of the MixBytes output
- ♦ Inserting the HW of these variables for the first two rounds (200 HW) allows solving the system

# Side Channel Attacks against the SHA-3 Finalists



	Benoît et al. (DPA)	This work (DPA)	This work (Profiling)
<b>BLAKE</b>	MAC Forgery	-	-
<b>Grøstl</b>	MAC Forgery	Key Recovery	Message Recovery
<b>JH</b>	-	MAC Forgery	-
<b>Keccak</b>	-	MAC Forgery (Key Recovery)	-
<b>Skein</b>	-	MAC Forgery	-

# Remarks



- ♦ The side channel analysis was performed for the Hamming weight leakage model, an analysis using a more complex model, such as the Hamming distance model, is more difficult
- ♦ Ranking the finalists in terms of side channel resistance is not possible since different implementations have different characteristics
- ♦ A feasibility study of the algebraic side channel attack for all finalists still remains

# Questions

