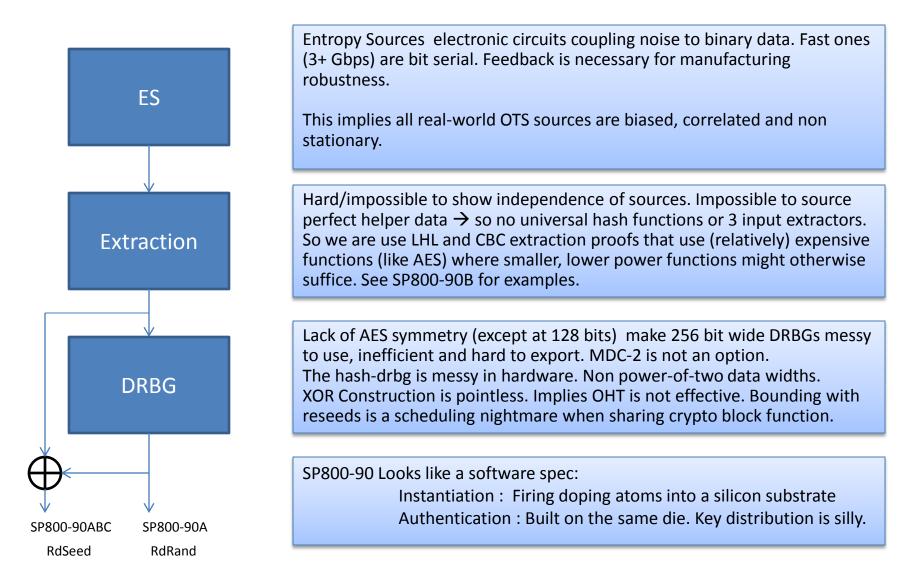
Intel and Random Numbers

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Inconvenient Truths

(about building crypto-secure random number sources)



What is Changing?

(in Intel products, that is pertinent to the problem to hand)

Basic cryptographic elements need to move:	Intel is doing this: AES-NI
 To hardware. 1) Contstant time 2) Limited side channels 3) Cheap and fast to software at all levels 	RdRand RdSeed (tools you can use approach)
 2) To non device models (like instructions) 1) Smaller attack surface 2) Bypsses OS, drivers, APIs and layers of bugs 	SHA-3 ? Poor scalability makes Keccak a problem.
3) Works in VMs.	Features for key management a

Wouldn't it be nice if SP800-90 and FIPS-140:

- Explicitly permit conformant RNGs as output only devices
- Define entropy quality statistically (because that is all the physical world gives you)

management a WIP.

- Expect conformant implementations to make statistical entropy quality and availability claims (because that is all the physical world gives you)
- Allow construction on the same die to imply implicit authentication.
 - Establishing and distributing keys on die to authenticate access to an on die RNG is a chicken and egg problem.