Seagate Secure Constellation® ES.2, Savvio® 10K.5 and Savvio® 15K.3 Self-Encrypting Drives FIPS 140 Module Security Policy

Security Level 2

Rev. 1.9 – November 05, 2015

Seagate Technology, LLC
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1 Introduction

1.1 Scope

This security policy applies to the FIPS 140-2 Cryptographic Module (CM) embedded in Seagate Constellation® ES.2, Savvio® 10K.5 and Savvio® 15K.3 Self-Encrypting Drives.

This document meets the requirements of the FIPS 140-2 standard (Appendix C) and Implementation Guidance (section 14.1). It does not provide interface details needed to develop a compliant application.

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1.2 Security Levels

<table>
<thead>
<tr>
<th>FIPS 140-2 Requirement Area</th>
<th>Security Level</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cryptographic Module Specification</td>
<td>2</td>
</tr>
<tr>
<td>Cryptographic Module Ports and Interfaces</td>
<td>2</td>
</tr>
<tr>
<td>Roles, Services and Authentication</td>
<td>2</td>
</tr>
<tr>
<td>Finite State Model</td>
<td>2</td>
</tr>
<tr>
<td>Physical Security</td>
<td>2</td>
</tr>
<tr>
<td>Operational Environment</td>
<td>N/A</td>
</tr>
<tr>
<td>Cryptographic Key Management</td>
<td>2</td>
</tr>
<tr>
<td>Electromagnetic Interface / Electromagnetic Compatibility (EMI / EMC)</td>
<td>3</td>
</tr>
<tr>
<td>Self – tests</td>
<td>2</td>
</tr>
<tr>
<td>Design Assurance</td>
<td>2</td>
</tr>
<tr>
<td>Mitigation of Other Attacks</td>
<td>N/A</td>
</tr>
</tbody>
</table>

The overall security level pursued for the cryptographic modules is Security Level 2.

1.3 References

1. FIPS PUB 140-2
2. Derived Test Requirements for FIPS PUB 140-2
3. Implementation Guidance for FIPS PUB 140-2 and the Cryptographic Module Validation Program
5. TCG Storage Architecture Core Specification, Specification Version 1.0, Revision 0.9, May 24, 2007
6. TCG Storage Interface Interactions Specification, Specification Version 1.0,
7. ATA-8 ACS
8. Serial ATA Rev 2.6 (SATA)
9. SCSI Primary Commands-4 Rev 15 (SPC-4)
10. SCSI Block Commands Rev15 (SBC-3)
11. Serial Attached SCSI-2 Rev 13 (SAS-2)

1.4 Acronyms

AES Advanced Encryption Standard (FIPS 197)
CBC Cipher Block Chaining, an operational mode of AES
CM Cryptographic Module
CO Crypto-officer
CSP Critical Security Parameter
DRBG Deterministic Random Bit Generator
MEK Media Encryption Key
FIPS 140 FIPS 140-2
HDA Head and Disk Assembly
HDD Hard Disk Drive
IV Initialization Vector for encryption operation
<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>LBA</td>
<td>Logical Block Address</td>
</tr>
<tr>
<td>MSID</td>
<td>Manufactured SID, public drive-unique value that is used as default PIN, TCG term</td>
</tr>
<tr>
<td>POR</td>
<td>Power-on Reset (power cycle)</td>
</tr>
<tr>
<td>PN</td>
<td>Part Number(s)</td>
</tr>
<tr>
<td>POST</td>
<td>Power on Self-Test</td>
</tr>
<tr>
<td>PSID</td>
<td>Physical SID, public drive-unique value</td>
</tr>
<tr>
<td>RNG</td>
<td>Random Number Generator</td>
</tr>
<tr>
<td>SID</td>
<td>Security ID, PIN for Drive Owner CO role, TCG term</td>
</tr>
<tr>
<td>SoC</td>
<td>System-on-a-Chip</td>
</tr>
<tr>
<td>SP</td>
<td>Security Provider or Security Partition (TCG), also Security Policy (FIPS 140)</td>
</tr>
</tbody>
</table>
2 Cryptographic Module Description

2.1 Overview

The Seagate Secure Constellation® ES.2, Savvio® 10K.5 and Savvio® 15K.3 Self-Encrypting Drives FIPS 140 Module is embodied in Seagate Constellation® ES.2, Savvio® 10K.5 and Savvio® 15K.3 Self-Encrypting Drives model disk drives. These products meet the performance requirements of the most demanding Enterprise applications. The cryptographic module (CM) provides a wide range of cryptographic services using FIPS approved algorithms. Services include hardware-based data encryption, instantaneous user data disposal with cryptographic erase, independently controlled and protected user data LBA bands and authenticated FW download. The services are provided through industry-standard TCG Enterprise SSC, SCSI and ATA protocols.

The CM has a multiple-chip embedded physical embodiment. The physical interface to the CM is a SATA or SAS connector. The logical interfaces are the industry-standard ATA (7), SCSI (9 & 10), TCG SWG (5), and Enterprise (4) protocols, carried on the SATA (8) or SAS (11) transport interface. The primary function of the module is to provide data encryption, access control and cryptographic erase of the data stored on the hard drive media. The human operator of the drive product interfaces with the CM through a “host” application on a host system.

The CM functionality is implemented in the ASIC, Serial Flash, SDRAM and firmware. Each of these components additionally provides non-security functionality that is logically isolated from the security functions. The drive media provides the non-volatile storage of the keys, CSPs and FW. This storage is in the “system area” of the media which is not logically accessible / addressable by the host application.

The ASICS are SoCs which have the following major logical functions: host interface using an industry standard SAS or SATA interface, a RW Channel interface to the HDA, interface to media motor controller, data encryption engines, and processing services which execute the firmware. An Approved Security Function, AES-256, is implemented in the data encryption engine.

During drive operation, the SDRAM hosts the firmware and the encrypted user data being transferred between the media and the ASIC.

The firmware is logically separated into four groups: ATA/SCSI command set, Security, Servo, and Read/Write. The FIPS 140 services are isolated in the Security section of the firmware.

Security functions fall into two categories. At-rest data is transferred to/from the drive’s media and encrypted/decrypted using ATA/SCSI write/read commands respectively. Other security operations, including authentication and management of cryptographic secrets, are accessed using ATA Security and Trusted Send/Receive or SCSI SECURITY PROTOCOL IN/OUT commands. The ATA Trusted Send/Receive and SCSI Security commands are actually wrappers for industry standard protocol TCG protocols.

2.2 Logical to Physical Port Mapping

For HW versions that support ATA protocol (defined in Section 2.3):

<table>
<thead>
<tr>
<th>FIPS 140-2 Interface</th>
<th>Module Ports</th>
</tr>
</thead>
<tbody>
<tr>
<td>Data Input</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Data Output</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Control Input</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Status Output</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Power Input</td>
<td>Power Connector</td>
</tr>
</tbody>
</table>

For HW versions that support SCSI protocol (defined in Section 2.3):
2.3 Product Versions

The following models and hardware versions (PNs) are validated with the following FW versions:

- **Savvio® 10K.5, 2.5-Inch, 10K-RPM, SAS Interface, 900/600/450/300 GB,**
  - 900 GB: 9XS066 [1, 7, 21], 9XS066-251 [2, 8, 13, 16, 20], 9XS066-257 [3, 9, 14, 17, 18], 9XS066-047 [4], 9XS066-090 [5, 11, 22, 23], 9XS066-031 [10, 19], 9XS066-037 [10, 19], 9XS066-046 [12]
  - 600 GB: 9XR066 [1, 7, 21], 9XR066-251 [2, 8, 13, 16, 20], 9XR066-257 [3, 9, 14, 17, 18], 9XR066-047 [4], 9XR066-090 [5, 11, 22, 23], 9XR066-038 [6, 15], 9XR066-046 [12]
  - 450 GB: 9XP066 [1, 7, 21], 9XP066-047 [4], 9XP066-090 [5, 11, 22, 23], 9XP066-046 [12]
  - 300 GB: 9XN066 [1, 7, 21], 9XN066-251 [2, 8, 13, 16, 20], 9XN066-257 [3, 9, 14, 17, 18], 9XN066-047 [4], 9XN066-090 [5, 11, 22, 23], 9XN066-046 [12]

FW Versions: A002 [1], CSF2 [2], CNF1 [3], NS03 [4], HF72 [5], NA00 [6], 0003 [7], CSF4 [8], CEF3 [9], CE01 [10], HF75 [11], 6E01 [12], CSF7 [13], CEF4 [14], F740 [15], CSF8 [16], CEF5 [17], CEF6 [18], CE06 [19], CSFA [20], 0004 [21], HF7C [22], HF7D [23]

- **Savvio® 15K.3, 2.5-Inch, 15K-RPM, SAS Interface, 300/146 GB,**
  - 300 GB: 9XM066 [24, 27, 37], 9XM066-251 [25, 28, 30, 32, 34, 36], 9XM066-257 [26, 29, 31, 33, 35]
  - 146 GB: 9XL066 [24, 27, 37], 9XL066-251 [25, 28, 30, 32, 34, 36], 9XL066-257 [26, 29, 31, 33, 35]

FW Versions: A002 [24], YSF3 [25], YNF2 [26], 0003 [27], YSF5 [28], YEF4 [29], YSF8 [30], YEF5 [31], YSF9 [32], YEF6 [33], YSBF [34], YEF9 [35], YSFC [36], 0004 [37]

- **Constellation® ES.2 7K, 3.5-Inch, 7K-RPM, SAS Interface, 3000 GB/2000 GB,**
  - 3000 GB: 9XT260 [38, 44, 56, 61, 62, 63], 9XT260-251 [39, 45, 49, 52, 57, 58, 59], 9XT260-257 [40, 46, 50, 52, 54, 60], 9XT260-038 [41, 51], 9XT260-047 [42], 9XT260-090 [43], 9XT260-031 [47, 55], 9XT260-037 [47, 55], 9XT260-046 [48]
  - 2000 GB: 9XT267 [44, 56, 61, 62]

FW Versions: 0002 [38], RSF3 [39], RNF3 [40], NQE1 [41], NS01 [42], NF72 [43], 0005 [44], RSF5 [45], REF5 [46], YE01 [47], 6EA1 [48], RSF8 [49], REF6 [50], NA00 [51], RSFA [52], REF7 [53], REF8 [54], YE04 [55], 0006 [56], RSFC [57], RSFD [58], RSFE [59], REFB [60], 0003 [61], 0004 [62], A006 [63]

- **Constellation® ES.2 7K, 3.5-Inch, 7K-RPM, SATA Interface, 3000 GB,**
  - 3000 GB: 9XT160 [64, 65]

FW Versions: F000 [64], F003 [65]

HW version that supports ATA protocol is:

- **Constellation® ES.2 7K, 3.5-Inch, 7K-RPM, SATA Interface, 3000/2000 GB.**
HW versions that support SCSI protocol are:

- Savvio® 10K.5, 2.5-Inch, 10K-RPM, SAS Interface, 900/600/450/300 GB,
- Savvio® 15K.3, 2.5-Inch, 15K-RPM, SAS Interface, 300/146 GB and
- Constellation® ES.2 7K, 3.5-Inch, 7K-RPM, SAS Interface, 3000 GB.

The photographs on the title page consist of representative HW versions of each model mentioned in this section.

### 2.4 FIPS Approved Algorithms

<table>
<thead>
<tr>
<th>Algorithm</th>
<th>Certificate Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASIC AES</td>
<td>#1416, #1417</td>
</tr>
<tr>
<td>Firmware AES</td>
<td>#1343</td>
</tr>
<tr>
<td>RSA</td>
<td>#650</td>
</tr>
<tr>
<td>SHA</td>
<td>#1225</td>
</tr>
<tr>
<td>800-90 DRBG</td>
<td>#62</td>
</tr>
</tbody>
</table>

Certificate #1416 is applicable for HW version that supports ATA protocol (defined in Section 2.3).

Certificate #1417 is applicable for HW versions that support SCSI protocol (defined in Section 2.3).

Certificates #1343, 650, 1225 and 62 are applicable for all HW versions of this Security Policy.

### 2.5 Self-Tests

<table>
<thead>
<tr>
<th>Function Tested</th>
<th>Self-Test Type</th>
<th>Implementation</th>
<th>Failure Behavior</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASIC AES</td>
<td>Power-On</td>
<td>Encrypt and Decrypt KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>Firmware AES</td>
<td>Power-On</td>
<td>Encrypt and Decrypt KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>RSA</td>
<td>Power-On</td>
<td>Verify KAT performed.</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>SHA-1</td>
<td>Power-On</td>
<td>Digest KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>SHA-256</td>
<td>Power-On</td>
<td>Digest KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>800-90 DRBG</td>
<td>Power-On</td>
<td>DRBG KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>Firmware Integrity Check</td>
<td>Power-On</td>
<td>16-bit CRC and ECC</td>
<td>Enters FW Integrity Error State and does not become operationally ready.</td>
</tr>
<tr>
<td>Firmware Load Check</td>
<td>Conditional: When new firmware is downloaded</td>
<td>RSA PKCS#1 signature verification of new firmware image is done before it can be loaded.</td>
<td>Firmware download is aborted.</td>
</tr>
<tr>
<td>800-90 DRBG</td>
<td>Conditional: When a random number is generated</td>
<td>Newly generated random number is compared to the previously generated random number. Test fails if they are equal.</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
</tbody>
</table>
2.6 FIPS 140 Approved Modes of Operation

Before the operator performs Secure Initialization steps detailed in Section 7.1, the drive will operate in a non FIPS compliant mode.

There are 2 approved modes of operation, “TCG Security” or “ATA Enhanced Security”. The modes provide the same FIPS services but with different command protocols and minor functional differences e.g. number of user ids. Note that the ATA Enhanced Security mode is only available on hardware versions that support the ATA protocol on the SATA interface.

The module’s FIPS modes of operation are enforced through configuration and policy. Violating these ongoing policy restrictions (detailed in Section 7.2) would mean that one is no longer using the drive in a FIPS compliant mode of operation. The operator can determine if the CM is operating in a FIPS approved mode by invoking the Show Status service (refer to Section 4.1).

The following sections describe the differences between the 2 modes.

2.6.1 TCG Security Mode

This mode has the capability to have multiple Users with independent access control to read/write/crypto erase independent data areas (LBA ranges). Note that by default there is a single “Global Range” that encompasses the whole user data area.

In addition to the Drive Owner and User(s) roles, this mode implements a CO role (EraseMaster) to administer the above capability.

2.6.2 ATA Enhanced Security Mode

This mode implements the Master and User roles, and lock/unlock/erase as defined in the ATA Security feature set as well as Sanitize feature set in ATA protocol. There is a single user data region which can be read/written/crypto-erased with one encryption key.

2.6.3 Entering FIPS Approved Modes of Operation

For models that supports the SCSI protocol (defined in Section 2.3) the CM will only operate in TCG Security mode. After the module is installed and configured per the Security Rules of this policy in Section 7.1, the drive is always in the Approved mode of operation except when a critical failure has been detected, causing a transition to a “Failed” state.

For models that support the ATA protocol (defined in Section 2.3), the operator may choose to initialize the CM to operate in either ATA Enhanced Security or TCG Security modes. After setting up (configuring) the module per the Security Rules of this policy, an operator can switch between the modes. To transition to ATA Enhanced Security Mode from uninitialized state, the Set PIN service is used on the User role. This mode corresponds to having a deactivated TCG Locking SP. To transition to TCG Security Mode, the host authenticates as BandMaster 0 or BandMaster 1 to the Locking SP from uninitialized state. The CM does not change mode across module resets. Note that to switch between the two modes the module must transition to the uninitialized state (exit FIPS mode service) which results in zeroization of keys and CSPs.

In some of these exit scenarios (e.g. repeated POST failure), the drive cannot be restored to FIPS mode and does not provide any FIPS services.

2.7 User Data Cryptographic Erase/Sanitize Methods

Since all user data is encrypted / decrypted by the CM for storage / retrieval on the drive media, the data can be erased/sanitized using cryptographic methods. The data is effectively erased/sanitized by changing the media encryption key (MEK). Thus, the FIPS 140 key management capability “zeroization” of the key effectively erases all the user data in that read operations will decrypt with a different key value and thus the data is not returned as it was written.

Other FIPS services can be used to erase all the other private keys and CSPs (see Section 2.8).
2.8 RevertSP Method

The TCG RevertSP method may be invoked to transition the CM back to the manufactured state (uninitialized). This corresponds to the Exit FIPS Mode service and is akin to a “restore to factory defaults” operation. This operation also provides a means to zeroize keys and CSPs. Subsequently, the CM has to be re-initialized before it can return to a FIPS compliant mode of operation. This RevertSP method is invoked as an unauthenticated service by virtue of the use of a public credential (PSID).

2.9 Show Status

Show status service can be used to determine if the drive is operational under the security constraints of FIPS. For this purpose TCG Level 0 Discovery mechanism is utilized. TCG Level 0 Discovery mechanism maybe invoked by the operator to know if drive in “use” or security “fail” state. If the Drive Security Life Cycle State is 0x80 then drive is in Use State i.e. security is operational. If the Drive Security Life Cycle State is 0xFF the drive is in security Fail State i.e. drive is not operational in terms of FIPS services.

In addition, for HW versions that support the SATA protocol then the Show Status service can be used to confirm the “SecurityOperatingMode”; i.e. FIPS Approved mode. The values of 0x01 or 0x02 correspond to ATA Enhanced Security Mode and TCG Security Mode respectively. The value 0x00 indicates the CM is in the uninitialized state.
3 Identification and Authentication (I&A) Policy

3.1 Operator Roles

Note: The following identifies the CO and User roles with a general description of the purposes. For further details of the services performed by each role in each FIPS mode, see section 4.1.

3.1.1 Crypto Officer Roles

3.1.1.1 Drive Owner

This CO role corresponds to the SID (Secure ID) Authority on the Admin SP as defined in Enterprise SSC [4]. This role is used to transition the CM to TCG Security Mode (applicable for SATA command interface) and to download a new FW image. Note: only a FIPS validated firmware version can be loaded to the module. Otherwise, the module is not operating in FIPS mode.

3.1.1.2 EraseMaster (TCG Security Mode)

This CO role corresponds to the same named role as defined in Enterprise SSC [4]. This role is used to enable/disable User roles, and erase user data region (LBA band). An operator is authenticated to this role with role-based authentication.

3.1.2 User Roles

3.1.2.1 BandMasters (0-15) (TCG Security Mode)

This user role corresponds to the same named role as defined in Enterprise SSC [4]. This role is used to lock/unlock and configure a user data band (“LBA band”) for read/write access.

A CM can be configured to support up to 16 user data bands, which are controlled by their respective BandMaster credentials. By default 2 user bands are enabled. BandMasters are enabled/disabled using the EraseMaster role. An operator is authenticated to the BandMaster role with identity-based authentication. If a user data band is erased (EraseMaster service) then the BandMaster PIN is reset to MSID.

3.1.2.2 User (ATA Enhanced Security Mode)

This role corresponds to the same named role as defined in ATA [7]. It can unlock (and also lock) the drive so that an operator can read and write data to the drive. This role can also use the Cryptographic Erase service.

3.1.2.3 Master (ATA Enhanced Security Mode)

This role corresponds to the same named role as defined in ATA [7]. This role only provides a backup authentication to the ATA User and does not have access to administration services beyond those of the ATA User role.

3.1.3 Unauthenticated Role

This role can perform the Show Status service.

If the operator has physical access to the drive, this role can also reset the module with a power cycle (which results in POSTs). This role can also use the public PSID value to invoke the Exit FIPS Mode service. See section 4.1 for details.

3.2 Authentication

3.2.1 Authentication Types

Some operator roles have role-based authentication and others have identity-based authentication. For example, the Drive Owner role uses role-based authentication as there is only one ID and one PIN. In TCG Security Mode, the CM has up to 16 User operators. Each of these operators is assigned a unique ID to which a PIN is associated, thus this provides identity-based authentication.

For some services the authentication is performed in a separate associated service; e.g. the Read Unlock service is the authentication for subsequent User Data Read service. If the User Data Read service is attempted without prior authentication then the command will fail.
3.2.2 Authentication in ATA Enhanced Security Mode

In ATA Enhanced Security Mode, Master and User operator authentication is provided through a PIN provided in the ATA Security command [7]. In the event of authentication failure, the ATA command will abort, and subsequent read/write services will abort. A password attempt counter is implemented as specified in ATA, which when reached, blocks Master/User service authentication (with command abort), until the module is reset (Unblock PIN service).

Depending on a parameter of the Set PIN service for the User password, the User services may or may not be fully extended to the Master role. If the Master Password Capability is set to “High”, then either role can access the same services. Otherwise the Master role only has access to the erase service.

Drive Owner authentication for the Set PIN and Enable/Disable FW Download services is provided through the TCG Authenticate to Admin SP.

3.2.3 Authentication in TCG Security Mode

Operator authentication is provided within a TCG session. The host application can have only a single session open at a time. Authentication of an operator, using the TCG interface, uses the Authenticate method to authenticate to a role after a session has been started. Authentications will persist until the session is closed.

During a session the application can invoke services for which the authenticated operator has access control. Note that a security rule of the CM is that the host must not authenticate to more than one operator (TCG authority) in a session.

For the Show Status the host application will authenticate to the “Anybody” authority which does not have a private credential. Therefore this operation is effectively an unauthenticated service.

3.2.4 Authentication Mechanism, Data and Strength

Operator authentication with PINs is implemented by hashing the operator input value and comparing it to the stored hash of the assigned PIN. The PINs have a retry attribute (“TryLimit”) that controls the number of unsuccessful attempts before the authentication is blocked until a module reset. The PINs have a maximum length of 32 bytes.

Per the policy security rules, the minimum PIN length is 4 bytes (Rule 3 in Section 7.1). This gives a probability of $1/2^{32}$ of guessing the PIN in a single random attempt. This easily meets the FIPS 140 authentication strength requirements of less than 1/1,000,000.

In TCG interface, each failed authentication attempt takes a minimum of 15ms to complete. Thus a maximum of $(60*1000)/15$ attempts can be processed in one minute. Thus the probability of multiple random attempts to succeed in one minute is $4000/2^{32}$. This is significantly lower than the FIPS requirement of 1/100,000.

In ATA security interface, the PIN blocking feature limits the number of unsuccessful attempts to 5 (it “unblocks” with module reset) and the minimum time for a module reset is about 6.8 seconds (about 10/min). Thus the probability of multiple random attempts to succeed is $10/2^{32}$. This is significantly lower than the FIPS requirement of 1/100,000.

3.2.5 Personalizing Authentication Data

The initial value for SID and various other PINs is a manufactured value (mSID). This is a device-unique, 32-byte, public value. The Security Rules (Section 7) for the CM requires that the PIN values must be “personalized” to private values using the “Set PIN” service. Note that for ATA Enhanced Security Mode, setting the User PIN also sets the Drive Owner PIN to the same value; the Drive Owner PIN can be set to a different value with the TCG Set Method.
4 Access Control Policy

4.1 Services

The following tables represent the FIPS 140 services for each FIPS Approved Mode in terms of the Approved Security Functions and operator access control.

Hardware versions that support ATA protocol (defined in Section 2.3) provide services indicated in Tables 1.1 and 1.2 (when in TCG Security Mode), Tables 2.1 and 2.2 (when in ATA Enhanced Security Mode).

Hardware versions that support SCSI protocol (defined in Section 2.3) provide services in Tables 1.1 and 1.2 (when in TCG Security Mode).

For cryptographic algorithm certificates and hardware version association, refer to Section 2.4.

Note the following:

- Use of the services described below is only compliant if the module is in the noted Approved mode.
- Underlying security functions used by higher level algorithms are not represented (e.g. hashing as part of asymmetric key)
- Operator authentication is not represented in this table.
- Some security functions listed are used solely to protect / encrypt keys and CSPs.
- Service input and output details are defined by the TCG, SCSI and ATA standards.
- Unauthenticated services (e.g. Show Status) do not provide access to private keys or CSPs.
- * Some services have indirect access control provided through enable / disable or lock / unlock services used by an authenticated operator; e.g. User data read / write.
- If the Operator value contains “opt” then the access is dependent on the module setup (see 3.2.2).
### Table 1.1 - FIPS 140 Authenticated Services (TCG Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Set PIN</td>
<td>Change operator authentication data.</td>
<td>EraseMaster, BandMasters, Drive Owner</td>
<td>Hashing</td>
<td>TCG Set Method</td>
</tr>
<tr>
<td>Lock / Unlock FW Download Port</td>
<td>Enable / Disable FW Download Service</td>
<td>Drive Owner</td>
<td>None</td>
<td>TCG Set Method</td>
</tr>
<tr>
<td>Firmware Download</td>
<td>Load complete firmware image. If the self-test of the code load passes then the device will run with the new code.</td>
<td>None**</td>
<td>Asymmetric Key</td>
<td>SCSI Write Buffer, ATA DOWNLOAD MICROCODE</td>
</tr>
<tr>
<td>Enable / Disable BandMasters</td>
<td>Enable / Disable a User Authority.</td>
<td>EraseMaster</td>
<td>None</td>
<td>TCG Set Method</td>
</tr>
<tr>
<td>Set Range Attributes</td>
<td>Set the location, size, and locking attributes of the LBA range.</td>
<td>BandMasters</td>
<td>None</td>
<td>TCG Set Method</td>
</tr>
<tr>
<td>Lock / Unlock User Data Range for Read and/or Write</td>
<td>Block or allow read (decrypt) / write (encrypt) of user data in a range.</td>
<td>BandMasters</td>
<td>None</td>
<td>TCG Set Method, ATA SECURITY UNLOCK</td>
</tr>
<tr>
<td>User Data Read / Write</td>
<td>Encryption / decryption of user data to/from a LBA range. Access control to this service is provided through Lock / Unlock User Data Range.</td>
<td>None*</td>
<td>Symmetric Key</td>
<td>SCSI Read, Write Commands ATA Read, Write Commands</td>
</tr>
<tr>
<td>Cryptographic Erase</td>
<td>Erase user data in an LBA range by cryptographic means: changing the encryption key. BandMaster PIN is also reset.</td>
<td>EraseMaster, RNG, Symmetric Key</td>
<td></td>
<td>TCG Erase Method</td>
</tr>
</tbody>
</table>

### Table 1.2 - FIPS 140 Unauthenticated Services (TCG Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Show Status</td>
<td>Reports if the CM is operational in terms of FIPS services and current FIPS approved mode***.</td>
<td>None</td>
<td>None</td>
<td>TCG Level 0 Discovery, Drive Security Life Cycle State***</td>
</tr>
<tr>
<td>Reset Module</td>
<td>Runs POSTs and zeroizes key &amp; CSP RAM.</td>
<td>None</td>
<td>None</td>
<td>POR</td>
</tr>
<tr>
<td>DRBG Generate Bytes</td>
<td>Returns an SP 800-90 DRBG Random Number of 256 bytes</td>
<td>None</td>
<td>None</td>
<td>TCG Random()</td>
</tr>
<tr>
<td>Exit FIPS Mode</td>
<td>Exit Approved Mode of Operation. Note: CM will enter non-FIPS mode.</td>
<td>None (using PSID)</td>
<td>None</td>
<td>TCG AdminSP.RevertSP()</td>
</tr>
</tbody>
</table>
### Table 2.1 - FIPS 140 Services – Authenticated Services  
(ATA Enhanced Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Set PIN</td>
<td>Change operator authentication data. Note: Setting the User PIN also sets the Drive Owner PIN.</td>
<td>Master, User, Drive Owner</td>
<td>Hashing</td>
<td>ATA SECURITY SET PASSWORD, TCG Set Method</td>
</tr>
<tr>
<td>Lock / Unlock FW Download</td>
<td>Enable / Disable FW Download Service</td>
<td>Drive Owner</td>
<td>None</td>
<td>TCG Set Method</td>
</tr>
<tr>
<td>Firmware Download</td>
<td>Load complete firmware image. If the self-test of the code load passes then the device will run with the new code.</td>
<td>None**</td>
<td>Asymmetric Key</td>
<td>ATA DOWNLOAD MICROCODE</td>
</tr>
<tr>
<td>Unlock User Data</td>
<td>Enable user data read/write and Set PIN services.</td>
<td>User (opt. Master)</td>
<td>Symmetric Key</td>
<td>ATA SECURITY UNLOCK</td>
</tr>
<tr>
<td>User Data Read / Write</td>
<td>Encryption / decryption of user data.</td>
<td>None*</td>
<td>Symmetric Key</td>
<td>ATA Read / Write Commands</td>
</tr>
<tr>
<td>Cryptographic Erase</td>
<td>Erase user data through cryptographic means: by zeroizing the encryption key and the User PIN. Note: FIPS mode is exited.</td>
<td>Master, User</td>
<td>RNG</td>
<td>ATA SECURITY ERASE PREPARE + ATA SECURITY ERASE UNIT</td>
</tr>
<tr>
<td>Sanitize</td>
<td>Sanitize user data through cryptographic means: by zeroizing the encryption key.</td>
<td>None*</td>
<td>RNG</td>
<td>ATA CRYPTO SCRAMBLE</td>
</tr>
<tr>
<td>Disable Services</td>
<td>Disables ATA Security commands until POR</td>
<td>None*</td>
<td>None</td>
<td>ATA SECURITY FREEZE LOCK</td>
</tr>
</tbody>
</table>
| Exit FIPS Mode               | Exit Approved Mode of Operation. Note: CM will enter non-FIPS mode.          | User*(opt. Master*)     | RNG, Hashing, Symmetric Key            | ATA SECURITY DISABLE PASSWORD  
ATA SECURITY ERASE PREPARE+ SECURITY ERASE UNIT                                    |

### Table 2.2 - FIPS 140 Unauthenticated Services  
(ATA Enhanced Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Unblock PIN</td>
<td>Reset Master and User password attempt counter.</td>
<td>None</td>
<td>None</td>
<td>POR</td>
</tr>
<tr>
<td>Show Status</td>
<td>Reports if the CM is: operation in terms of FIPS services and current FIPS approved mode***.</td>
<td>None</td>
<td>None</td>
<td>TCG Level 0 Discovery, Drive Security Life Cycle State***</td>
</tr>
<tr>
<td>Reset Module</td>
<td>Runs POSTs and zeroizes key &amp; CSP RAM storage.</td>
<td>None</td>
<td>None</td>
<td>POR</td>
</tr>
<tr>
<td>Exit FIPS Mode</td>
<td>Exit Approved Mode of Operation. Note: CM will enter non-FIPS mode.</td>
<td>None (using PSID)</td>
<td>None</td>
<td>TCG AdminSP.RevertSP()</td>
</tr>
</tbody>
</table>

*Security has to be Unlocked  
**FW Download Port has to be Unlocked  
*** Applicable for CM that support SATA protocol (refer to Section 2.9)
4.2 Cryptographic Keys and CSPs

The following table defines the keys / CSPs and the operators / services which use them. It also describes the lifecycle of these data items in terms of initial value, input / output, storage and zeroization. Note the following:

- Lifecycle – Initial Value represents the value before the required Security Rules for module setup have been completed. An initial value of “undefined” means that there is no way to authenticate to the associated operator until it has been set by the operator.
- The use of PIN CSPs for authentication is implied by the operator access control.
- The Set PIN service is represented in this table even though generally it is only used at module setup.
- All non-volatile storage of keys and CSPs is in the system area of the drive media to which there is no logical or physical access from outside of the module.
- The module uses SP 800-90 DRBG and adopts Hash_DRBG mechanism.
- Non-critical security parameters are not represented in this table.
- Read access of private values are internal only to the CM and are thus not represented in this table.
- There is no security-relevant audit feature.
<table>
<thead>
<tr>
<th>Name</th>
<th>Mode (ATA / TCG / Both)</th>
<th>Description</th>
<th>Type (Pub / Priv key / CSP (e.g. PIN), size)</th>
<th>Operator Role</th>
<th>Services Used In</th>
<th>Access <strong>(W, X)</strong></th>
<th>Initial Value</th>
<th>Storage</th>
<th>Storage Form (Plaintext / Encrypted)</th>
<th>Entry / Output</th>
<th>Zerorization</th>
</tr>
</thead>
<tbody>
<tr>
<td>SID (Secure ID), aka Drive Owner PIN</td>
<td>Both</td>
<td>Auth. Data</td>
<td>Private, PIN, 32 bytes</td>
<td>Drive Owner</td>
<td>Set PIN</td>
<td>W</td>
<td>MSID</td>
<td>Media (System Area)</td>
<td>SHA Digest</td>
<td>Electronic Input from Host</td>
<td>RevertSP</td>
</tr>
<tr>
<td>Master, User Passwords</td>
<td>ATA</td>
<td>Auth. Data</td>
<td>Private, PIN, 32 bytes</td>
<td>None (subject to unlocked)</td>
<td>Set PIN</td>
<td>W</td>
<td>MSID (Master), Undefined (User)</td>
<td>Media (System Area)</td>
<td>SHA Digest</td>
<td>Electronic Input from Host</td>
<td>RevertSP, Cryptographic Erase (User)</td>
</tr>
<tr>
<td>Master, User MEK</td>
<td>ATA</td>
<td>MEK mixed with PINs</td>
<td>Private, AES Key, 256 bits</td>
<td>Master, User</td>
<td>Unlock User Data</td>
<td>X</td>
<td>RNG generated during manufacturing</td>
<td>Media (System Area)</td>
<td>Obfuscated (considered plaintext for FIPS purposes)</td>
<td>None</td>
<td>RevertSP, Cryptographic Erase, Sanitize</td>
</tr>
<tr>
<td>EraseMaster</td>
<td>TCG</td>
<td>EraseMaster Auth Data</td>
<td>Private, PIN, 32 bytes</td>
<td>EraseMaster</td>
<td>Set PIN</td>
<td>W</td>
<td>MSID</td>
<td>Media (System Area)</td>
<td>SHA Digest</td>
<td>Electronic Input from Host</td>
<td>RevertSP</td>
</tr>
<tr>
<td>BandMasters (0-15) Passw</td>
<td>TCG</td>
<td>Users Auth. Data</td>
<td>Private, PIN, 32 bytes</td>
<td>BandMasters</td>
<td>Set PIN</td>
<td>W</td>
<td>MSID</td>
<td>Media (System Area)</td>
<td>SHA Digest</td>
<td>Electronic Input from Host</td>
<td>RevertSP</td>
</tr>
<tr>
<td>LBA Range MEKs</td>
<td>TCG</td>
<td>MEK (per LBA band)</td>
<td>Private, AES Key, 256 bits</td>
<td>Users</td>
<td>Unlock User Data</td>
<td>X</td>
<td>RNG generated during manufacturing</td>
<td>Media (System Area)</td>
<td>Obfuscated (considered plaintext for FIPS purposes)</td>
<td>None</td>
<td>RevertSP, Cryptographic Erase</td>
</tr>
<tr>
<td>Entropy Input String</td>
<td>Both</td>
<td>*Input to a DRBG mechanism of a string of bits that contains entropy</td>
<td>Private, 520 bytes</td>
<td>None</td>
<td>Services which use the RNG (e.g. cryptographic erase, sanitize)</td>
<td>X</td>
<td>Entropy value collected at power up or reseeding</td>
<td>RAM</td>
<td>None</td>
<td>None</td>
<td>Reset</td>
</tr>
<tr>
<td>Seed</td>
<td>Both</td>
<td>*String of bits that is used as input to a DRBG mechanism</td>
<td>Private, Hash seed, 544 bytes</td>
<td>None</td>
<td>Services which use the RNG (e.g. cryptographic erase, sanitize)</td>
<td>X</td>
<td>Undefined, depends on DRBG instantiation inputs</td>
<td>RAM</td>
<td>None</td>
<td>None</td>
<td>Reset</td>
</tr>
<tr>
<td>Internal State</td>
<td>Both</td>
<td>*Collection of stored information about DRBG instantiation</td>
<td>Private, V and C</td>
<td>None</td>
<td>Services which uses the RNG (e.g. cryptographic erase, sanitize)</td>
<td>X</td>
<td>Undefined, updated at each DRBG usage</td>
<td>RAM</td>
<td>None</td>
<td>None</td>
<td>Reset</td>
</tr>
<tr>
<td>ORG0-0 - ORG0-3</td>
<td>Both</td>
<td>Firmware Load Test Signature Verify Key</td>
<td>Public, RSA Key, 2048 bits</td>
<td>Drive Owner (enable FW download)</td>
<td>FW Download</td>
<td>X</td>
<td>Public keys generated during manufacturing</td>
<td>Media (System Area)</td>
<td>Plaintext</td>
<td>None</td>
<td>None (Public)</td>
</tr>
</tbody>
</table>

* Source: Section 4 Terms and Definitions of NIST Special Publication 800-90
** W - Write access is allowed, X - Execute access is allowed
4.3 Non-Critical Security Parameters

This section lists the security-related information which do not compromise the security of the module.

- AES IV (i.e. Initialization Vector)
  The CM HW AES IV (CBC mode) is derived for each read/write operation.

- PIN Retry Attributes – Tries, TryLimit and Persistence
  These parameters affect the handling of failed authentication attempts.

- PSID (Physical SID)
  This public drive-unique value is only used for the TCG RevertSP Admin SP method (i.e. Exit FIPS Mode service). This method will leave the CM in a non FIPS compliant “factory default” mode and will require a re-initialization for the CM to resume operation in a FIPS compliant mode.

- MSID (Manufactured SID)
  This drive-unique value is the manufactured default value for Drive Owner and Master roles.

5 Physical Security

5.1 Mechanisms

The CM has the following physical security:

- Production-grade components with standard passivation
- One Opaque, tamper-evident, security label (TEL) on the exposed (back) side of the PCBA applied by Seagate manufacturing prevents electronic design visibility and protects physical access to the electronics by board removal
- On each side of the top cover a tamper-evident security label is applied by Seagate manufacturing prevent HDA cover removal for access or visibility to the media
- Exterior of the drive is opaque
- The tamper-evident labels cannot be penetrated or removed and reapplied without tamper-evidence
- The tamper-evident labels cannot be easily replicated with a low attack time
  - Security label on PCBA of drive to provide tamper-evidence of PCBA removal
5.2 Operator Requirements

The operator is required to inspect the CM periodically for one or more of the following tamper evidence:

- Checkerboard pattern on security label or substrate
- Security label over screws at indicated locations is missing or penetrated,
- Text (including size, font, orientation) on security label does not match original,
- Security label cutouts do not match original.
- Upon discovery of tamper evidence, the module should be removed from service.
6 Operational Environment

The FIPS 140-2 Area 6 Operational Environment requirements are not applicable because the CM operates in a “non-modifiable operational environment”. That is, while the module is in operation the operational environment cannot be modified and no code can be added or deleted. FW can be upgraded (replaced) with a signed FW download operation. If the code download is successfully authenticated then the module will begin operating with the new code image.

7 Security Rules

7.1 Secure Initialization

The following are the security rules for initialization and operation of the CM in a FIPS 140 compliant manner. Reference the appropriate sections of this document for details.

1. Users: At installation and periodically examine the physical security mechanisms for tamper evidence.
2. For ATA CM, transition to a FIPS approved mode is by doing one of the following:
   - ATA Enhanced Security Mode: User Set PIN.
   - TCG Security Mode: authenticates to the Locking SP as BandMaster 0 or BandMaster 1
3. COs and Users: At installation, set all operator PINs applicable for the FIPS mode to private values of at least 4 bytes length:
   - TCG Security: Drive Owner, EraseMaster and BandMasters
4. Drive Owner: At installation, disable the “Makers” authority1
5. At installation, the value of LockOnReset1 for FW Download must be set to “Power Cycle” and it must not be modified.
6. At installation, the value of PortLocked1 for FW Download must be set to “TRUE”.

7.2 Ongoing Policy Restrictions

1. Prior to assuming a new role, close the current Session and start a new Session, or do a power cycle, so that the previous authentication is cleared.
2. Users for TCG Security Mode: User Data Read/Writes shall be an authenticated service2. Therefore, set ReadLockEnabled1 and WriteLockEnabled1 to “TRUE” (the default value is “FALSE”). If a band is configured with a value of “FALSE” then the band is to be considered excluded from the module boundary.

8 Mitigation of Other Attacks Policy

The CM does not make claims to mitigate against other attacks beyond the scope of FIPS 140-2.

1 Refer to Section 1.3, Item 5.
2 Refer to Section 4.1, Table 1.1.