Seagate Secure® TCG Enterprise SSC Self-Encrypting Drives
FIPS 140 Module

Security Policy

Security Level 2

Rev. 1.5 – December 23, 2015

Seagate Technology, LLC
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<td>24</td>
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</table>
1 Introduction

1.1 Scope
This security policy applies to the FIPS 140-2 Cryptographic Module (CM) embedded in Seagate Secure® TCG Enterprise SSC Self-Encrypting Drives.

This document meets the requirements of the FIPS 140-2 standard (Appendix C) and Implementation Guidance (section 14.1). It does not provide interface details needed to develop a compliant application.

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1.2 Security Levels

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<td>2</td>
</tr>
<tr>
<td>Finite State Model</td>
<td>2</td>
</tr>
<tr>
<td>Physical Security</td>
<td>2</td>
</tr>
<tr>
<td>Operational Environment</td>
<td>N/A</td>
</tr>
<tr>
<td>Cryptographic Key Management</td>
<td>2</td>
</tr>
<tr>
<td>Electromagnetic Interface / Electromagnetic Compatibility (EMI / EMC)</td>
<td>3</td>
</tr>
<tr>
<td>Self – tests</td>
<td>2</td>
</tr>
<tr>
<td>Design Assurance</td>
<td>2</td>
</tr>
<tr>
<td>Mitigation of Other Attacks</td>
<td>N/A</td>
</tr>
</tbody>
</table>

The overall security level pursued for the cryptographic modules is Security Level 2.

1.3 References
1. FIPS PUB 140-2
2. Derived Test Requirements for FIPS PUB 140-2
3. Implementation Guidance for FIPS PUB 140-2 and the Cryptographic Module Validation Program
5. TCG Storage Architecture Core Specification, Specification Version 1.0, Revision 0.9, May 24, 2007
6. TCG Storage Interface Interactions Specification, Specification Version 1.0
7. ATA-8 ACS
8. Serial ATA Rev 2.6 (SATA )
9. SCSI Primary Commands-4 Rev 15 (SPC-4)
10. SCSI Block Commands Rev15 (SBC-3)
11. Serial Attached SCSI-2 Rev 13 (SAS-2)

1.4 Acronyms
AES Advanced Encryption Standard (FIPS 197)
ASIC Application Specific Integrated Circuit
CBC Cipher Block Chaining, an operational mode of AES
CM Cryptographic Module
CO Crypto-officer
CSP Critical Security Parameter
DRBG Deterministic Random Bit Generator
GCM Galois/Counter Mode (SP800-38D)
HDA Head and Disk Assembly
HDD Hard Disk Drive
HMAC Hash-based message authentication code
<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>IV</td>
<td>Initialization Vector for encryption operation</td>
</tr>
<tr>
<td>LBA</td>
<td>Logical Block Address</td>
</tr>
<tr>
<td>KAT</td>
<td>Known Answer Test</td>
</tr>
<tr>
<td>MEK</td>
<td>Media Encryption Key</td>
</tr>
<tr>
<td>MSID</td>
<td>Manufactured SID, public drive-unique value that is used as default PIN, TCG term</td>
</tr>
<tr>
<td>POR</td>
<td>Power-on Reset (power cycle)</td>
</tr>
<tr>
<td>PN</td>
<td>Part Number(s)</td>
</tr>
<tr>
<td>POST</td>
<td>Power on Self-Test</td>
</tr>
<tr>
<td>PSID</td>
<td>Physical SID, public drive-unique value</td>
</tr>
<tr>
<td>RNG</td>
<td>Random Number Generator</td>
</tr>
<tr>
<td>SHA</td>
<td>Secure Hash Algorithm</td>
</tr>
<tr>
<td>SID</td>
<td>Security ID, PIN for Drive Owner CO role, TCG term</td>
</tr>
<tr>
<td>SoC</td>
<td>System-on-a-Chip</td>
</tr>
<tr>
<td>SP</td>
<td>Security Provider or Security Partition (TCG), also Security Policy (FIPS 140)</td>
</tr>
</tbody>
</table>
2 Cryptographic Module Description

2.1 Overview

The Seagate Secure® TCG Enterprise SSC Self-Encrypting Drives FIPS 140 Module is embodied in Seagate Enterprise Capacity® HDD v4, Enterprise Performance® HDD v5, Enterprise Performance® HDD v8, Enterprise Capacity® HDD v3 Self-Encrypting Drives model disk drives. These products meet the performance requirements of the most demanding Enterprise applications. The cryptographic module (CM) provides a wide range of cryptographic services using FIPS approved algorithms. Services include hardware-based data encryption, instantaneous user data disposal with cryptographic erase, independently controlled and protected user data LBA bands and authenticated FW download. The services are provided through industry-standard TCG Enterprise SSC, SCSI and ATA protocols.

The CM has a multiple-chip embedded physical embodiment. The physical interface to the CM is a SATA or SAS connector. The logical interfaces are the industry-standard ATA (refer to Section 1.3, item 7), SCSI (refer to Section 1.3, items 9 & 10), TCG SWG (refer to Section 1.3, item 4), and Enterprise (refer to Section 1.3, item 4) protocols, carried on the SATA (refer to Section 1.3, item 8) or SAS (refer to Section 1.3, item 11) transport interface. The primary function of the module is to provide data encryption, access control and cryptographic erase of the data stored on the hard drive media. The human operator of the drive product interfaces with the CM through a “host” application on a host system. The cryptographic boundary of the CM surrounds the entire drive unit, including all of its hardware, firmware, and electronics.

2.2 Logical to Physical Port Mapping

For HW versions that support ATA protocol (defined in Section 2.3):

<table>
<thead>
<tr>
<th>FIPS 140-2 Interface</th>
<th>Module Ports</th>
</tr>
</thead>
<tbody>
<tr>
<td>Data Input</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Data Output</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Control Input</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Status Output</td>
<td>SATA Connector</td>
</tr>
<tr>
<td>Power Input</td>
<td>Power Connector</td>
</tr>
</tbody>
</table>

For HW versions that support SCSI protocol (defined in Section 2.3):

<table>
<thead>
<tr>
<th>FIPS 140-2 Interface</th>
<th>Module Ports</th>
</tr>
</thead>
<tbody>
<tr>
<td>Data Input</td>
<td>SAS Connector</td>
</tr>
<tr>
<td>Data Output</td>
<td>SAS Connector</td>
</tr>
<tr>
<td>Control Input</td>
<td>SAS Connector</td>
</tr>
<tr>
<td>Status Output</td>
<td>SAS Connector</td>
</tr>
<tr>
<td>Power Input</td>
<td>Power Connector</td>
</tr>
</tbody>
</table>

2.3 Product Versions

The following models are validated with the following FW versions:

**Enterprise Capacity® HDD v4, 3.5-Inch, 7K-RPM, SAS Interface, 6000/4000/2000 GB**

- 6000 GB: ST6000NM0114 [1,2,3,4,5,6,7,8]
- 4000 GB: ST4000NM0114 [1,2,3,4,5,6,7,8]
- 2000 GB: ST2000NM0114 [1,2,3,4,5,6,7,8]

FW Versions: KF01[1], MT13[2], MF14[3], MF15[4], ETB1[5], MF17[6], KF05[7], MF18[8]

- 6000 GB: ST6000NM0104 [9, 10,11,12,13,14,15,16,17,18,19,20,21,22,23 ]
- 4000 GB: ST4000NM0104 [9, 10,11,12,13,14,15,16,17,18,19,20,21,22,23]
- 2000 GB: ST2000NM0104 [9, 10,11,12,13,14,15,16,17,18,19,20,21,22,23]

FW Versions: EF01[9], MEE4[10], HP00[11], MEE5[12], MEE6[13], MEE8[14], NE01[15], MSE1[16], MEE9[17], NE02[18], 3P00[19], 3P01[20], 3P02[21], EF05[22], NA00[23]
Enterprise Capacity® HDD v4, 3.5-Inch, 7K-RPM, SATA Interface, 6000/4000/2000 GB

6000 GB: ST6000NM0094 [24]
4000 GB: ST4000NM0094 [24]

FW Versions: NF05[24]

6000 GB: ST6000NM0084 [25,26]
4000 GB: ST4000NM0084 [25,26]
2000 GB: ST2000NM0084 [25,26]

FW Versions: ZZZZ[25], SF05[26]

Enterprise Capacity® HDD v5, 3.5-Inch, 7K-RPM, SAS Interface, 8000 GB

8000 GB: ST8000NM0125 [27,28]

FW Versions: KFF1[27], PF11[28]

8000 GB: ST8000NM0135 [29,30]

FW Versions: EFF1[29], PSE1[30]

Enterprise Performance® HDD v5, 2.5-Inch, 15K-RPM, SAS Interface, 600/450/300 GB

600 GB: ST600MP0025 [31,32,33,34,35,36,37,38]
450 GB: ST450MP0025 [31,32,33,34,35,36,37,38]
300 GB: ST300MP0025 [31,32,33,34,35,36,37,38]

FW Versions: NF03[31], VSC4[32], VEC3[33], VEC4[34], VEC5[35], VSC5[36], VEC7[37], VEC8[38]

600 GB: ST600MP0085 [39]
450 GB: ST450MP0085 [39]
300 GB: ST300MP0085 [39]

FW Versions: KF03[39]

600 GB: ST600MP0055 [40]
450 GB: ST450MP0055 [40]
300 GB: ST300MP0055 [40]

FW Versions: EF03[40]

600 GB: ST600MX0102 [41,42,43]

FW Versions: KF03[41], VT13[42], VT14[43]

600 GB: ST600MX0072 [44]

FW Versions: EF03[44]

Enterprise Performance® HDD v8, 2.5-Inch, 10K-RPM, SAS Interface, 1800/1200/900/600 GB

1800 GB: ST1800MM0048 [45,46,47,48,49]
1200 GB: ST1200MM0048 [45,46,47,48,49]
900 GB: ST900MM0048 [45,46,47,48,49]
600 GB: ST600MM0048 [45,46,47,48,49]

FW Versions: KF02[45], TF12[46], TF13[47], TF16[48], 4201[49]

1800 GB: ST1800MM0078 [50,51,52,53,54,55,56]
1200 GB: ST1200MM0078 [50,51,52,53,54,55,56]
900 GB: ST900MM0078 [50,51,52,53,54,55,56]
600 GB: ST600MM0078 [50,51,52,53,54,55,56]

FW Versions: EF02[50], TEE3[51], TEE4[52], TEE5[53], TEE8[54], TSE1[55], TEE9[56]
1200 GB: ST1200MM00108 [57,58,59,60,61,62,63,64,65]
600 GB: ST600MM00108 [57,58,59,60,61,62,63,64,65]
FW Versions: NF02[57], TSC4[58], TEC3[59], TEC4[60], TEC5[61], NF03[62], TSC5[63], TEC7[64], TEC8[65]

1800 GB: ST1800MM0118 [66,67]
1200 GB: ST1200MM0118 [66,67]
900 GB: ST900MM0118 [66,67]
600 GB: ST600MM0118 [66,67]
FW Versions: KF02[66], TT13[67]

1800 GB: ST1800MM0158 [68]
1200 GB: ST1200MM0158 [68]
900 GB: ST900MM0158 [68]
600 GB: ST600MM0158 [68]
FW Versions: EF02[68]

**Enterprise Capacity® HDD v3, 2.5-Inch, 7K-RPM, SAS Interface, 2000 GB**
2000 GB: ST2000NX0333 [69,70,71,72,73]
FW Versions: KF02[69], NT17[70], NF13[71], NF14[72], KF03[73]

2000 GB: ST2000NX0353 [74,75,76,77,78]
FW Versions: EF02[74], NEE3[75], NEE4[76], NEE5[77], EF03[78]

HW versions that support SCSI protocol are:
- Enterprise Capacity® HDD v4, 3.5 inch, 7K-RPM, SAS Interface, 6000/4000/2000 GB
- Enterprise Capacity® HDD v5, 3.5 inch, 7K-RPM, SAS Interface, 8000 GB
- Enterprise Performance® HDD v5, 2.5-Inch, 15K-RPM, SAS Interface, 600/450/300 GB
- Enterprise Performance® HDD v8, 2.5-Inch, 10K-RPM, SAS Interface, 1800/1200/900/600 GB
- Enterprise Capacity® HDD v3, 2.5-Inch, 7K-RPM, SAS Interface, 2000 GB

HW version that supports ATA protocol is:
- Enterprise Capacity® HDD v4, 3.5 inch, 7K-RPM, SATA Interface, 6000/4000/2000 GB

HW version that support 32 bands support is:
- Enterprise Capacity® HDD v4, 3.5 inch, 7K-RPM, SAS Interface, 6000/4000/2000 GB
- Enterprise Capacity® HDD v5, 3.5 inch, 7K-RPM, SAS Interface, 8000 GB

The photographs on the title page consist of representative HW versions of both models mentioned in this section.

### 2.4 FIPS Approved Algorithms

<table>
<thead>
<tr>
<th>Algorithm</th>
<th>Certificate Number</th>
</tr>
</thead>
<tbody>
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<td>ASIC AES</td>
<td>#2842, #2803</td>
</tr>
<tr>
<td>ASIC SHA-256</td>
<td>#2383, #2352</td>
</tr>
<tr>
<td>Firmware AES</td>
<td>#1343</td>
</tr>
<tr>
<td>Firmware AES-GCM</td>
<td>#2841</td>
</tr>
<tr>
<td>RSA</td>
<td>#1021</td>
</tr>
<tr>
<td>SHA</td>
<td>#1225</td>
</tr>
<tr>
<td>Firmware HMAC</td>
<td>#1597</td>
</tr>
<tr>
<td>800-90 DRBG</td>
<td>#62</td>
</tr>
<tr>
<td>800-132 PBKDF</td>
<td>Vendor Affirmed</td>
</tr>
</tbody>
</table>
ASIC AES Certificate #2842 is applicable for HW version that supports SCSI protocol (defined in Section 2.3).

ASIC AES Certificate #2803 is applicable for HW version that supports ATA protocol (defined in Section 2.3).

ASIC SHA-256 Certificate #2383 is applicable for HW versions that support SCSI protocol (defined in Section 2.3).

ASIC SHA-256 Certificate #2352 is applicable for HW versions that support ATA protocol (defined in Section 2.3).

For cryptographic algorithms such as AES, SHA and RSA, the CM only uses AES-256, SHA-256 and RSA-2048.

The remaining certificates are applicable for all HW versions of this Security Policy.

### 2.5 Self-Tests

<table>
<thead>
<tr>
<th>Function Tested</th>
<th>Self-Test Type</th>
<th>Implementation</th>
<th>Failure Behavior</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASIC AES</td>
<td>Power-On</td>
<td>Encrypt and Decrypt KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>ASIC SHA-256</td>
<td>Power-On</td>
<td>Digest KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>Firmware AES</td>
<td>Power-On</td>
<td>Encrypt and Decrypt KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>Firmware GCM</td>
<td>Power-On</td>
<td>Encrypt and Decrypt KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>RSA</td>
<td>Power-On</td>
<td>Sign Verify KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>Firmware HMAC</td>
<td>Power-On</td>
<td>Digest KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>800-90 DRBG</td>
<td>Power-On</td>
<td>DRBG KAT performed</td>
<td>Enters FIPS Self Test Error State and rejects host commands with error code.</td>
</tr>
<tr>
<td>Firmware Integrity Check</td>
<td>Power-On</td>
<td>Signature Verification</td>
<td>Enters FW Integrity Error State and does not become operationally ready.</td>
</tr>
<tr>
<td>Firmware Load Check</td>
<td>Conditional:</td>
<td>RSA PKCS#1 signature</td>
<td>Firmware download is aborted.</td>
</tr>
</tbody>
</table>
2.6 FIPS 140 Approved Modes of Operation

Before the operator performs Secure Initialization steps detailed in Section 7.1, the drive will operate in a non-FIPS Approved mode (uninitialized state).

For CM that support SCSI protocol on the SAS interface, the operator can only initialize the CM as “TCG Security” mode. For CM that support ATA protocol on the SATA interface, the operator may choose to initialize the CM to operate in either “TCG Security” or “ATA Enhanced Security” mode. After setting up (configuring) the module per the Security Rules of this policy, the CM is always in Approved mode of operation except when a critical failure has been detected, when any ‘Exit FIPS mode’ services are invoked, or when the module is not in ‘Use’ state. For CM that supports both Approved modes, an operator can switch the CM between these Approved modes of operation and to do so, the CM must transition to the uninitialized state (via ‘Exit FIPS mode’ service) which results in zeroization of keys and CSPs.

The module’s FIPS modes of operation are enforced through configuration and policy. Violating the Security rules and ongoing policy restrictions (detailed in Section 7.1 and Section 7.2) would mean that one is no longer using the drive in a FIPS Approved mode of operation.

Sections 2.6.1 and 2.6.2 describe the differences between the 2 modes.

2.6.1 TCG Security Mode

This mode has the capability to have multiple Users with independent access control to read/write/crypto erase independent data areas (LBA ranges). Note that by default there is a single “Global Range” that encompasses the whole user data area.

In addition to the Drive Owner and User(s) roles, this mode implements a CO role (EraseMaster) to administer the above capability.

2.6.2 ATA Enhanced Security Mode

This mode implements the Master and User roles, and lock/unlock/erase as defined in the ATA Security feature set as well as Sanitize feature set in ATA protocol. There is a single user data region which can be read/written/crypto-erased with one encryption key.

2.7 User Data Cryptographic Erase/Sanitize Methods

Since all user data is encrypted / decrypted by the CM for storage / retrieval on the drive media, the data can be erased/sanitized using cryptographic methods. The data is effectively erased/sanitized by changing the media encryption key (MEK). Thus, the FIPS 140 key management capability “zeroization” of the key effectively erases all the user data in that read operations will decrypt with a different key value and thus the data is not returned as it was written.

Other FIPS service can be used to erase all the other private keys and CSPs (see Section 2.8).
2.8 RevertSP Method

The TCG RevertSP method may be invoked to transition the CM back to the manufactured state (uninitialized). This corresponds to the Exit FIPS Mode service and is akin to a “restore to factory defaults” operation. This operation also provides a means to zeroize keys and CSPs. Subsequently, the CM has to be re-initialized before it can return to a FIPS Approved mode of operation. This RevertSP method is invoked as an unauthenticated service by virtue of the use of a public credential (PSID).

2.9 Show Status

Show status service (refer to Section 4.1) can be used to determine if the drive is operational under the security constraints of FIPS. For this purpose TCG Level 0 Discovery mechanism and TCG Get method are utilized.

TCG Level 0 Discovery mechanism maybe invoked by the operator to know if drive in “use” or security “fail” state. If the Drive Security Life Cycle State is 0x80 then drive is in Use State i.e. security is operational. If the Drive Security Life Cycle State is 0xFF the drive is in security Fail State i.e. drive is not operational in terms of FIPS services.

In addition, the TCG Get method can be used to retrieve the approved modes of operation value. The values of 0x01 or 0x02 correspond to ATA Enhanced Security Mode and TCG Security Mode respectively. The value 0x00 indicates the CM is in the uninitialized state.
3 Identification and Authentication (I&A) Policy

3.1 Operator Roles

Note: The following identifies the CO and User roles with a general description of the purposes. For further details of the services performed by each role in each FIPS mode, see section 4.1.

3.1.1 Crypto Officer Roles

3.1.1.1 Drive Owner
This CO role corresponds to the SID (Secure ID) Authority on the Admin SP as defined in Enterprise SSC [refer to Section 1.3, item 4]. This role is used to transition the CM to TCG Security Mode (applicable for SATA command interface) and to download a new FW image. Note: only a FIPS validated firmware version can be loaded to the module. Otherwise, the module is not operating in FIPS mode.

3.1.1.2 EraseMaster (TCG Security Mode)
This CO role corresponds to same named role as defined in Enterprise SSC [refer to Section 1.3, item 4]. This role is used to enable/disable User roles, and erase user data region (LBA band). An operator is authenticated to this role with role-based authentication.

3.1.2 User Roles

3.1.2.1 BandMasters (0-15/0-31) (TCG Security Mode)
This user role corresponds to the same named role as defined in Enterprise SSC [refer to Section 1.3, item 4]. This role is used to lock/unlock and configure a user data band (“LBA band”) for read/write access.

A CM can be configured to support up to 32 user data bands, which are controlled by their respective BandMaster credentials. By default 2 user bands are enabled. BandMasters are enabled/disabled using the EraseMaster role. An operator is authenticated to the BandMaster role with identity-based authentication. If a user data band is erased (EraseMaster service) then the BandMaster PIN is reset to MSID.

3.1.2.2 User (ATA Enhanced Security Mode)
This role corresponds to the same named role as defined in ATA [refer to Section 1.3, item 7]. It can unlock (and also lock) the drive so that an operator can read and write data to the drive. This role can also use the Cryptographic Erase service.

3.1.2.3 Master (ATA Enhanced Security Mode)
This role corresponds to the same named role as defined in ATA [refer to Section 1.3, item 7]. This role only provides a backup authentication to the ATA User and does not have access to administration services beyond those of the ATA User role.

3.1.3 Unauthenticated Role
This role can perform the Show Status service. If the operator has physical access to the drive, this role can also reset the module with a power cycle (which results in POSTs). This role can also use the public PSID value to invoke the Exit FIPS Mode service. See section 4.1 for details.

3.2 Authentication

3.2.1 Authentication Types
Some operator roles have role-based authentication and others have identity-based authentication. For example, the Drive Owner role uses role-based authentication as there is only one ID and one PIN. In TCG Security Mode, the CM has up to 16 User operators. Each of these operators is assigned a unique ID to which a PIN is associated, thus this provides identity-based authentication.

For some services the authentication is performed in a separate associated service; e.g. the Read Unlock service is the authentication for subsequent User Data Read service. If the User Data Read service is attempted without prior authentication then the command will fail.
3.2.2 **Authentication in ATA Enhanced Security Mode**

In ATA Enhanced Security Mode, Master and User operator authentication is provided through a PIN provided in the ATA Security command [refer to Section 1.3, item 7]. In the event of authentication failure, the ATA command will abort, and subsequent read/write services will abort. A password attempt counter is implemented as specified in ATA, which when reached, blocks Master/User service authentication (with command abort), until the module is reset (Unblock PIN service).

Depending on a parameter of the Set PIN service for the User password, the User services may or may not be fully extended to the Master role. If the Master Password Capability is set to “High”, then either role can access the same services. Otherwise the Master role only has access to the erase service.

Drive Owner authentication for the Set PIN and Enable/Disable FW Download services is provided through the TCG Authenticate to Admin SP.

3.2.3 **Authentication in TCG Security Mode**

Operator authentication is provided within a TCG session. The host application can have only a single session open at a time. Authentication of an operator, using the TCG interface, uses the Authenticate method to authenticate to a role after a session has been started. Authentications will persist until the session is closed.

During a session the application can invoke services for which the authenticated operator has access control. Note that a security rule of the CM is that the host must not authenticate to more than one operator (TCG authority) in a session.

For the Show Status the host application will authenticate to the “Anybody” authority which does not have a private credential. Therefore this operation is effectively an unauthenticated service.

3.2.4 **Authentication Mechanism, Data and Strength**

Operator authentication with PINs is implemented by hashing the operator input value and comparing it to the stored hash of the assigned PIN. The PINs have a retry attribute (“TryLimit”) that controls the number of unsuccessful attempts before the authentication is blocked until a module reset. The PINs have a maximum length of 32 bytes.

Per the policy security rules, the minimum PIN length is 4 bytes (Rule 3 in Section 7.1). This gives a probability of 1/2^32 of guessing the PIN in a single random attempt. This easily meets the FIPS 140 authentication strength requirements of less than 1/1,000,000.

In TCG interface, each failed authentication attempt takes a minimum of 15ms to complete. Thus a maximum of \(\frac{60\times1000}{15}\) attempts can be processed in one minute. Thus the probability of multiple random attempts to succeed in one minute is 4000/2^32. This is significantly lower than the FIPS requirement of 1/100,000.

In ATA security interface, the PIN blocking feature limits the number of unsuccessful attempts to 5 (it “unblocks” with module reset) and the minimum time for a module reset is about 15 seconds (about 4/min). Thus the probability of multiple random attempts to succeed is 4/2^32. This is significantly lower than the FIPS requirement of 1/100,000.

3.2.5 **Personalizing Authentication Data**

The initial value for SID and various other PINs is a manufactured value (mSID). This is a device-unique, 32-byte, public value. The Security Rules (Section 7) for the CM requires that the PIN values must be “personalized” to private values using the “Set PIN” service. Note that for ATA Enhanced Security Mode, setting the User PIN also sets the Drive Owner PIN to the same value; the Drive Owner PIN can be set to a different value with the TCG Set Method.
4 Access Control Policy

4.1 Services

The following tables represent the FIPS 140 services for each FIPS Approved Mode in terms of the Approved Security Functions and operator access control.

Hardware versions that support ATA protocol (defined in Section 2.3) provide services indicated in Tables 1.1 and 1.2 (when in TCG Security Mode), Tables 2.1 and 2.2 (when in ATA Enhanced Security Mode).

Hardware versions that support SCSI protocol (defined in Section 2.3) provide services in Tables 1.1 and 1.2 (when in TCG Security Mode).

For cryptographic algorithm certificates and hardware version association, refer to Section 2.4.

Note the following:
- Use of the services described below is only compliant if the module is in the noted Approved mode.
- Underlying security functions used by higher level algorithms are not represented (e.g. hashing as part of asymmetric key).
- Operator authentication is not represented in this table.
- Some security functions listed are used solely to protect / encrypt keys and CSPs.
- Service input and output details are defined by the TCG, SCSI and ATA standards.
- Unauthenticated services (e.g. Show Status) do not provide access to private keys or CSPs.
- Some services have indirect access control provided through enable / disable or lock / unlock services used by an authenticated operator; e.g. User data read / write.
- If the Operator value contains “optional” then the access is dependent on the module setup (see Section 3.2.2).
### Table 1.1 - FIPS 140 Authenticated Services (TCG Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Set PIN</td>
<td>Change operator authentication data.</td>
<td>EraseMaster</td>
<td>Hashing</td>
<td>TCG Set Method</td>
</tr>
<tr>
<td>Firmware Download</td>
<td>Enable/Disable FW download and load complete firmware image. If the self-test of the code load passes then the device will run with the new code.</td>
<td>Drive Owner**</td>
<td>Asymmetric Key</td>
<td>TCG Set Method, SCSI Write Buffer, ATA DOWNLOAD MICROCODE</td>
</tr>
<tr>
<td>Enable / Disable BandMasters</td>
<td>Enable / Disable a User Authority.</td>
<td>EraseMaster</td>
<td>None</td>
<td>TCG Set Method</td>
</tr>
<tr>
<td>Set Range Attributes</td>
<td>Set the location, size, and locking attributes of the LBA range.</td>
<td>BandMasters</td>
<td>None</td>
<td>TCG Set Method, ATA SECURITY UNLOCK</td>
</tr>
<tr>
<td>Lock / Unlock User Data Range for Read and/or Write</td>
<td>Block or allow read (decrypt) / write (encrypt) of user data in a range.</td>
<td>BandMasters</td>
<td>None</td>
<td>TCG Set Method, ATA SECURITY UNLOCK</td>
</tr>
<tr>
<td>User Data Read / Write</td>
<td>Encryption / decryption of user data to/from a LBA range. Access control to this service is provided through Lock / Unlock User Data Range.</td>
<td>None*</td>
<td>Symmetric Key</td>
<td>SCSI Read, Write Commands ATA Read, Write Commands</td>
</tr>
<tr>
<td>Cryptographic Erase</td>
<td>Erase user data in an LBA range by cryptographic means: changing the encryption key. BandMaster PIN is also reset.</td>
<td>EraseMaster, DRBG, Symmetric Key</td>
<td>TCG Erase Method</td>
<td></td>
</tr>
</tbody>
</table>

### Table 1.2 - FIPS 140 Unauthenticated Services (TCG Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Show Status</td>
<td>Reports if the CM is operational in terms of FIPS services and approved mode of operation value.</td>
<td>None</td>
<td>None</td>
<td>TCG Level 0 Discovery, TCG Get Method Drive Security Life Cycle State =0x80(Use State) and, Approved mode of operation value =0x02.</td>
</tr>
<tr>
<td>Reset Module</td>
<td>Runs POSTs and zeroizes key &amp; CSP RAM.</td>
<td>None</td>
<td>None</td>
<td>POR</td>
</tr>
<tr>
<td>DRBG Generate Bytes</td>
<td>Returns an SP 800-90 DRBG Random Number of 256 bytes</td>
<td>None</td>
<td>None</td>
<td>TCG Random()</td>
</tr>
<tr>
<td>Exit FIPS Mode</td>
<td>Exit Approved Mode of Operation. Note: CM will enter non-FIPS mode.</td>
<td>None (using PSID)</td>
<td>None</td>
<td>TCG AdminSP.RevertSP()</td>
</tr>
<tr>
<td>FIPS 140 Compliance Descriptor</td>
<td>Reports FIPS 140 Revision, Overall Security Level, Hardware and Firmware revisions and Module name</td>
<td>None</td>
<td>None</td>
<td>SCSI SECURITY PROTOCOL IN – Protocol 0 ATA TRUSTED RECEIVE – Protocol 0</td>
</tr>
</tbody>
</table>
## Table 2.1 - FIPS 140 Services – Authenticated Services (ATA Enhanced Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Set PIN</td>
<td>Change operator authentication data. Note: Setting the User PIN also sets the Drive Owner PIN.</td>
<td>User (optional Master, Drive Owner)</td>
<td>Hashing</td>
<td>ATA SECURITY SET PASSWORD, TCG Set Method</td>
</tr>
<tr>
<td>Firmware Download</td>
<td>Enable / Disable FW Download and load complete firmware image. If the self-test of the code load passes then the device will run with the new code.</td>
<td>Drive Owner**</td>
<td>Asymmetric Key</td>
<td>TCG Set Method, ATA DOWNLOAD MICROCODE</td>
</tr>
<tr>
<td>Unlock User Data</td>
<td>Enable user data read/write and Set PIN services.</td>
<td>User (optional Master)</td>
<td>Symmetric Key (to unwrap MEK)</td>
<td>ATA SECURITY UNLOCK</td>
</tr>
<tr>
<td>User Data Read / Write</td>
<td>Encryption / decryption of user data.</td>
<td>None*</td>
<td>Symmetric Key</td>
<td>ATA Read / Write Commands</td>
</tr>
<tr>
<td>Cryptographic Erase</td>
<td>Erase user data through cryptographic means: by zeroing the encryption key and the User PIN. Note: FIPS mode is exited.</td>
<td>Master, User</td>
<td>DRBG</td>
<td>ATA SECURITY ERASE PREPARE + ATA SECURITY ERASE UNIT</td>
</tr>
<tr>
<td>Sanitize</td>
<td>Sanitize user data through cryptographic means: by zeroing the encryption key.</td>
<td>None*</td>
<td>DRBG</td>
<td>ATA CRYPTO SCRAMBLE</td>
</tr>
<tr>
<td>Disable Services</td>
<td>Disables ATA Security commands until POR</td>
<td>None*</td>
<td>None</td>
<td>ATA SECURITY FREEZE LOCK</td>
</tr>
<tr>
<td>Exit FIPS Mode</td>
<td>Exit Approved Mode of Operation. Note: CM will enter non-FIPS mode.</td>
<td>User (optional Master)</td>
<td>DRBG, Hashing, Symmetric Key</td>
<td>ATA SECURITY DISABLE PASSWORD, ATA SECURITY ERASE PREPARE + ATA SECURITY ERASE UNIT</td>
</tr>
</tbody>
</table>

## Table 2.2 - FIPS 140 Unauthenticated Services (ATA Enhanced Security Mode)

<table>
<thead>
<tr>
<th>Service Name</th>
<th>Description</th>
<th>Operator Access Control</th>
<th>Security Function</th>
<th>Command(s)/Event(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Unblock PIN</td>
<td>Reset Master and User password attempt counter.</td>
<td>None</td>
<td>None</td>
<td>POR</td>
</tr>
<tr>
<td>Show Status</td>
<td>Reports if the CM is operational in terms of FIPS services and approved mode of operation value.</td>
<td>None</td>
<td>None</td>
<td>TCG Level 0 Discovery, TCG Get Method Drive Security Life Cycle State=0x80(Use State) and, Approved mode of operation value=0x01.</td>
</tr>
<tr>
<td>Reset Module</td>
<td>Runs POSTs and zeroizes key &amp; CSP RAM storage.</td>
<td>None</td>
<td>None</td>
<td>POR</td>
</tr>
<tr>
<td>Exit FIPS Mode</td>
<td>Exit Approved Mode of Operation. Note: CM will enter non-FIPS mode.</td>
<td>None (using PSID)</td>
<td>None</td>
<td>TCG AdminSP.RevertSP()</td>
</tr>
<tr>
<td>FIPS 140 Compliance Descriptor</td>
<td>Reports FIPS 140 Revision, Overall Security Level, Hardware and Firmware revisions and Module name</td>
<td>None</td>
<td>None</td>
<td>ATA TRUSTED RECEIVE – Protocol 0</td>
</tr>
</tbody>
</table>

*Security has to be Unlocked

**FW Download Port has to be Unlocked
4.2 Cryptographic Keys and CSPs

The following table defines the keys / CSPs and the operators / services which use them. Note the following:

- The use of PIN CSPs for authentication is implied by the operator access control.
- The Set PIN service is represented in this table even though generally it is only used at module setup.
- All non-volatile storage of keys and CSPs is in the system area of the drive media to which there is no logical or physical access from outside of the module.
- The module uses SP 800-90 DRBG and adopts Hash_DRBG mechanism.
- Read access of private values are internal only to the CM and are thus not represented in this table.
- There is no security-relevant audit feature.
<table>
<thead>
<tr>
<th>Name</th>
<th>Mode (ATA / TCG / Both)</th>
<th>Description</th>
<th>Type (Pub / Priv, key / CSP (e.g. PIN)), size</th>
<th>Operator Role</th>
<th>Services Used In</th>
<th>Access <strong>(W, X)</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td>SID (Secure ID, aka Drive Owner PIN)</td>
<td>Both</td>
<td>Auth. Data</td>
<td>Private, PIN, 32 bytes</td>
<td>Drive Owner</td>
<td>Set PIN</td>
<td>W</td>
</tr>
<tr>
<td>Master, User Passwords</td>
<td>ATA</td>
<td>Auth. Data</td>
<td>Private, PIN, 32 bytes</td>
<td>None (subject to unlocked)</td>
<td>Set PIN</td>
<td>W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Master, User</td>
<td>Unlock User Data</td>
<td>X</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Master, User</td>
<td>Cryptographic Erase</td>
<td>X</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Master, User</td>
<td>Sanitize</td>
<td>X</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Master, User</td>
<td>Exit FIPS Mode</td>
<td>X</td>
</tr>
<tr>
<td>Master, User MEK</td>
<td>ATA</td>
<td>Media Encryption Key</td>
<td>Private, AES Key, 512 bits</td>
<td>Master, User</td>
<td>Unlock User Data</td>
<td>X</td>
</tr>
<tr>
<td>EraseMaster</td>
<td>TCG</td>
<td>EraseMaster, Auth Data</td>
<td>Private, PIN, 32 bytes</td>
<td>EraseMaster</td>
<td>Set PIN</td>
<td>W</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Cryptographic Erase</td>
<td>X</td>
</tr>
<tr>
<td>BandMasters (0-15/0-32) Passwords</td>
<td>TCG</td>
<td>Users Auth. Data</td>
<td>Private, PIN, 32 bytes</td>
<td>BandMasters</td>
<td>Unlock User Data</td>
<td>X</td>
</tr>
<tr>
<td>LBA Range MEKs</td>
<td>TCG</td>
<td>MEK (per LBA band)</td>
<td>Private, AES Key, 512 bits</td>
<td>Users</td>
<td>Unlock User Data</td>
<td>X</td>
</tr>
<tr>
<td>Entropy Input String</td>
<td>Both</td>
<td>*Input to a DRBG mechanism of a string of bits that contains entropy</td>
<td>Private, 520 bytes</td>
<td>None</td>
<td>Services which use the DRBG (e.g.: cryptographic erase, sanitize)</td>
<td>X</td>
</tr>
<tr>
<td>Seed</td>
<td>Both</td>
<td>*String of bits that is used as input to a DRBG mechanism</td>
<td>Private, Hash seed, 544 bytes</td>
<td>None</td>
<td>Services which use the DRBG (e.g.: cryptographic erase, sanitize)</td>
<td>X</td>
</tr>
<tr>
<td>Internal State</td>
<td>Both</td>
<td>*Collection of stored information about DRBG instantiation</td>
<td>Private, V and C</td>
<td>None</td>
<td>Services which uses the DRBG (e.g.: cryptographic erase, sanitize)</td>
<td>X</td>
</tr>
<tr>
<td>ORG0-0 - ORG0-1</td>
<td>Both</td>
<td>Firmware Load Test Signature Verify Key</td>
<td>Public, RSA Key, 2048 bits</td>
<td>Drive Owner (enable FW download)</td>
<td>FW Download</td>
<td>X</td>
</tr>
<tr>
<td>MEKEK (MEK Encryption Key)</td>
<td>Both</td>
<td>This key is used to protect the MEK</td>
<td>Private, AES Key, 32 bytes</td>
<td>Master, User, BandMasters, EraseMaster</td>
<td>Unlock User Data, Cryptographic Erase, Set PIN</td>
<td>W, X</td>
</tr>
<tr>
<td>Master Key</td>
<td>Both</td>
<td>This key is used to protect the MEKEK</td>
<td>Private, AES Key, 32 bytes</td>
<td>Master, User, BandMasters, EraseMaster</td>
<td>Unlock User Data, Cryptographic Erase, Set PIN</td>
<td>W, X</td>
</tr>
<tr>
<td>CSPSK</td>
<td>Both</td>
<td>Used internally within PBKDF</td>
<td>Private, AES Key, 32 bytes</td>
<td>Master, User, BandMasters, EraseMaster</td>
<td>Unlock User Data, Cryptographic Erase, Set PIN</td>
<td>W, X</td>
</tr>
<tr>
<td>HMAC Key</td>
<td>Both</td>
<td>Used internally within PBKDF</td>
<td>Private, HMAC Key, 32 bytes</td>
<td>Master, User, BandMasters, EraseMaster</td>
<td>Unlock User Data, Cryptographic Erase, Set PIN</td>
<td>W, X</td>
</tr>
</tbody>
</table>

* Source: Section 4 Terms and Definitions of NIST Special Publication 800-90
** W - Write access is allowed, X - Execute access is allowed
5 Physical Security

5.1 Mechanisms

The CM has the following physical security:

- Production-grade components with standard passivation
- One opaque, tamper-evident security label (TEL) on the exposed (back) side of the PCBA applied by Seagate manufacturing prevents electronic design visibility and protects physical access to the electronics by board removal
- Two tamper-evident security labels applied by Seagate manufacturing prevent top and bottom cover removal for access or visibility to the media
- Exterior of the drive is opaque
- The tamper-evident labels cannot be penetrated or removed and reapplied without tamper-evidence
- The tamper-evident labels cannot be easily replicated with a low attack time

  o Security label on PCBA of drive to provide tamper-evidence of PCBA removal

Figure 1: Enterprise Capacity® 3.5, HDD v4 (SAS Interface)

Figure 2: Enterprise Capacity® 3.5, HDD v4 (SATA)
Figure 3: Enterprise Performance® 2.5, HDD v5(SAS)

Figure 4: Enterprise Performance® 2.5, HDD v8(SAS)
• Security labels on side of drive to provide tamper-evidence of HDA cover removal,

Figure 5: Enterprise Capacity® 2.5, HDD v3 (SAS)

Figure 6: Enterprise Capacity® 3.5, HDD v4 (SAS/SATA interface)

Figure 7: Enterprise Performance® HDD v5, Enterprise Performance® HDD v8, Enterprise Capacity® HDD v3 (SAS Interface) security labels on sides of drive
5.2 **Operator Requirements**

The operator is required to inspect the CM periodically for one or more of the following tamper evidence:

- Checkerboard pattern on security label or substrate

![Tamper Evidence Checkerbox Pattern](image1)

**Figure 8:** Enterprise Capacity® 3.5, HDD v4 (SAS/SATA) Top Cover Tamper Evidence

![Tamper Evidence](image2)

**Figure 9:** Enterprise Capacity® 3.5, HDD v4 (SAS/SATA) PCBA Tamper Evidence
• Security label over screws at indicated locations is missing or penetrated,

Figure 10: Enterprise Capacity® 3.5, HDD v4 (SAS/SATA) Screw covered by Security label

Figure 11: Enterprise Performance® 2.5, HDD v5(SAS) Screw covered by Security Label
• Text (including size, font, orientation) on security label does not match original,
• Security label cutouts do not match original.

Upon discovery of tamper evidence, the module should be removed from service.
6 Operational Environment

The FIPS 140-2 Area 6 Operational Environment requirements are not applicable because the CM operates in a “non-modifiable operational environment”. That is, while the module is in operation the operational environment cannot be modified and no code can be added or deleted. FW can be upgraded (replaced) with a signed FW download operation. If the code download is successfully authenticated then the module will begin operating with the new code image.

7 Security Rules

7.1 Secure Initialization

The following are the security rules for initialization and operation of the CM in a FIPS 140 compliant manner. Reference the appropriate sections of this document for details.

1. Users: At installation and periodically examine the physical security mechanisms for tamper evidence.
2. CM that supports ATA protocol on the SATA interface can transition to either of the modes by doing one of the following:
   - ATA Enhanced Security Mode: User Set PIN.
   - TCG Security Mode: authenticates to the Locking SP as BandMaster 0, BandMaster 1 or EraseMaster.
   CM that supports SCSI protocol on the SAS interface can only transition to TCG Security Mode.
   - Transition to TCG Security Mode is done by authenticating to Locking SP as BandMaster 0, BandMaster 1 or EraseMaster.
3. COS and Users: At installation, set all operator PINs applicable for the FIPS mode to private values of at least 4 bytes length:
   - TCG Security: Drive Owner, EraseMaster and BandMasters
4. Drive Owner: At installation, disable the “Makers” authority.
5. At installation, the value of LockOnReset for FW Download must be set to “Power Cycle” and it must not be modified.
6. At installation, the value of PortLocked for FW Download must be set to “TRUE”.

7.2 Ongoing Policy Restrictions

1. Prior to assuming a new role, close the current Session and start a new Session, or do a powercycle, so that the previous authentication is cleared.
2. Users for TCG Security Mode: User Data Read/Writes shall be an authenticated service. Therefore, set ReadLockEnabled and WriteLockEnabled to “TRUE” (the default value is “FALSE”). If a band is configured with a value of “FALSE” then the band is to be considered excluded from the module boundary.

8 Mitigation of Other Attacks Policy

The CM does not make claims to mitigate against other attacks beyond the scope of FIPS 140-2.

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1 Refer to Section 1.3, Item 5.
2 Refer to Section 4.1, Table 1.1.