FIPS 140-2 Non-Proprietary Security Policy

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1.1 Purpose

This is a non-proprietary Cryptographic Module Security Policy for the HP P-Class Smart Array RAID Controllers (Hardware Models: P230i, P430, P431, P731m, P830, and P830i; Firmware Version: 1.66) from Hewlett-Packard Development Company, L.P. This Security Policy describes how the HP P-Class Smart Array RAID Controllers meet the security requirements of Federal Information Processing Standards (FIPS) Publication 140-2, which details the U.S. and Canadian Government requirements for cryptographic modules. More information about the FIPS 140-2 standard and validation program is available on the U.S. National Institute of Standards and Technology (NIST) and Canada’s Communications Security Establishment (CSE) Cryptographic Module Validation Program (CMVP) website at http://csrc.nist.gov/groups/STM/cmvp.

This document also describes how to run the modules in a secure FIPS-Approved mode of operation. This policy was prepared as part of the Level 2 FIPS 140-2 validation of the modules. The HP P-Class Smart Array RAID Controllers are referred to in this document as Smart Array Controllers, crypto modules, or the modules.

1.2 References

This document deals only with operations and capabilities of the module in the technical terms of a FIPS 140-2 cryptographic module security policy. More information is available on the modules from the following sources:

- The HP website (http://www.hp.com) contains information on the full line of products from HP.
- The CMVP website (http://csrc.nist.gov/groups/STM/cmvp/documents/140-1/140val-all.htm) contains contact information for individuals to answer technical or sales-related questions for the module.

1.3 Document Organization

The Security Policy document is one document in a FIPS 140-2 Submission Package. In addition to this document, the Submission Package contains:

- Vendor Evidence document
- Finite State Model document
- Other supporting documentation as additional references

This Security Policy and the other validation submission documentation were produced by Coresec Security, Inc. under contract to HP. With the exception of this Non-Proprietary Security Policy, the FIPS 140-2 Submission Package is proprietary to HP and is releasable only under appropriate non-disclosure agreements. For access to these documents, please contact HP.
2.1 Overview

The HP P-Class Smart Array RAID Controllers are a family of serial-attached SCSI\textsuperscript{1} host bus adapters that provide intelligent control for storage array. The controllers can be card-based or embedded within an HP server, and provide a high speed data path, on-board storage cache, remote management, and encryption of data at rest. Additional drives can be easily added to increase capacity. The purpose of the controller is to transform an application’s high-level ‘read’ or ‘write’ disk operations into the individual instructions required for a RAID\textsuperscript{2} array using an embedded RAID-on-Chip (ROC) processor. Disk operations are protected in transit via the Smart Array Controllers’ on-board memory cache that acts as a buffer for disk input/output operations. When a controller detects a power loss, any data in the cache is written to the flash memory for retrieval when the power returns.

Caching allows the controller to use write-back caching that informs the operating system of a completed write when data is written to the cache instead of waiting until it is written to disk. Smart Array Controllers also implement a read-ahead caching algorithm that detects sequential read activity and predicts when a sequential-read will follow. This allows the controller to anticipate data needs and reduce wait times. The read-ahead caching is disabled when a non-sequential read activity is detected to reduce any slow down for random read requests.

Controllers can be stand-up cards, mezzanine, or embedded within an HP server. Each controller contains a PCIe\textsuperscript{3} connector, multiple serial attached SCSI (SAS) ports, and a cryptographic state LED\textsuperscript{4}. Controllers can have factory- or user-installed physical security kits that include hardcovers and tamper-evident seals. The HP server provides a Smart Storage Administrator GUI and CLI that are used to manage the controllers. For a list of servers compatible with the HP P-Class Smart Array RAID Controllers, refer to the HP Smart Array Controllers Compatibility Matrix for HP Gen8 Servers datasheet.

The Smart Array Controllers provide encryption for data at rest. Each controller includes a PMC-Sierra ASIC\textsuperscript{5} that generates the keys to be used for encryption. The controllers utilize a front-end strategy to encrypt all host data. Data from the host first enters the encryption engine before moving to the cache module and then to the RAID storage. The controllers also include a key management framework for managing disk encryption keys. Each logical drive in the storage array is encrypted with its own disk encryption key. These keys are then encrypted with a second key for storage on the drive. Smart Array stores keys in encrypted form in multiple locations to provide data storage that is secure and mobile.

The HP P-Class Smart Array RAID Controllers are validated at the FIPS 140-2 Section levels shown in Table 1.

<table>
<thead>
<tr>
<th>Section</th>
<th>Section Title</th>
<th>Level</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Cryptographic Module Specification</td>
<td>2</td>
</tr>
<tr>
<td>2</td>
<td>Cryptographic Module Ports and Interfaces</td>
<td>2</td>
</tr>
<tr>
<td>3</td>
<td>Roles, Services, and Authentication</td>
<td>2</td>
</tr>
</tbody>
</table>

\textsuperscript{1} SCSI – Small Computer System Interface  
\textsuperscript{2} RAID – Redundant Array of Independent Disks  
\textsuperscript{3} PCIe – Peripheral Component Interconnect Express  
\textsuperscript{4} LED – Light Emitting Diode  
\textsuperscript{5} ASIC – Application-Specific Integrated Circuit
2.2 Module Specification

The HP P-Class Smart Array RAID Controllers are hardware modules with a multi-chip embedded embodiment. The overall security level of the modules is 2. The cryptographic boundary of the HP P-Class Smart Array RAID Controllers is defined by the hard metal and plastic physical security kit that surrounds all hardware and firmware components. The modules are primarily composed of the following components:

- PMC-Sierra 806X ROC processor
- Flash NVRAM\(^7\)
- Dual in-line memory (DIMM) Module
- Bootstrap and Crypto NVRAM
- SAS Support Logic module
- PCIe Connector
- A multistate LED

In addition, there is a Manufacturing, Local, and SAS Mfg ID NVRAM that do not process any cryptographic information.

The controllers are delivered in several form factors (mezzanine card, stand-up card, daughter card, and embedded on the main logic board in an HP Gen8 server platform) and appear in a variety of physical layouts (depending on the form factor). Each module includes the Smart Array firmware v1.66 and Express Logic’s ThreadX RTOS\(^8\) v5.5. The module firmware is stored in the Flash NVRAM until the system is initialized. After a successful integrity check the run-time firmware is unpacked and loaded onto the DIMM module for execution by the ROC. Table 2 below provides details regarding the form factor and embedded ROC for each controller model.

<table>
<thead>
<tr>
<th>Controller Model</th>
<th>Form Factor</th>
<th>Embedded ROC</th>
</tr>
</thead>
<tbody>
<tr>
<td>P230i</td>
<td>embedded</td>
<td>PMC-Sierra 8062</td>
</tr>
<tr>
<td>P430</td>
<td>stand-up card</td>
<td>PMC-Sierra 8061</td>
</tr>
<tr>
<td>P431</td>
<td>stand-up card</td>
<td>PMC-Sierra 8061</td>
</tr>
</tbody>
</table>

\(^7\)EMI/EMC – Electromagnetic Interference / Electromagnetic Compatibility
\(^8\)NVRAM – Non-Volatile Random Access Memory
\(^8\)RTOS – Real-Time Operating System
The modules implement the FIPS-Approved algorithms listed in Table 3 below.

### Table 3 – FIPS-Approved Algorithm Implementations

<table>
<thead>
<tr>
<th>Algorithm</th>
<th>Certificate Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>PM8601</td>
<td>PM8602</td>
</tr>
<tr>
<td>AES(^9) ECB(^{10}) encryption/decryption with 256-bit keys</td>
<td>#2902</td>
</tr>
<tr>
<td>XTS(^{11,12,13}) - AES encryption/decryption with XTS_256-bit keys</td>
<td>#2902</td>
</tr>
<tr>
<td>SHA(^{14})-256</td>
<td>#2442</td>
</tr>
<tr>
<td>HMAC(^{15}) with SHA-256</td>
<td>#1837</td>
</tr>
<tr>
<td>SP(^{16})800-90A CTR DRBG(^{17})</td>
<td>#529</td>
</tr>
</tbody>
</table>

The modules include the FIPS-Approved Password-Based Key Derivation Function (PBKDF2) specified in SP 800-132 option 2 as a key establishment technique. CO and User passwords shall be at least 10 characters to ensure a sufficient strength for the PBKDF2-derived keys. Keys derived from the PBKDF2 function shall only be used for storage applications.

The module also employs the following non-Approved algorithms:
- 256-bit AES Key Wrap (ECB mode)
- Non-Deterministic Random Number Generator (NDRNG) which is a free running oscillator, used to generate entropy for the CTR DRBG

### 2.3 Module Interfaces

The module’s physical ports can be categorized into the following logical interfaces defined by FIPS 140-2:
- Data Input Interface
- Data Output Interface
- Control Input Interface
- Status Output Interface

Each interface is mapped to the module in Figure 1, which shows the physical boundary and associated interfaces.

---

9 AES – Advance Encryption Service
10 ECB – Electronic Code Book
11 XTS – XEX-based tweaked-codebook mode with ciphertext stealing
12 XEX – XOR-Encrypt-XOR
13 XOR – Exclusive Or
14 SHA – Secure Hash Algorithm
15 HMAC – (keyed-) Hashed Message Authentication Code
16 SP – Special Publication
17 DRBG – Deterministic Random Bit Generator
The HP P-Class Smart Array RAID Controllers have the following physical interfaces (which map to the FIPS-required logical interfaces as shown in Table 4):

- PCIe connector
- SAS ports
  - P230i – 1 x4 port
  - P430 – 1 x8 port
  - P431 – 2 x4 ports
  - P731m – 4 x2 ports
  - P830 – 2 x8 ports
  - P830i – 2 x8 ports
- DIMM bus (in remote mode only)
- Multistate LED
- Serial port
- Power

Figure 1 – HP Smart Array Block Diagram
Table 4 – FIPS 140-2 Logical Interface Mappings

<table>
<thead>
<tr>
<th>Physical Port/Interface</th>
<th>FIPS 140-2 Interface</th>
</tr>
</thead>
<tbody>
<tr>
<td>PCIe Connector</td>
<td>Data Input</td>
</tr>
<tr>
<td></td>
<td>Data Output</td>
</tr>
<tr>
<td></td>
<td>Control Input</td>
</tr>
<tr>
<td></td>
<td>Status Output</td>
</tr>
<tr>
<td></td>
<td>Power Input</td>
</tr>
<tr>
<td>SAS port(s)</td>
<td>Data Input</td>
</tr>
<tr>
<td></td>
<td>Data Output</td>
</tr>
<tr>
<td>DIMM bus (remote mode only)</td>
<td>Data Input</td>
</tr>
<tr>
<td></td>
<td>Data Output</td>
</tr>
<tr>
<td></td>
<td>Status Output</td>
</tr>
<tr>
<td>Multistate LED</td>
<td>Status Output</td>
</tr>
<tr>
<td>Serial port</td>
<td>Status Output</td>
</tr>
</tbody>
</table>

2.4 Roles and Services

The modules support role-based authentication. There are two roles in each module (as required by FIPS 140-2) that operators may assume: a Crypto Officer (CO) role and a User role. Roles are assumed explicitly by means of a username and password. The password is sent with every command listed in the CO and User services. Please note that the keys and Critical Security Parameters (CSPs) listed in the table indicate the type of access required using the following notation:

- **R** – Read: The CSP is read.
- **W** – Write: The CSP is established, generated, modified, or zeroized.
- **X** – Execute: The CSP is used within an Approved or Allowed security function or authentication mechanism.

Operator services are listed and described in Table 5.

Table 5 – Mapping of Operator Services to Inputs, Outputs, CSPs, and Type of Access

<table>
<thead>
<tr>
<th>Service</th>
<th>Operator</th>
<th>Description</th>
<th>Input</th>
<th>Output</th>
<th>CSP and Type of Access</th>
</tr>
</thead>
<tbody>
<tr>
<td>Initialize module</td>
<td>CO x User</td>
<td>Configure the module for operation</td>
<td>Command and password</td>
<td>Command response and status output</td>
<td>CO password – X</td>
</tr>
<tr>
<td>Set/reset IKEK¹⁹</td>
<td>CO x User</td>
<td>Set or reset IKEK</td>
<td>Command and password</td>
<td>Command response and status output</td>
<td>IKEK – W CO password – X</td>
</tr>
<tr>
<td>Set/reset Local Master Key</td>
<td>CO x User</td>
<td>Set or reset Local Master Key</td>
<td>Command and password</td>
<td>Command response and status output</td>
<td>Local Master Key – W CO password – X</td>
</tr>
</tbody>
</table>

¹⁸ Note that the “Show status” and “Perform self-test” services are allocated to the Crypto Officer and User roles. However, module operators are not required to assume an authorized role to perform these services, as these services do not affect the security of the module (refer to FIPS Implementation Guidance 5.2 for details).

¹⁹ IKEK – an initial key encryption key that is referred to as the Master Key in HP documentation.
<table>
<thead>
<tr>
<th>Service</th>
<th>Operator</th>
<th>Description</th>
<th>Input</th>
<th>Output</th>
<th>CSP and Type of Access</th>
</tr>
</thead>
<tbody>
<tr>
<td>Enable encryption</td>
<td>CO</td>
<td>Turn encryption on for the controller as part of initialization</td>
<td>Command and password</td>
<td>Command response and status output</td>
<td>DEK(^{20}) – R, X KEK(^{21}) – R, X CO password – X</td>
</tr>
<tr>
<td>Set key cache policies</td>
<td>x</td>
<td>Establish policies for where cache storage exists and what keys are stored there</td>
<td>Command</td>
<td>Command response and status output</td>
<td>KEK – R CO password – X</td>
</tr>
<tr>
<td>Enable User role</td>
<td>x</td>
<td>Create User and assign a password</td>
<td>Command and password</td>
<td>Command response and status output</td>
<td>User password – W CO password – X</td>
</tr>
<tr>
<td>Key management mode</td>
<td>x</td>
<td>Select ‘Local Key Management Mode’ or ‘Remote Key Management Mode’ on GUI</td>
<td>Command and password</td>
<td>Command response and status output</td>
<td>Local Master Key – R, W, X CO password – X</td>
</tr>
<tr>
<td>Change password</td>
<td>x</td>
<td>Change operator password</td>
<td>Command</td>
<td>Command response and status output</td>
<td>CO password – W User password – W</td>
</tr>
<tr>
<td>Lock firmware</td>
<td>x</td>
<td>Lock firmware so that it cannot be flashed</td>
<td>Command</td>
<td>Command response</td>
<td>CO password – X User password – X</td>
</tr>
<tr>
<td>Allow/Disallow plaintext logical drive creation</td>
<td>x</td>
<td>Disallow ensures all new logical drive are created with encryption enabled. Allow lets the CO choose to create plaintext volumes.</td>
<td>Command</td>
<td>Command response and status output</td>
<td>CO password – X</td>
</tr>
<tr>
<td>Reset CO password</td>
<td>x</td>
<td>Allow CO to reset password by answering a preset security question</td>
<td>Command</td>
<td>Command response and status output</td>
<td>CO password – R, W</td>
</tr>
<tr>
<td>Clear Encryption</td>
<td>x</td>
<td>Zeroize all CSPs via the Clear Encryption Configuration button under utilities on the Encryption Manager GUI.</td>
<td>Command</td>
<td>Command response and status output</td>
<td>All CSPs – W</td>
</tr>
</tbody>
</table>

\(^{20}\) DEK – Data Encryption Key  
\(^{21}\) KEK – Key Encryption Key (also referred to as the Drive Encryption Key in HP documentation)
2.4.1 Authentication

The modules support role-based authentication. Module operators must input a password when requesting the services listed in Table 5. Each command is passed to the module with the associated operator password. The module verifies the password to ensure the operator is authorized to perform the requested command. Table 6 lists the strength of the authentication mechanism used by the modules.

<table>
<thead>
<tr>
<th>Authentication Type</th>
<th>Strength</th>
</tr>
</thead>
</table>
| Username/Password       | The minimum length of the password is ten characters, with 94 different case-sensitive alphanumeric characters and symbols possible for usage. The module imposes character type and case restrictions so that the password must have a number, upper case letter, lower case letter, and special character. The remaining 6 characters could be any of the 94 choices. The chance of a random attempt falsely succeeding is 1: \((10^5*26^3*32*94^6)\), or 1: 149,232,631,038,033,920; which is less than 1:1,000,000 as required by FIPS 140-2. In addition, the module imposes a restriction on the number of passwords that can be entered into the module. After ten failures, there is a 15 minute delay before another attempt can be made. So, in effect and at most, 10 passwords can be tried per 15 minutes. The probability that a random attempt will succeed or a false acceptance will occur in one minute is
\[= \frac{1}{149,232,631,038,033,920\text{ possible passwords} / 10\text{ passwords per minute}}\]
\[= \frac{1}{14.9233 \times 10^{15}}\]
which is less than 1:100,000 as required by FIPS 140-2. |

2.5 Physical Security

The HP P-Class Smart Array RAID Controllers consist of production-grade components that include standard passivation techniques. Each module is pre-installed with a FIPS physical security kit that consists of metal covers for all ports and components in the module. The covers are opaque within the visible spectrum and are designed to satisfy Level 2 physical security requirements. These covers prevent visibility from the vent holes in the front of the stand alone controllers. Tamper-evident seals are applied to the covers to provide physical evidence of attempts to remove the covers. The tamper-evident seals must be inspected periodically for tamper evidence. The placement of covers and tamper-evident seals can be found in the Secure Operations section of this document.
If any evidence of tampering is observed on the covers or tamper-evident seals, the module shall be considered to be in a non-compliant state. Upon such discovery, the CO shall immediately take the module out of operation and return to the vendor.

2.6 Operational Environment

The requirements in this section are not applicable. The modules do not provide a general-purpose operating system (OS) to module operators. Each module employs the ThreadX v5.5 OS, which provides only a limited operational environment. Only the modules’ firmware can be executed by the modules.

2.7 Cryptographic Key Management

The module supports the CSPs listed in Table 7.

Note: The module can operate in either of two different modes: local or remote. In local mode, the module generates and stores all keys. In remote mode, an external Enterprise Secure Key Manager (ESKM) is used to generate an IKEK and store KEKs and Controller keys. The server platform provides secure communications to the external ESKM. The table below describes the keys as they apply to each mode.
### Table 7 – List of Cryptographic Keys, Cryptographic Key Components, and CSPs

<table>
<thead>
<tr>
<th>CSP</th>
<th>CSP Type</th>
<th>Generation / Input</th>
<th>Output</th>
<th>Storage</th>
<th>Zeroization</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Local and Remote Mode</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DEK</td>
<td>256-bit AES-XTS key</td>
<td>Generated internally</td>
<td>Exits the module in encrypted form for storage</td>
<td>Stored in plaintext in volatile DIMM module</td>
<td>Reboot of controller</td>
<td>Encryption and decryption of logical drives</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Logical drive deleted</td>
<td></td>
</tr>
<tr>
<td>Crypto Officer password</td>
<td>10 – 16 character password</td>
<td>Entered electronically</td>
<td>Never exits the module</td>
<td>Stored in encrypted form in NVRAM</td>
<td>Return to factory reset</td>
<td>Authenticate Crypto Officer</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Stored in plaintext in volatile DIMM module</td>
<td>Reboot or return to factory reset</td>
<td></td>
</tr>
<tr>
<td>User Password</td>
<td>10 – 16 character password</td>
<td>Entered electronically</td>
<td>Never exits the module</td>
<td>Stored in encrypted form in NVRAM</td>
<td>Return to factory reset</td>
<td>Authenticate User</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Stored in plaintext in volatile DIMM module</td>
<td>Reboot or return to factory reset</td>
<td></td>
</tr>
<tr>
<td>CTR_DRBG seed</td>
<td>384-bit random value</td>
<td>Generated internally</td>
<td>Never exits the module</td>
<td>Stored temporarily in volatile DIMM module in plaintext</td>
<td>Automatically upon completion of CTR_DRBG seed operation</td>
<td>Used to seed the CTR_DRBG</td>
</tr>
<tr>
<td>CTR_DRBG entropy input</td>
<td>256-bit random value</td>
<td>Generated internally</td>
<td>Never exits the module</td>
<td>Stored temporarily in volatile DIMM module in plaintext</td>
<td>Automatically upon completion of CTR_DRBG seed operation</td>
<td>Used in the process of generating a random number</td>
</tr>
<tr>
<td><strong>Remote Mode Only</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CSP</td>
<td>CSP Type</td>
<td>Generation / Input</td>
<td>Output</td>
<td>Storage</td>
<td>Zeroization</td>
<td>Use</td>
</tr>
<tr>
<td>------------</td>
<td>---------------</td>
<td>-----------------------------------------</td>
<td>---------------------------------------------</td>
<td>-------------------------------------------------------------------------</td>
<td>------------------------------</td>
<td>----------------------------------------------------------------------</td>
</tr>
<tr>
<td>KEK</td>
<td>256-bit AES-XTS key</td>
<td>Generated internally and may be re-input electronically in encrypted form</td>
<td>May exit the module in encrypted form for storage</td>
<td>Stored in volatile DIMM module in plaintext</td>
<td>Reboot of controller or return to factory reset</td>
<td>Encryption and decryption of DEKs in remote mode only. There is one KEK per physical drive</td>
</tr>
<tr>
<td>IKEK</td>
<td>256-bit AES-ECB key</td>
<td>Imported over shared memory interface</td>
<td>Never exits the module</td>
<td>Stored in plaintext in NVRAM</td>
<td>Return to factory reset</td>
<td>Initial key used for encryption and decryption of KEKs and User and CO passwords</td>
</tr>
<tr>
<td>Controller Key</td>
<td>256-bit AES-ECB</td>
<td>Generated internally</td>
<td>Exits the module in encrypted form for storage</td>
<td>Stored in volatile DIMM module in plaintext form</td>
<td>Reboot of controller or return to factory reset.</td>
<td>Performs encryption and decryption per controller at initialization of the controller</td>
</tr>
</tbody>
</table>

**Local Mode Only**

| Local Master Key | 256-bit AES key | Derived as per SP 800-132 using PBKDF2 and HMAC-SHA-256 | Never exits the module                       | Stored in plaintext in NVRAM                                             | Return to factory reset       | Encryption and decryption of DEKs                                      |
2.8 EMI/EMC

HP P-Class Smart Array RAID Controllers were tested and found conformant to the Electromagnetic Interference (EMI)/Electromagnetic Compatibility (EMC) requirements specified by 47 Code of Federal Regulations, Part 15, Subpart B, Unintentional Radiators, Digital Devices, Class A (business use).

2.9 Self-Tests

Cryptographic self-tests are performed by the modules when the modules are first powered up and loaded into memory. Additional cryptographic self-tests are performed when a random number is created. The following sections list the self-tests performed by the modules, their expected error statuses, and error resolutions.

2.9.1 Power-Up Self-Tests

The HP P-Class Smart Array RAID Controllers perform the following self-tests at power-up:

- Firmware integrity check – a 32-bit Cyclic Redundancy Check (CRC)
- Known Answer Tests (KATs)
  - AES-ECB encrypt KAT
  - AES-ECB decrypt KAT
  - AES-XTS encrypt KAT
  - AES-XTS decrypt KAT
  - SHA-256 KAT
  - HMAC SHA-256 KAT
  - CTR DRBG KAT

If any of these self-test fail, encrypted drives are taken offline and the modules enter a critical error state. An error message of the failure is logged.

2.9.2 Conditional Self-Tests

The HP P-Class Smart Array RAID Controllers perform the following conditional self-tests:

- Continuous RNG for NDRNG
- Continuous RNG for CTR DRBG

If any of the RNG conditional self-tests fail, key generation is halted and the module enters a soft error state. The RNG is re-instantiated and the test is run a second time. If the test fails again, the module enters a critical error and all cryptographic operations are halted. An error message of each failure is logged.

2.9.3 Critical Functions Self-Tests

The following critical function tests, which are described in SP 800-90A are performed by the modules:

- DRBG Instantiate Test is performed at start-up or anytime the DRBG is instantiated.
- DRBG Generate Test is performed whenever the DRBG is requested to generate a random number.
- DRBG Reseed Test is performed whenever the DRBG is re-seeded.
- DRBG Un-instantiate test is performed whenever the DRBG is un-instantiated.

A failure of any of these tests results in a critical error for the DRBG, requiring that the module be replaced. When the DRBG is in error, no new keys can be generated.
2.10 Mitigation of Other Attacks

This section is not applicable. The modules do not claim to mitigate any attacks beyond the FIPS 140-2 Level 2 requirements for this validation.
3 Secure Operation

The HP P-Class Smart Array RAID Controllers meet Level 2 requirements for FIPS 140-2. The sections below describe how to place and keep the modules in FIPS-Approved mode of operation.

3.1 Initial Setup

The P230i and P830i controllers are pre-installed in the target server. The P430, P431, and P731m controllers must be installed in a supported server. The HP Smart Array P430 Controller User Guide, HP Smart Array P431 Controller User Guide, and HP Smart Array P731m Controller User Guide include the sets to install the controllers in a supported server.

The modules are delivered in a non-operational factory state with physical security kits installed. The CO is responsible for installation (as applicable), initialization, and security-relevant configuration and management activities for each module. The modules can be configured through the underlying server’s Smart Storage Administrator (SSA), Secure Encryption Graphical User Interface (GUI), HP SSA Scripting interface, or through the SSA Command Line Interface (CLI) utility. Once initialized, only the Secure Encryption GUI can be used to manage the module. The commands and buttons used in these interfaces translate to commands that enter the modules over the PCIe bus.

To configure the modules for their Approved mode of operation, the CO must:

1. Verify physical security mechanisms are properly installed.
2. Set the CO password, key management mode, encryption mode, and disallow plaintext volumes
3. Enable the User role
4. Verify and lock the firmware

Guidance for performing these tasks through the SSA GUI can be found in the HP Secure Encryption Installation and User Guide and in this FIPS 140-2 Security Policy.

3.1.1 Initial Setup using the Server GUI

To initialize each module using the SSA GUI, start the HP SSA utility and select the controller to be configured. Then follow the steps below to complete the initial setup.

- Ensure physical security mechanisms are properly installed

The modules are delivered with physical security kits pre-installed by HP. These physical security kits include metal port/component covers and tamper-evident tape. The CO shall inspect the modules upon receipt to ensure that the kits are properly installed. Diagrams and descriptions of the installed kits are provided below.

a. The P230i is an embedded controller and is shown in Figure 2 with the physical security kit installed. The P431 is shown with the required one (1) metal cover and three (3) strips of tamper-evident tape.
b. The P430 and P431 are stand-up cards and are shown in Figure 3 and Figure 4 with the physical security kit installed. The P430 is shown with the required one (1) metal cover and one (1) section of tamper-evident tape. The P431 is shown with the required one (1) metal cover and one (1) section of tamper-evident tape.
c. The P731m controller is a mezzanine card and is depicted in Figure 5 with the physical security kit installed. The P731m is shown with the required one (1) metal cover and one (1) section of tamper-evident tape.

Figure 4 – P431 Controller

Figure 5 – P731m Controller

d. The P830 is a stand-up card and is shown in Figure 6 with the physical security kit installed. The P830 is shown with the required one (1) metal cover and three (3) sections of tamper-evident tape.
e. The P830i is a daughter card and is shown in Figure 7 with the physical security kit installed. The P830i is shown with the required one (1) metal cover and one (1) section of tamper-evident tape.

- Set the CO password, key management mode, encryption mode, and disallow plaintext volumes
  1. Under **Tools**, click **Encryption Manager**.
  2. Select “Perform Initial Setup”. This will display the **Perform Initial Setup** screen.
  3. Under **Create Crypto Officer Password**, click **Show**.
  4. Enter (then re-enter) the desired password in the **Create Crypto Officer Password** fields. CO password is required to be at least 10 characters.
  5. Under **Encryption Mode**, select “Enable and Disallow Future Plaintext Volumes”.
  6. Under **Master Key**, enter the name of the Master Key in the field provided.
  7. Under **Key Management Mode**, select the desired key management mode.
  8. Click **OK**.

In Local mode, this password will be used to generate the Local Master Key.

- Enable the User role
1. Under **Tools**, click **Encryption Manager**.
2. Select “Set/Change User Password”. This will display the **Set/Change User Password** screen.
3. Under **New Password**, click **Show**.
4. Enter (then re-enter) the desired password in the **New Password** fields. User password is required to be at least 10 characters.
5. Click **OK**.

- **Verify and lock firmware**

The modules require the proper firmware version be installed. To check if a module is currently running the correct version, the CO must go to the ‘More info’ page for the controller on the GUI.

If the version is not v1.66, the firmware must be updated to the v1.66 version. To perform a firmware update, the updated firmware must be imported and applied to the controller. The controller will verify the firmware signature and then perform the update.

Once the firmware version is set to v1.66, the CO must lock the firmware. The firmware can be locked using the GUI Secure Management page by clicking the ‘Lock Firmware’ link. Locking the firmware prevents any further updates to the firmware, and ensures that the module is operating with the validated firmware.

### 3.1.2 Initial Setup using the SSA Scripting Interface

To initialize each module using the SSA Scripting interface, follow the steps in the *HP Smart Storage Administrator User Guide*, section “Accessing HP SSA in the online environment” to download and launch the application. Then follow the steps below to complete the initial setup.

- **Ensure physical security mechanisms are properly installed**

Modules have physical security kits installed by HP. See 3.1.1 for diagrams of the physical security kits.

- **This is a scripting interface, so all configurations can be set with one script. This requires that the module firmware be verified prior to running the script. To check if a module is currently running the correct version, the CO must verify the firmware through the SSA CLI or SSA GUI. This Set the CO password, key management mode, encryption mode, disallow plaintext volumes, set User password, and lock the firmware using the following script:**

  ```plaintext
  Action= Configure
  Method= Custom
  Controller= [SLOT n]
  AcceptEULA=yes
  EncryptionCryptoPasswordSet=[CO Password]
  EncryptionMasterKey= [IKEK name]
  EncryptionKeyManager= [Local | Remote]
  AllowPlainText= False
  Encryption= Enable
  EncryptionUserPasswordSet= [User Password]
  Firmwarelock= On
  ```

  Where:
  1. **Controller= [SLOT n]** where ‘n’ identifies the slot of the controller that the script is configuring.
2. EncryptionCryptoPasswordSet sets the CO’s password. The CO password must be at least 10 characters long.
3. EncryptionKeyManager sets the encryption mode to either ‘Local’ or ‘Remote’.
4. AllowPlainText is set to ‘False’ so that all new volumes created will be encrypted.
5. Encryption is set to ‘Enable’. This initiates self-tests and the module is capable of encrypted data.
6. EncryptionUserPasswordSet sets the User password. The User password must be at least 10 characters long.
7. FirmwareLock is set to ‘On’. Locking the firmware prevents any further updates to the firmware, and ensures that the module is operating with the validated firmware.

3.1.3 Initial Setup using the SSA CLI

To initialize the module using the SSA CLI, follow the steps in the HP Smart Storage Administrator User Guide, section “Accessing HP SSA in the online environment to download and launch the application.” Then open the HP Smart Storage Administrator CLI. Then follow the steps below to complete the initial setup.

- **Ensure physical security mechanisms are properly installed**
  Modules have physical security kits installed by HP. See 3.1.1 for diagrams of the physical security kits as they appear when installed.

- **Set the CO password, key management mode, encryption mode, and disallow plaintext volumes**

  To set these configuration items, use the following command sequence:

  ```plaintext
  Controller slot=[PCI slot number] enableencryption [eula=yes] 
  encryption=on localkeymanagermode=[on|off] mixedvolumes=off 
  [password=PASSWORD STRING] masterkey=MASTERKEY
  ```

  where:
  1. encryption=on enables the encryption mode for the module
  2. localkeymanagermode sets the local mode when ‘on’ and the remote mode when ‘off’
  3. mixedvolumes=off allows only encrypted logical disk creation.
  4. password allows the operator to input the CO password. The CO password must be at least 10 characters long.

  In Local mode, this password is used to generate the Local Master Key.

- **Enable the User role**

  To enable the User role, use the following command sequence:

  ```plaintext
  Controller slot=[PCI slot number] setpasswd user=user 
  password=PASSWORD STRING
  ```

  The User password is required to be at least 10 characters long.

- **Verify and lock firmware**
The modules require the proper firmware version be installed. To check if a module is currently running the correct version, the CO must enter `ctrl slot=N show detail` through the SSA CLI. N is the slot of the controller that contains the module. The output displayed on the screen will include the firmware version installed on the specified controller.

If the version is not v1.66, the firmware must be updated to the v1.66 version. To perform a firmware update, use the appropriate Smart Components to import the updated firmware and apply it to the controller. The controller will verify the firmware signature and then perform the update.

Once the firmware version is set to v1.66, the CO must lock the firmware. The firmware can be locked using the command `ctrl slot=N modify fwlock=on`, where N is again the slot of the controller whose firmware is being locked. Locking the firmware prevents any further updates to the firmware, and ensures that the module is operating with the validated firmware.

### 3.2 Secure Management

The Crypto Officer is responsible for ensuring that the modules are operating in their FIPS-Approved mode of operation.

#### 3.2.1 Management

When configured according to the Crypto Officer guidance in this Security Policy, the modules only run in their Approved mode of operation. The Crypto Officer shall configure the modules via the SSA GUI, SSA CLI utilities, or SSA Scripting interface. The Crypto Officer shall monitor and manage the modules only through the SSA GUI. The CO password shall be at least 10 characters in length. The Crypto Officer shall not set the controller password or disable encryption. Plaintext volumes shall not be allowed and shall not be moved to the controller. Detailed instructions to monitor and troubleshoot the systems are provided in the HP Secure Encryption Installation and User Guide.

#### 3.2.2 Physical Inspection

For the modules to operate in their FIPS-Approved mode of operation, the factory-installed physical security kits must be in place as specified in Section 3.1. Upon receipt, the CO shall inspect the module to ensure the physical security kit has been properly installed.

Per FIPS 140-2 Implementation Guidance (IG 14.4), the CO is also responsible for direct control and observation of any changes to the modules where the tamper-evident seals are removed or installed to ensure that the security of the modules is maintained during such changes and that the modules are returned to their Approved state.

The CO is also required to periodically inspect the modules for evidence of tampering at intervals specified per end-user policy. The CO must visually inspect the tamper-evident seals for tears, rips, dissolved adhesive, and other signs of tampering. If evidence of tampering is found during periodic inspection, the Crypto Officer must zeroize the keys and contact HP Customer Service via email at service@hp.com. The Crypto Officer must be sure to include contact information and the shipping address, as well as the controller serial number. HP Customer Service will instruct the CO on how to return the module to HP for installation of new tamper evident labels.

#### 3.2.3 Monitoring Status

The Crypto Officer should monitor the modules’ status regularly for Approved mode of operation. When configured according to the Crypto Officer’s guidance, the modules only operate in the Approved mode.
To monitor the controller’s encryption status, each controller has an encryption LED that will be on to show that encryption is enabled and all attached logical drives are encrypted. In addition, the SSA GUI will indicate the controller’s encryption status on the Encryption Manager page in the section marked “Settings”. When properly configured, the controller’s encryption status will be shown as ‘Enabled’. All attached logical drives shall have a lock icon next to them, to indicate they are encrypted drives. Only encrypted drives shall be allowed.

3.2.4 Zeroization

In order to zeroize all keys and CSPs the modules must be returned to the factory mode. On the GUI, this is done using the ‘Clear Encryption Configuration’ button. No encrypted logical drives can be attached for either of these commands to succeed. These commands will zeroize all keys and CSPs. The modules will need to be re-initialized to return to operation.

3.3 User Guidance

The User can reset his or her password and shall be responsible for ensuring that the new password meets the criteria listed in Section 3.1. The User can also lock the firmware. A User can also perform zeroization as discussed in 3.2.4 and view the controller’s encryption status using the methods discussed in 3.2.3. A User can move logical volumes to the controller. These volumes must be set to encrypt prior to moving to the controller.

3.4 Non-Approved Mode of Operation

When configured according to the Crypto Officer guidance in this Security Policy, the modules do not support a non-Approved mode of operation.
Table 8 provides definitions for the acronyms used in this document.

**Table 8 – Acronyms**

<table>
<thead>
<tr>
<th>Acronym</th>
<th>Definition</th>
</tr>
</thead>
<tbody>
<tr>
<td>AES</td>
<td>Advanced Encryption System</td>
</tr>
<tr>
<td>CLI</td>
<td>Command Line Interface</td>
</tr>
<tr>
<td>CMVP</td>
<td>Cryptographic Module Validation Program</td>
</tr>
<tr>
<td>CO</td>
<td>Crypto Officer</td>
</tr>
<tr>
<td>CSE</td>
<td>Communications Security Establishment</td>
</tr>
<tr>
<td>CSP</td>
<td>Critical Security Parameter</td>
</tr>
<tr>
<td>DEK</td>
<td>Data Encryption Key</td>
</tr>
<tr>
<td>DIMM</td>
<td>Dual in-line Memory</td>
</tr>
<tr>
<td>DRBG</td>
<td>Deterministic Random Bit Generator</td>
</tr>
<tr>
<td>ECB</td>
<td>Electronic Code Book</td>
</tr>
<tr>
<td>EMC</td>
<td>Electromagnetic Compatibility</td>
</tr>
<tr>
<td>EMI</td>
<td>Electromagnetic Interference</td>
</tr>
<tr>
<td>ESKM</td>
<td>Enterprise Secure Key Manager</td>
</tr>
<tr>
<td>FIPS</td>
<td>Federal Information Processing Standard</td>
</tr>
<tr>
<td>GUI</td>
<td>Graphical User Interface</td>
</tr>
<tr>
<td>HMAC</td>
<td>(keyed-) Hash Message Authentication Code</td>
</tr>
<tr>
<td>I/O</td>
<td>Input/Output</td>
</tr>
<tr>
<td>IG</td>
<td>Implementation Guidance</td>
</tr>
<tr>
<td>IKEK</td>
<td>Initial KEK Encryption Key or Master Key</td>
</tr>
<tr>
<td>KAT</td>
<td>Known Answer Test</td>
</tr>
<tr>
<td>KEK</td>
<td>Key Encryption Key or Drive Encryption Key</td>
</tr>
<tr>
<td>LED</td>
<td>Light Emitting Diode</td>
</tr>
<tr>
<td>Mbps</td>
<td>Megabits per Second</td>
</tr>
<tr>
<td>NDRNG</td>
<td>Non-Deterministic Random Number Generator</td>
</tr>
<tr>
<td>NIST</td>
<td>National Institute of Standards and Technology</td>
</tr>
<tr>
<td>NVRAM</td>
<td>Non-Volatile Random Access Memory</td>
</tr>
<tr>
<td>OS</td>
<td>Operating System</td>
</tr>
<tr>
<td>PBKDF2</td>
<td>Password Based Key Derivation Function</td>
</tr>
<tr>
<td>PCI</td>
<td>Peripheral Component Interconnect</td>
</tr>
<tr>
<td>Acronym</td>
<td>Definition</td>
</tr>
<tr>
<td>---------</td>
<td>------------------------------------------------</td>
</tr>
<tr>
<td>PCIe</td>
<td>PCI Express</td>
</tr>
<tr>
<td>RAID</td>
<td>Redundant Array of Independent Disks</td>
</tr>
<tr>
<td>RNG</td>
<td>Random Number Generator</td>
</tr>
<tr>
<td>ROC</td>
<td>RAID-on-Chip</td>
</tr>
<tr>
<td>RTOS</td>
<td>Real-Time Operating System</td>
</tr>
<tr>
<td>SAS</td>
<td>Serial Attached SCSI</td>
</tr>
<tr>
<td>SCSI</td>
<td>Small Computer System Interface</td>
</tr>
<tr>
<td>SHA</td>
<td>Secure Hash Algorithm</td>
</tr>
<tr>
<td>SP</td>
<td>Special Publication</td>
</tr>
<tr>
<td>SSA</td>
<td>Smart Storage Administrator</td>
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<tr>
<td>XEX</td>
<td>XOR-Encrypt-XOR</td>
</tr>
<tr>
<td>XOR</td>
<td>Exclusive Or</td>
</tr>
<tr>
<td>XTS</td>
<td>XEX-Based Tweaked-Codebook Mode with Ciphertext Stealing</td>
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</tbody>
</table>