

HYPORI



Hypori FIPS Object Module for OpenSSL
Version 2.0.10

Hypori FIPS 140-2 Non-proprietary
Security Policy for OpenSSL

Version 2.0.10

December 9, 2016

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Acknowledgments

This document was derived from the Security Policy associated with FIPS 140-2 validation certificate number 1747.

References

Reference	Full Specification Name
[ANS X9.31]	Digital Signatures Using Reversible Public Key Cryptography for the Financial Services Industry (rDSA)
[FIPS 140-2]	Security Requirements for Cryptographic modules, May 25, 2001
[FIPS 180-4]	Secure Hash Standard
[FIPS 186-4]	Digital Signature Standard
[FIPS 197]	Advanced Encryption Standard
[FIPS 198-1]	The Keyed-Hash Message Authentication Code (HMAC)
[SP 800-38B]	Recommendation for Block Cipher Modes of Operation: The CMAC Mode for Authentication
[SP 800-38C]	Recommendation for Block Cipher Modes of Operation: The CCM Mode for Authentication and Confidentiality
[SP 800-38D]	Recommendation for Block Cipher Modes of Operation: Galois/Counter Mode (GCM) and GMAC
[SP 800-56A]	Recommendation for Pair-Wise Key Establishment Schemes Using Discrete Logarithm Cryptography
[SP 800-67R1]	Recommendation for the Triple Data Encryption Algorithm (TDEA) Block Cipher
[SP 800-89]	Recommendation for Obtaining Assurances for Digital Signature Applications
[SP 800-90A]	Recommendation for Random Number Generation Using Deterministic Random Bit Generators
[SP 800-131A]	Transitions: Recommendation for Transitioning the Use of Cryptographic Algorithms and Key Lengths

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1. Introduction

This document is the non-proprietary security policy for the Hypori FIPS Object Module for OpenSSL, hereafter referred to as the Module.

The Module is a software library providing a C-language application program interface (API) for use by other processes that require cryptographic functionality. The Module is classified by FIPS 140-2 as a software module, multi-chip standalone module embodiment. The physical cryptographic boundary is the general purpose computer on which the module is installed. The logical cryptographic boundary of the Module is the `fipscanister` object module, a single object module file named `fipscanister.o` (Linux^{®1}/Unix^{®2} and Vxworks^{®3}) or `fipscanister.lib` (Microsoft Windows^{®4}). The Module performs no communications other than with the calling application (the process that invokes the Module services).

The FIPS 140-2 security levels for the Module are as follows:

Security Requirement	Security Level
Cryptographic Module Specification	1
Cryptographic Module Ports and Interfaces	1
Roles, Services, and Authentication	2
Finite State Model	1
Physical Security	NA
Operational Environment	1
Cryptographic Key Management	1
EMI/EMC	1
SelfTests	1
Design Assurance	3
Mitigation of Other Attacks	NA

Table 1 – Security Level of Security Requirements

The Module’s software version for this validation is 2.0.10. The v2.0.10 Module incorporates changes from the v2.0 module to support additional platforms. The v2.0.10 Module can be used in all the environments supported by the earlier v2.0, v2.0.1, v2.0.2, v2.0.3, v2.0.4, v2.0.5, v2.0.6, v2.0.7, v2.0.8 and v2.0.9 revisions of the Module.

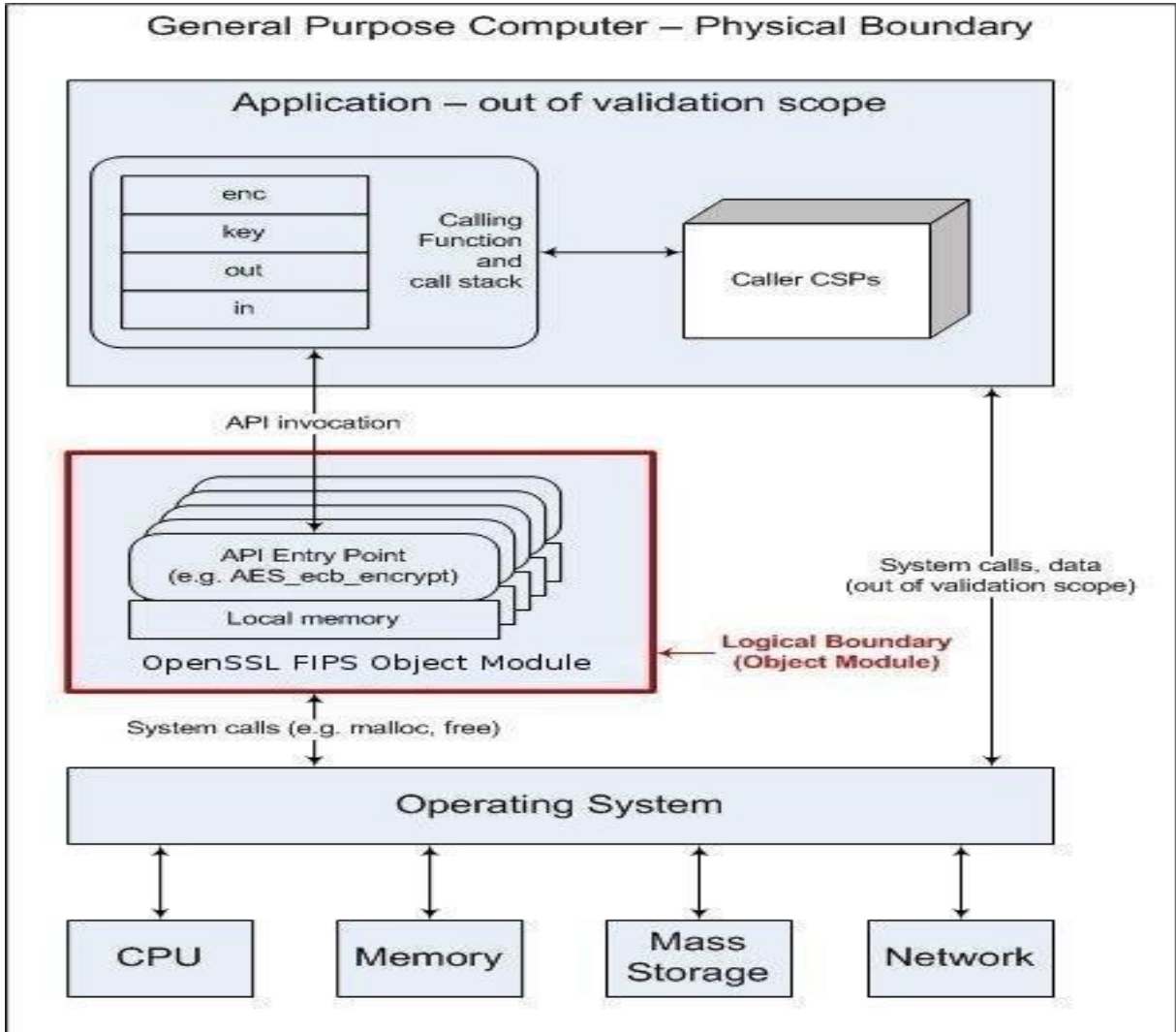
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² UNIX is a registered trademark of The Open Group.

³ Vxworks is a registered trademark owned by Wind River Systems, Inc.

⁴ Windows is a registered trademark of Microsoft Corporation in the United States and other countries.

Figure 1 - Module Block Diagram



2. Tested Configurations

#	Operational Environment	Processor	Optimizations (Target)	EC	B
1	Android 2.2 (HTC Desire)	Qualcomm QSD 8250 (ARMv7)	NEON	P	U2
2	Android 2.2 (Dell Streak)	Qualcomm QSD 8250 (ARMv7)	None	P	U2
3	Microsoft Windows 7 32 bit	Intel Celeron (x86)	None	BKP	W2
4	uCLinux 0.9.29	ARM 922T (ARMv4)	None	BKP	U2
5	Fedora 14	Intel Core i5 (x86)	AESNI	BKP	U2
6	HPUX 11i (hpuxia64cc, 32 bit mode)	Intel Itanium 2 (IA64)	None	BKP	U1
7	HPUX 11i (hpux64ia64cc, 64 bit mode)	Intel Itanium 2 (IA64)	None	BKP	U1
8	Ubuntu 10.04	Intel Pentium T4200 (x86)	None	BKP	U2
9	Android 3.0	NVIDIA Tegra 250 T20 (ARMv7)	None	P	U2
10	Linux 2.6.27	PowerPC e300c3 (PPC)	None	BKP	U2
11	Microsoft Windows 7 64 bit	Intel Pentium 4 (x86)	None	BKP	W2
12	Ubuntu 10.04 32 bit	Intel Core i5 (x86)	AESNI	BKP	U2
13	Linux 2.6.33	PowerPC32 e300 (PPC)	None	BKP	U2
14	Android 2.2	OMAP 3530 (ARMv7)	NEON	BKP	U2
15	DSP Media Framework 1.4	TI C64x+	None	BKP	U2
16	VxWorks 6.8	TI TNETV1050 (MIPS)	None	BKP	U2
17	Linux 2.6	Broadcom BCM11107 (ARMv6)	None	BKP	U2
18	Linux 2.6	TI TMS320DM6446 (ARMv4)	None	BKP	U2
19	Linux 2.6.32	TI AM3703CBP (ARMv7)	None	BKP	U2
20	Solaris 10 32bit	SPARCT3 (SPARCv9)	None	BKP	U2
21	Solaris 10 64bit	SPARCT3 (SPARCv9)	None	BKP	U2
22	Solaris 11 32bit	Intel Xeon 5675 (x86)	None	BKP	U2
23	Solaris 11 64bit	Intel Xeon 5675 (x86)	None	BKP	U2
24	Solaris 11 32bit	Intel Xeon 5675 (x86)	AESNI	BKP	U2
25	Solaris 11 64bit	Intel Xeon 5675 (x86)	AESNI	BKP	U2
26	Oracle Linux 5 64bit	Intel Xeon 5675 (x86)	None	BKP	U2
27	CascadeOS 6.1 32bit	Intel Pentium T4200 (x86)	None	BKP	U2
28	CascadeOS 6.1 64bit	Intel Pentium T4200(x86)	None	BKP	U2
29	Ubuntu 10.04 32bit	Intel Pentium T4200 (x86)	None	BKP	U1
30	Ubuntu 10.04 64bit	Intel Pentium T4200 (x86)	None	BKP	U1
31	Oracle Linux 5	Intel Xeon 5675 (x86)	AESNI	BKP	U2
32	Oracle Linux 6 Intel	Xeon 5675 (x86)	None	BKP	U2
33	Oracle Linux 6	Intel Xeon 5675 (x86)	AESNI	BKP	U2
34	Solaris 11 32bit	SPARCT3 (SPARCv9)	None	BKP	U2
35	Solaris 11 64bit	SPARCT3(SPARCv9)	None	BKP	U2
36	Android 4.0	NVIDIA Tegra 250 T20 (ARMv7)	None	P	U2
37	Linux 2.6	Freescale PowerPCe500	None	BKP	U2
38	Apple iOS 5.1	ARMv7	None	BKP	U2
39	WinCE 6.0	ARMv5TEJ	None	BKP	
40	WinCE 5.0	ARMv7	None	BKP	W2
41	Android 4.0	OMAP 3	NEON	P	U2
42	NetBSD 5.1	PowerPCe500	None	BKP	U2
43	NetBSD 5.1	Intel Xeon 5500 (x86)	None	BKP	U2
44	Windows 7 64bit	Intel Core i52430M (x86)	AESNI	BKP	W2
45	Android 4.1	TI DM3730 (ARMv7)	None	P	U2
46	Android 4.1	TI DM3730 (ARMv7)	NEON	P	U2
47	Android 4.2	Nvidia Tegra 3 (ARMv7)	None	P	U2
48	Android 4.2	Nvidia Tegra 3 (ARMv7)	NEON	P	U2
49	Windows Embedded	Freescale i.MX53xA (ARMv7)	NEON	BKP	W2

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	Compact 7				
50	Windows Embedded Compact 7	Freescale i.MX53xD (ARMv7)	NEON	BKP	W2
51	Android 4.0	Qualcomm Snapdragon APQ8060(ARMv7)	NEON	BKP	U2
52	Apple OS X 10.7	Intel Core i73615QM	None	BKP	U2
53	Apple iOS 5.0	ARM Cortex A8 (ARMv7)	NEON	BKP	U2
54	OpenWRT 2.6	MIPS 24Kc	None	BKP	U2
55	QNX 6.4	Freescale i.MX25 (ARMv4)	None	BKP	U2
56	Apple iOS 6.1	Apple A6X SoC (ARMv7s)	None	BKP	U2
57	eCos 3	Freescale i.MX27 926ejs (ARMv5TEJ)	None	BKP	U2
58	VMware Horizon Workspace 1.5 under VMware ESXi 5.0	Intel Xeon E31220 (x86)	None	BKP	U2
59	VMware Horizon Workspace 1.5 under VMware ESXi 5.0	Intel Xeon E31220 (x86)	AESNI	BKP	U2
60	Ubuntu 13.04	AM335x CortexA8 (ARMv7)	None	BKP	U2
61	Ubuntu 13.04	AM335x CortexA8 (ARMv7)	NEON	BKP	U2
62	Linux 3.8	ARM926 (ARMv5TEJ)	None	BKP	U2
63	Linux 3.4 under Citrix XenServer 6.2	Intel Xeon E52430L (x86)	None	BKP	U2
64	Linux 3.4 under Citrix XenServer 6.2	Intel Xeon E52430L (x86)	AESNI	BKP	U2
65	Linux 3.4 under VMware ESXi 5.1	Intel Xeon E52430L (x86)	None	BKP	U2
66	Linux 3.4 under VMware ESXi 5.1	Intel Xeon E52430L (x86)	AESNI	BKP	U2
67	Linux 3.4 under Microsoft Windows 2012 HyperV	Intel Xeon E52430L (x86)	None	BKP	U2
68	Linux 3.4 under Microsoft Windows 2012 HyperV	Intel Xeon E52430L (x86)	AESNI	BKP	U2
69	Apple iOS 6.0	Apple A5 / ARM CortexA9 (ARMv7)	BKP	U2	
70	Apple iOS 6.0	Apple A5 / ARM CortexA9 (ARMv7)	NEON	BKP	U2
71	PexOS 1.0 under vSphere ESXi 5.1	Intel Xeon E52430L (x86)	None	BKP	U2
72	PexOS 1.0 under vSphere ESXi 5.1	Intel Xeon E52430L (x86)	AESNI	BKP	U2
73	Linux 2.6	Freescale e500v2 (PPC)	None	BKP	U2
74	AcanOS 1.0	Intel Core i73612QE (x86)	None	BKP	U2
75	AcanOS 1.0	Intel Core i73612QE (x86)	AESNI	BKP	U2
76	AcanOS 1.0	Feroceon 88FR131 (ARMv5)	None	BKP	U2
77	FreeBSD 8.4	Intel Xeon E5440 (x86)	None	BKP	U2
78	FreeBSD 9.1	Xeon E52430L (x86)	None	BKP	U2
79	FreeBSD 9.1	Xeon E52430L (x86)	AESNI	BKP	U2
80	ArbOS 5.3	Xeon E5645 (x86)	None	BKP	U2
81	ArbOS 5.3	Xeon E5645 (x86)	AESNI	BKP	U2
82	Linux ORACLESP 2.6	ASPEED ASTSeries (ARMv5)	None	BKP	U2
83	Linux ORACLESP 2.6	Emulex PILOT 3 (ARMv5)	None	BKP	U2
84	FreeBSD 9.2	Xeon E52430L (x86)	None	BKP	U2
85	FreeBSD 9.2	Xeon E52430L (x86)	AESNI	BKP	U2
86	FreeBSD 10.0	Xeon E52430L (x86)	None	BKP	U2
87	FreeBSD 10.0	Xeon E52430L (x86)	AESNI	BKP	U2

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88	FreeBSD 8.4 32bit	Xeon E5440 (x86)	None	BKP	U2
89	VMware Horizon Workspace 2.1 x86 under vSphere ESXi 5.5	Intel Xeon E31220	None	BKP	U2
90	VMware Horizon Workspace 2.1 x86 under vSphere ESXi 5.5	Intel Xeon E31220	AESNI	BKP	U2
91	QNX 6.5 on ARMv4	Freescale i.MX25 (ARMv4)	None	BKP	U2
92	Apple iOS 7.1 64bit on ARMv8	Apple A7 (ARMv8)	None	BKP	U2
93	Apple iOS 7.1 64bit on ARMv8	Apple A7 (ARMv8)	NEON	BKP	U2
94	TSLinux 2.4	Arm920Tid (ARMv4)	None	BKP	U2
95	iOS 8.1 64bit	Apple A7 (ARMv8)	None	BKP	U2
96	iOS 8.1 64bit	Apple A7 (ARMv8)	NEON and Crypto Extensions	BKP	U2
97	VxWorks 6.9	Freescale P2020 (PPC)	None	BKP	U2
98	iOS 8.1 32bit	Apple A7 (ARMv8)	None	BKP	U2
99	iOS 8.1 32bit	Apple A7 (ARMv8)	NEON	BKP	U2
100	Android 5.0 32bit	Qualcomm APQ8084 (ARMv7)	None	BKP	U2
101	Android 5.0 32bit	Qualcomm APQ8084 (ARMv7)	NEON	BKP	U2
102	Android 5.0 64bit	SAMSUNG Exynos742 (ARMv8)	None	BKP	U2
103	Android 5.0 64bit	SAMSUNG Exynos7420 (ARMv8)	NEON and Crypto Extensions	BKP	U2
104	Android 4.4	Intel Xeon E5-2450 (x86)	None	BKP	U1
105	Android 4.4	Intel Xeon E5-2450 (ARMv7)	None	BKP	U1

Table 2 - Tested Configurations (B = Build Method; EC = Elliptic Curve Support). The EC column indicates support for prime curve only (P), or all NIST defined B, K, and P curves (BKP).

See Appendix A for additional information on build method and optimizations. See Appendix C for a list of the specific compilers used to generate the Module for the respective operational environment.

3. Ports and Interfaces

The physical ports of the Module are the same as the computer system on which it is executing. The logical interface is a C-language application program interface (API).

Logical interface type	Description
Control input	API entry point and corresponding stack parameters
Data input	API entry point data input stack parameters
Status output	API entry point return values and status stack parameters
Data output	API entry point data output stack parameters

Table 3 - Logical interfaces

As a software module, control of the physical ports is outside module scope. However, when the module is performing self-tests, or is in an error state, all output on the logical data output interface is inhibited. The module is single-threaded and in error scenarios returns only an error value (no data output is returned).

4. Modes of Operation and Cryptographic Functionality

The Module supports only a FIPS 140-2 Approved mode. Tables 4a and 4b list the Approved and Non-approved but Allowed algorithms, respectively.

Function	Algorithm	Options	Cert #
Random Number Generation; Symmetric key generation	[SP 80090] DRBG ⁵ Prediction resistance supported for all variations	Hash DRBG HMAC DRBG, no reseed CTR DRBG (AES), no derivation function	157, 229, 264, 292, 316, 342, 485, 540, 607, 723, 1262
Encryption, Decryption and CMAC	[SP 80067]	3Key Triple-DES TECB, TCBC, TCFB, TOFB; CMAC generate and verify	1223, 1346, 1398, 1465, 1492, 1522, 1695, 1742, 1780, 1853, 2270
	[FIPS 197] AES [SP 80038B] CMAC [SP 80038C] CCM [SP 80038D] GCM [SP 80038E] XTS	128/ 192/256 ECB, CBC, OFB, CFB 1, CFB 8, CFB 128, CTR, XTS; CCM; GCM; CMAC generate and verify	1884, 2116, 2234, 2342, 2394, 2484, 2824, 2929, 3090, 3264, 4154
Message Digests	[FIPS 1803]	SHA1, SHA2 (224, 256, 384, 512)	1655, 1840, 1923, 2019, 2056, 2102, 2368, 2465, 2553, 2702, 3419
Keyed Hash	[FIPS 198] HMAC	SHA1, SHA2 (224, 256, 384, 512)	1126,

⁵ For all DRBGs the "supported security strengths" is just the highest supported security strength per [SP800-90] and [SP800-57].

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			1288, 1363, 1451, 1485, 1526, 1768, 1856, 1937, 2063, 2722
		PKG: CURVES(P224 P384 P521) PKV: CURVES(P192 P224 P256 P384 P521)	264, 378
Digital Signature and Asymmetric Key Generation	[FIPS 1862] RSA	GenKey9.31, SigGen9.31, SigGenPKCS1.5, SigGenPSS, SigVer9.31, SigVerPKCS1.5, SigVerPSS (2048/3072/4096 with all SHA2 sizes)	960, 1086, 1145, 1205, 1273, 1477, 1535, 1581, 1664, 2262
	[FIPS 1864] DSA	PQG Gen, PQG Ver, Key Pair Gen, Sig Gen, Sig Ver (1024/2048/3072 with all SHA2 sizes)	589, 661, 693, 734, 748, 764, 853, 870, 896, 933, 1128
	[FIPS 1862] ECDSA	PKG: CURVES(P224 P384 P521 K233 K283 K409 K571 B233 B283 B409 B571) PKV: CURVES(P192 P224 P256 P384 P521 K163 K233 K283 K409 K571 B163 B233 B283 B409 B571)	270, 315, 347, 383, 394, 413, 496, 528, 558, 620, 956
		PKG: CURVES(P224 P384 P521) PKV: CURVES(P192 P224 P256 P384 P521)	264, 378
	[FIPS 1864] ECDSA	PKG: CURVES(P-224 P-256 P-384 P-521 K-224 K-256 K-384 K-521 B-224 B-256 B-384 B-521 ExtraRandomBits TestingCandidates PKV: CURVES(ALL-P ALL-K ALL-B SigGen: CURVES(P-224: (SHA-224, 256, 384, 394, 512) P-256: (SHA-224, 256, 384, 512) P-384: (SHA-224, 256, 384, 512) P-521: (SHA-224, , 256, 384, 512) K-233: (SHA-224, 256, 384, 512) K-283: (SHA-224, 256, 384, 512) K-409: (SHA-224, 256, 384, 512) K-571: (SHA-224, 256, 384, 512) B-233: (SHA-224, 256,	270, 315, 347, 383, 394, 413, 496, 528, 558, 620,

		384, 512) B-283: (SHA-224, 256, 384, 512) B-409: (SHA-224, 256, 384, 512) B-571: (SHA-224, 256, 384, 512)) SigVer: CURVES(P-192: (SHA-1, 224, 256, 384, 512) P-224: (SHA-1, 224, 256, 384, 512) P-256: (SHA-1, 224, 256, 384, 512) P-384: (SHA-1, 224, 256, 384, 512) P-521: (SHA-1, 224, 256, 384, 512) K-163: (SHA-1, 224, 256, 384, 512) K-233: (SHA-1, 224, 256, 384, 512) K-283: (SHA-1, 224, 256, 384, 512) K-409: (SHA-1, 224, 256, 384, 512) K-571: (SHA-1, 224, 256, 384, 512) B-163: (SHA-1, 224, 256, 384, 512) B-233: (SHA-1, 224, 256, 384, 512) B-409: (SHA-1, 224, 256, 384, 512) B-571: (SHA-1, 224, 256, 384, 512))	663, 956
		PKG: CURVES(P-224 P-256 P-384 P-521) PKV: CURVES(ALL-P) SigGen: CURVES(P-224: (SHA-224, 256, 384, 512) P-256: (SHA-224, 256, 384, 512) P-384: (SHA-224, 256, 384, 512) P-521: (SHA-224, 256, 384, 512)) SigVer: CURVES(P-192: (SHA-1, 224, 256, 384, 512) P-224: (SHA-1, 224, 256, 384, 512) P-256: (SHA-1, 224, 256, 384, 512) P-384: (SHA-1, 224, 256, 384, 512) P-521: (SHA-1, 224, 256, 384, 512))	264, 378
ECC CDH (CVL)	[SP 800-56A] (§5.7.1.2)	All NIST defined B, K and P curves except sizes 163 and 192	12, 24, 36, 53, 71, 85, 260, 331, 372, 472, 958
		All NIST defined P curves	10, 49

Table 4a – FIPS Approved Cryptographic Functions

The Module supports only NIST defined curves for use with ECDSA and ECC CDH

Category	Algorithm	Description
Key Agreement	EC DH	Non-compliant (untested) DH scheme using elliptic curve, supporting all NIST defined B, K and P curves. Key agreement is a service provided for calling process use, but is not used to establish keys into the Module.
Key Encryption, Decryption	RSA	The RSA algorithm may be used by the calling application for encryption or decryption of keys. No claim is made for SP 800-56B compliance, and no CSPs are established into or exported out of the module using these services.

Table 4b – Non-FIPS Approved But Allowed Cryptographic Functions

The Module implements the following services which are Non-Approved per the SP 800-131A transition:

Function	Algorithm	Options	Cert #
Random Number Generation; Symmetric	[ANS X9.31] RNG	AES 128/192/256	985, 1087,

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key generation			1119, 1166, 1186, 1202, 1278 1292, 1314,
Random Number Generation; Symmetric key generation	[SP 800-90] DRBG	Dual EC DRBG	157, 229, 264, 292, 316, 342, 485
Digital Signature and Asymmetric Key Generation	[FIPS 1862] RSA	GenKey9.31, SigGen9.31, SigGenPKCS1.5, SigGenPSS (1024/1536 with all SHA sizes, 2048/3072/4096 with SHA1)	589, 661, 693, 734, 748, 764
	[FIPS 1862] DSA	PQG Gen, Key Pair Gen, Sig Gen (1024 with all SHA sizes, 2048/3072 with SHA1)	589, 661, 693, 734, 748, 764
	[FIPS 1864] DSA	PQG Gen, Key Pair Gen, Sig Gen (1024 with all SHA sizes, 2048/3072 with SHA1)	589, 661, 693, 734, 748, 764
	[FIPS 1862] ECDSA	PKG: CURVES(P192 K163 B163) SIG(gen): CURVES(P192 P224 P256 P384 P521 K163 K233 K283 K409 K571 B163 B233 B283 B409 B571)	270, 315, 347, 383, 394, 413
			264, 378
	[FIPS 1864] ECDSA	PKG: CURVES(P192 K163 B163) SigGen: CURVES(P192: (SHA1, 224, 256, 384, 512) P224:(SHA1) P256:(SHA1) P384: (SHA1) P521:(SHA1) K163: (SHA1, 224, 256, 384, 512) K233:(SHA1) K283:(SHA1) K409:(SHA1) K571:(SHA1) B163: (SHA1, 224, 256, 384, 512) B233:(SHA1) B283: (SHA1) B409:(SHA1) B571:(SHA1))	270, 315, 347, 383, 394, 413
		264, 378	
ECC CDH (CVL)	[SP 800-56A] (§5.7.1.2)	All NIST Recommended B, K and P curves sizes 163 and 192	12, 24, 36, 53, 71, 85
			10, 49

X9.31 RNG is Non-Approved effective December 31, 2015, per the CMVP Notice "X9.31 RNG transition, December 31, 2015". These algorithms shall not be used when operating in the FIPS Approved mode of operation. EC DH Key Agreement provides a maximum of 256 bits of security strength. RSA Key Wrapping provides a maximum of 256 bits of security strength.

The Module requires an initialization sequence (see IG 9.5): the calling application invokes `FIPS_mode_set()`⁶, which returns a "1" for success and "0" for failure. If `FIPS_mode_set()` fails then all cryptographic services fail from then on. The application can test to see if FIPS mode has been successfully performed.

The Module is a cryptographic engine library, which can be used only in conjunction with additional software. Aside from the use of the NIST defined elliptic curves as trusted third party domain parameters, all other FIPS 186-3 assurances are outside the scope of the Module, and are the responsibility of the calling process.

4.1. Critical Security Parameters and Public Keys

All CSPs used by the Module are described in this section. All access to these CSPs by Module services are described in Section 4. The CSP names are generic, corresponding to API parameter data structures.

CSP Name	Description
RSA SGK	RSA (1024 to 16384 bits) signature generation key
RSA KDK	RSA (1024 to 16384 bits) key decryption (private key transport) key
DSA SGK	[FIPS 186-4] DSA (1024/2048/3072) signature generation key or [FIPS 186-2] DSA (1024) signature generation key
ECDSA SGK	ECDSA (All NIST defined B, K, and P curves) signature generation key
EC DH Private	EC DH (All NIST defined B, K, and P curves) private key agreement key.
AES EDK	AES (128/192/256) encrypt / decrypt key
AES CMAC	AES (128/192/256) CMAC generate / verify key
AES GCM	AES (128/192/256) encrypt / decrypt / generate / verify key
AES XTS	AES (256/512) XTS encrypt / decrypt key
TDES EDK	Triple-DES (3-Key) encrypt / decrypt key
TDES CMAC	Triple-DES (3-Key) CMAC generate / verify key
HMAC Key	Keyed hash key (160/224/256/384/512)
Hash_DRBG CSPs	V (440/888 bits) and C (440/888 bits), entropy input (length dependent on security strength)
HMAC_DRBG CSPs	V (160/224/256/384/512 bits) and Key (160/224/256/384/512 bits), entropy input (length dependent on security strength)
CTR_DRBG CSPs	V (128 bits) and Key (AES 128/192/256), entropy input (length dependent on security strength)

⁶ The function call in the Module is `FIPS_module_mode_set()` which is typically used by an application via the `FIPS_mode_set()` wrapper function.

CO-AD-Digest	Pre-calculated HMAC-SHA-1 digest used for Crypto Officer role authentication
User-AD-Digest	Pre-calculated HMAC-SHA-1 digest used for User role authentication

Table 4.1a – Critical Security Parameters

Authentication data is loaded into the module during the module build process, performed by an authorized operator (Crypto Officer), and otherwise cannot be accessed.

The module does not output intermediate key generation values.

CSP Name	Description
RSA SVK	RSA (1024 to 16384 bits) signature verification public key
RSA KEK	RSA (1024 to 16384 bits) key encryption (public key transport) key
DSA SVK	[FIPS 186-4] DSA (1024/2048/3072) signature verification key or [FIPS 186-2] DSA (1024) signature verification key
ECDSA SVK	ECDSA (All NIST defined B, K and P curves) signature verification key
EC DH Public	EC DH (All NIST defined B, K and P curves) public key agreement key.

Table 4.1b – Public Keys

For all CSPs and Public Keys:

- **Storage:** RAM, associated to entities by memory location. The Module stores DRBG state values for the lifetime of the DRBG instance. The module uses CSPs passed in by the calling application on the stack. The Module does not store any CSP persistently (beyond the lifetime of an API call), with the exception of DRBG state values used for the Modules' default key generation service.
- **Generation:** The Module implements 800-90 compliant DRBG services for creation of symmetric keys, and for generation of DSA, elliptic curve, and RSA keys as shown in Table 4a. The calling application is responsible for storage of generated keys returned by the module.
- **Entry:** All CSPs enter the Module's logical boundary in plaintext as API parameters, associated by memory location. However, none cross the physical boundary.
- **Output:** The Module does not output CSPs, other than as explicit results of key generation services. However, none cross the physical boundary.
- **Destruction:** Zeroization of sensitive data is performed automatically by API function calls for temporarily stored CSPs. In addition, the module provides functions to explicitly destroy CSPs related to random number generation services. The calling application is responsible for parameters passed in and out of the module.

Private and secret keys as well as seeds and entropy input are provided to the Module by the calling application, and are destroyed when released by the appropriate API function calls. Keys residing in internally allocated data structures (during the lifetime of an API call) can only be accessed using the Module defined API. The operating system protects memory and process space from unauthorized access. Only the calling application that creates or imports keys can use or export such keys. All API functions are executed by the invoking calling application in a non-overlapping sequence such that no two API functions will execute concurrently. An authorized application as user (Crypto-Officer and User) has access to all key data generated during the operation of the Module.

In the event Module power is lost and restored the calling application must ensure that any AES-GCM keys used for encryption or decryption are re-distributed.

Module users (the calling applications) shall use entropy sources that meet the security strength required for the random number generation mechanism as shown in [SP 800-90A Table 2 (Hash_DRBG, HMAC_DRBG), and Table 3 (CTR_DRBG)]. This entropy is supplied by means of callback functions. Those functions must return an error if the minimum entropy strength cannot be met.

5. Roles, Authentication and Services

The Module implements the required User and Crypto Officer roles and requires authentication for those roles. Only one role may be active at a time and the Module does not allow concurrent operators. The User or Crypto Officer role is assumed by passing the appropriate password to the `FIPS_module_mode_set()` function. The password values may be specified at build time and must have a minimum length of 16 characters. Any attempt to authenticate with an invalid password will result in an immediate and permanent failure condition rendering the Module unable to enter the FIPS mode of operation, even with subsequent use of a correct password.

Authentication data is loaded into the Module during the Module build process, performed by the Crypto Officer, and otherwise cannot be accessed.

Since minimum password length is 16 characters, the probability of a random successful authentication attempt in one try is a maximum of $1/256^{16}$, or less than $1/10^{38}$. The Module permanently disables further authentication attempts after a single failure, so this probability is independent of time.

Both roles have access to all of the services provided by the Module.

- User Role (User): Loading the Module and calling any of the API functions.
- Crypto Officer Role (CO): Installation of the Module on the host computer system and calling of any API functions.

All services implemented by the Module are listed below, along with a description of service CSP access.

Service	Role	Description
Initialize	User, CO	Module initialization. Does not access CSPs.
Self-test	User, CO	Perform self tests (<code>FIPS_selftest</code>). Does not access CSPs.
Show status	User, CO	Functions that provide module status information: <ul style="list-style-type: none"> • Version (as unsigned long or const char *) • FIPS Mode (Boolean) Does not access CSPs.
Zeroize	User, CO	Functions that destroy CSPs: <ul style="list-style-type: none"> • <code>fips_drbg_uninstantiate</code>: for a given DRBG context, overwrites DRBG CSPs (Hash_DRBG CSPs, HMAC_DRBG CSPs, CTR_DRBG CSPs) All other services automatically overwrite CSPs stored in allocated memory. Stack cleanup is the responsibility of the calling application.

Random number generation	User, CO	Used for random number and symmetric key generation. <ul style="list-style-type: none"> • Seed or reseed a DRBG instance • Determine security strength of a DRBG instance • Obtain random data Uses and updates Hash_DRBG CSPs, HMAC_DRBG CSPs, CTR_DRBG CSPs.
Asymmetric key generation	User, CO	Used to generate DSA, ECDSA and RSA keys: RSA SGK, RSA SVK; DSA SGK, DSA SVK; ECDSA SGK, ECDSA SVK There is one supported entropy strength for each mechanism and algorithm type, the maximum specified in SP800-90
Symmetric encrypt/decrypt	User, CO	Used to encrypt or decrypt data. Executes using AES EDK, Triple-DES EDK (passed in by the calling process).
Symmetric digest	User, CO	Used to generate or verify data integrity with CMAC. Executes using AES CMAC, Triple-DES, CMAC (passed in by the calling process).
Message digest	User, CO	Used to generate a SHA-1 or SHA-2 message digest. Does not access CSPs.
Keyed Hash	User, CO	Used to generate or verify data integrity with HMAC. Executes using HMAC Key (passed in by the calling process).
Key transport ⁷	User, CO	Used to encrypt or decrypt a key value on behalf of the calling process (does not establish keys into the module). Executes using RSA KDK, RSA KEK (passed in by the calling process).
Key agreement	User, CO	Used to perform key agreement primitives on behalf of the calling process (does not establish keys into the module). Executes using EC DH Private, EC DH Public (passed in by the calling process).
Digital signature	User, CO	Used to generate or verify RSA, DSA or ECDSA digital signatures. Executes using RSA SGK, RSA SVK; DSA SGK, DSA SVK; ECDSA SGK, ECDSA SVK (passed in by the calling process).
Utility	User, CO	Miscellaneous helper functions. Does not access CSPs.

Table 5 - Services and CSP Access

⁷ "Key transport" can refer to a) moving keys in and out of the module or b) the use of keys by an external application. The latter definition is the one that applies to the OpenSSL FIPS Object Module.

6. Self-test

The Module performs the self-tests listed below on invocation of Initialize or Self-test.

Algorithm	Type	Test Attributes
Software integrity	KAT	HMAC-SHA1
HMAC	KAT	One KAT per SHA1, SHA224, SHA256, SHA384 and SHA512 Per IG 9.3, this testing covers SHA POST requirements.
AES	KAT	Separate encrypt and decrypt, ECB mode, 128 bit key length
AES CCM	KAT	Separate encrypt and decrypt, 192 key length
AES GCM	KAT	Separate encrypt and decrypt, 256 key length
XTS-AES	KAT	128, 256 bit key sizes to support either the 256-bit key size (for XTS-AES-128) or the 512-bit key size (for XTS-AES-256)
AES CMAC	KAT	Sign and verify CBC mode, 128, 192, 256 key lengths
Triple-DES	KAT	Separate encrypt and decrypt, ECB mode, 3-Key
Triple-DES CMAC	KAT	CMAC generate and verify, CBC mode, 3-Key
RSA	KAT	Sign and verify using 2048 bit key, SHA-256, PKCS#1
DSA	PCT	Sign and verify using 2048 bit key, SHA-384
DRBG	KAT	CTR_DRBG: AES, 256 bit with and without derivation function HASH_DRBG: SHA256 HMAC_DRBG: SHA256
ECDSA	PCT	Keygen, sign, verify using P-224, K-233 and SHA512. The K-233 self-test is not performed for operational environments that support prime curve only (see Table)
ECC CDH	KAT	Shared secret calculation per SP 800-56A §5.7.1.2, IG 9.6

Table 6a - Power On Self Tests (KAT = Known answer test; PCT = Pairwise consistency test)

The Module is installed using one of the set of instructions in Appendix A, as appropriate for the target system. The HMAC-SHA-1 of the Module distribution file as tested by the CMT Laboratory and listed in Appendix A is verified during installation of the Module file as described in Appendix A.

The `FIPS_mode_set()`⁸ function performs all power-up self-tests listed above with no operator intervention required, returning a “1” if all power-up self-tests succeed, and a “0” otherwise. If any component of the power-up self-test fails an internal flag is set to prevent subsequent invocation of any cryptographic function calls. The module will only enter the FIPS Approved mode if the module is reloaded and the call to `FIPS_mode_set()` succeeds.

⁸ `FIPS_mode_set()` calls Module function `FIPS_module_mode_set()`

The power-up self-tests may also be performed on-demand by calling `FIPS_selftest()`, which returns a “1” for success and “0” for failure. Interpretation of this return code is the responsibility of the calling application.

The Module also implements the following conditional tests:

Algorithm	Test
DRBG	Tested as required by [SP800-90] Section 11
DRBG	FIPS 140-2 continuous test for stuck fault
DSA	Pairwise consistency test on each generation of a key pair
ECDSA	Pairwise consistency test on each generation of a key pair
RSA	Pairwise consistency test on each generation of a key pair

Table 6b - Conditional Tests

In the event of a DRBG self-test failure the calling application must unstantiate and re-instantiate the DRBG per the requirements of [SP 800-90]; this is not something the Module can do itself.

Pairwise consistency tests are performed for both possible modes of use, e.g. Sign/Verify and Encrypt/Decrypt.

The Module supports two operational environment configurations for elliptic curve: NIST prime curves only (listed in Table 2 with the EC column marked "P") and all NIST defined curves (listed in Table 2 with the EC column marked "BKP").

7. Operational Environment

The tested operating systems segregate user processes into separate process spaces. Each process space is logically separated from all other processes by the operating system software and hardware. The Module functions entirely within the process space of the calling application, and implicitly satisfies the FIPS 140-2 requirement for a single user mode of operation.

8. Mitigation of other Attacks

The module is not designed to mitigate against attacks which are outside of the scope of FIPS 140-2.

Appendix A Installation and Usage Guidance

The test platforms represent different combinations of installation instructions. For each platform there is a build system, the host providing the build environment in which the installation instructions are executed, and a target system on which the generated object code is executed. The build and target systems may be the same type of system or even the same device, or may be different systems – the Module supports cross-compilation environments.

Each of these command sets are relative to the top of the directory containing the uncompressed and expanded contents of the distribution files *openssl-fips-2.0.10.tar.gz* (all NIST defined curves as listed in Table 2 with the EC column marked "BKP") or *openssl-fips-ecp-2.0.10.tar.gz* (NIST prime curves only as listed in Table 2 with the EC column marked "P"). The command sets are:

```
U1:
    ./config no-asm
    make
    make install
U2:
    ./config
    make
    make install
W1:
    ms\do_fips no-asm
W2:
    ms\do_fips
```

8.1. *Installation Instructions*

1. Download and copy the distribution file to the build system.

These files can be downloaded from <http://www.openssl.org/source/>.

2. Verify the HMAC-SHA-1 digest of the distribution file; see Appendix B. An independently acquired FIPS 140-2 validated implementation of SHA-1 HMAC must be used for this digest verification. Note that this verification can be performed on any convenient system and not necessarily on the specific build or target system. Alternatively, a copy of the distribution on physical media can be obtained from OSF⁹.
3. Unpack the distribution.

```
gunzip -c openssl-fips-2.0.10.tar.gz | tar xf - cd openssl-fips-2.0.10
```

4. Execute one of the installation command sets U1, W1, U2, W2 as shown above. No other command sets shall be used.
5. The resulting *fipsanister.o* or *fipsanister.lib* file is now available for use.

⁹ For some prospective users the acquisition, installation, and configuration of a suitable FIPS 140-2 validated product may not be convenient. OSF will on request mail a CD containing the source code distribution, via USPS or international post. A distribution file received by that means need not be verified by a FIPS 140-2 validated implementation of HMAC-SHA-1. For instructions on requesting this CD see <http://openssl.com/fips/verify.html>.

6. The calling application enables FIPS mode by calling the `FIPS_mode_set()`¹⁰ function. Note that failure to use one of the specified commands sets exactly as shown will result in a module that cannot be considered compliant with FIPS 140-2.

8.2. *Linking the Runtime Executable Application*

Note that applications interfacing with the FIPS Object Module are outside of the cryptographic boundary. When linking the application with the FIPS Object Module two steps are necessary:

1. The HMAC-SHA-1 digest of the FIPS Object Module file must be calculated and verified against the installed digest to ensure the integrity of the FIPS object module.
2. A HMAC-SHA1 digest of the FIPS Object Module must be generated and embedded in the FIPS Object Module for use by the `FIPS_mode_set()` function at runtime initialization.

The `fips_standalone_sha1` command can be used to perform the verification of the FIPS Object Module and to generate the new HMAC-SHA-1 digest for the runtime executable application. Failure to embed the digest in the executable object will prevent initialization of FIPS mode.

At runtime the `FIPS_mode_set()` function compares the embedded HMAC-SHA-1 digest with a digest generated from the FIPS Object Module object code. This digest is the final link in the chain of validation from the original source to the runtime executable application file.

Optimization

The “asm” designation means that assembler language optimizations were enabled when the binary code was built, “no-asm” means that only C language code was compiled.

For OpenSSL with x86 there are three possible optimization levels:

1. No optimization (plain C)
2. SSE2 optimization
3. AES-NI+PCLMULQDQ+SSSE3 optimization

Other theoretically possible combinations (e.g. AES-NI only, or SSE3 only) are not addressed individually, so that a processor which does not support all three of AES-NI, PCLMULQDQ, and SSSE3 will fall back to SSE2 optimization.

For more information, see:

- <http://www.intel.com/support/processors/sb/CS-030123.htm?wapkw=sse2>
- <http://software.intel.com/en-us/articles/intel-advanced-encryption-standard-instructions-aes-ni/?wapkw=aes-ni>

For OpenSSL with ARM there are two possible optimization levels:

1. Without NEON
2. With NEON (ARM7 only)

¹⁰ `FIPS_mode_set()` calls the Module function `FIPS_module_mode_set()`

For more information, see <http://www.arm.com/products/processors/technologies/neon.php>

Appendix B Controlled Distribution File Fingerprint

The *OpenSSL FIPS Object Module v2.0.10* consists of the FIPS Object Module (the *fipscanister.o* or *fipscanister.lib* contiguous unit of binary object code) generated from the specific source files.

For all NIST defined curves (listed in Table 2 with the EC column marked "BKP") the source files are in the specific special OpenSSL distribution *openssl-fips-2.0.10.tar.gz* with HMAC-SHA-1 digest of

```
af8bda4bb9739e35b4ef00a9bc40d21a6a97a780
```

located at <http://www.openssl.org/source/openssl-fips-2.0.10.tar.gz>.

The `openssl` command from a version of OpenSSL that incorporates a previously validated version of the module may be used:

```
openssl sha1 -hmac etaonrishdlcupfm openssl-fips-2.0.10.tar.gz
```

For NIST prime curves only (listed in Table 2 with the EC column marked "P") the source files are in the specific special OpenSSL distribution *openssl-fips-ecp-2.0.10.tar.gz* with HMAC-SHA-1 digest of

```
02cc9ddfffb2e917d1cdc9ebc97a9731c40f6394
```

located at <http://www.openssl.org/source/openssl-fips-ecp-2.0.10.tar.gz>. Note this is from the previous revision of the FIPS Object Module as no modifications relevant to NIST prime curves only were introduced in revision 2.0.10.

The set of files specified in this tar file constitutes the complete set of source files of this module. There shall be no additions, deletions, or alterations of this set as used during module build. The OpenSSL distribution tar file (and patch file if used) shall be verified using the above HMAC-SHA-1 digest(s).

The arbitrary 16 byte key of:

```
65 74 61 6f 6e 72 69 73 68 64 6c 63 75 70 66 6d
```

(equivalent to the ASCII string "etaonrishdlcupfm") is used to generate the HMAC-SHA-1 value for the FIPS Object Module integrity check.

The functionality of all earlier revisions of the FIPS Object Module are subsumed by this latest revision, so there is no reason to use older revisions for any new deployments.

Appendix C Compilers

This appendix lists the specific compilers used to generate the Module for the respective Operational Environments. Note this list does not imply that use of the Module is restricted to only the listed compiler versions, only that the use of other versions has not been confirmed to produce a correct result.

#	Operational Environment	Compiler
1	Android 2.2 (HTC Desire)	gcc 4.4.0
2	Android 2.2 (Dell Streak)	gcc 4.4.0
3	Microsoft Windows 7 32 bit	Microsoft 32bit C/C++ Optimizing Compiler Version 16.00
4	uClinux 0.9.29	gcc 4.2.1
5	Fedora 14	gcc 4.5.1
6	HPUX 11i (hpuxia64cc, 32 bit mode)	HP C/aC++ B3910B
7	HPUX 11i (hpux64ia64cc, 64 bit mode)	HP C/aC++ B3910B
8	Ubuntu 10.04	gcc 4.1.3
9	Android 3.0	gcc 4.4.0
10	Linux 2.6.27	gcc 4.2.4
11	Microsoft Windows 7 64 bit	Microsoft C/C++ Optimizing Compiler Version 16.00 for x64
12	Ubuntu 10.04 32 bit	gcc 4.1.3
13	Linux 2.6.33	gcc 4.1.0
14	Android 2.2	gcc 4.1.0
15	DSP Media Framework 1.4	TMS320C6x C/C++ Compiler v6.0.13
16	VxWorks 6.8	gcc 4.1.2
17	Linux 2.6	gcc 4.3.2
18	Linux 2.6	gcc 4.3.2
19	Linux 2.6.32	gcc 4.3.2
20	Solaris 10 32bit	gcc 3.4.3
21	Solaris 10 64bit	gcc 3.4.3
22	Solaris 11 32bit	gcc 4.5.2
23	Solaris 11 64bit	gcc 4.5.2
24	Solaris 11 32bit	gcc 4.5.2
25	Solaris 11 64bit	gcc 4.5.2
26	Oracle Linux 5 64bit	gcc 4.1.2
27	CascadeOS 6.1 32bit	gcc 4.4.5
28	CascadeOS 6.1 64bit	gcc 4.4.5
29	Ubuntu 10.04 32bit	gcc 4.1.3
30	Ubuntu 10.04 64bit	gcc 4.1.3
31	Oracle Linux 5	gcc 4.1.2
32	Oracle Linux 6	gcc 4.4.6
33	Oracle Linux 6	gcc 4.4.6
34	Solaris 11 32bit	Sun C 5.12
35	Solaris 11 64bit	Sun C 5.12
36	Android 4.0	gcc 4.4.3
37	Linux 2.6	gcc 4.1.0
38	Apple iOS 5.1	gcc 4.2.1

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39	WinCE 6.0	Microsoft C/C++ Optimizing Compiler Version 15.00 for ARM
40	WinCE 5.0	Microsoft C/C++ Optimizing Compiler Version 13.10 for ARM
41	Android 4.0	gcc 4.4.3
42	NetBSD 5.1	gcc 4.1.3
43	NetBSD 5.1	gcc 4.1.3
44	Windows 7	Microsoft (R) C/C++ Optimizing Compiler Version 16.00 for x64
45	Android 4.1	gcc 4.6
46	Android 4.1	gcc 4.6
47	Android 4.2	gcc 4.6
48	Android 4.2	gcc 4.6
49	Windows Embedded Compact 7	Microsoft C/C++ Optimizing Compiler Version 15.00.20720 for ARM
50	Windows Embedded Compact 7	Microsoft C/C++ Optimizing Compiler Version 15.00.20720 for ARM
51	Android 4.0	gcc 4.4.3
52	Apple OS X 10.7	Apple LLVM version 4.2
53	Apple iOS 5.0	gcc 4.2.1
54	OpenWRT 2.6	gcc 4.6.3
55	QNX 6.4	gcc 4.3.3
56	Apple iOS 6.1	gcc 4.2.1
57	eCos 3	gcc 4.3.2
58	VMware Horizon Workspace 1.5 under vSphere	gcc 4.5.1
59	VMware Horizon Workspace 1.5 under vSphere	gcc 4.5.1
60	Ubuntu 13.04	gcc 4.7.3
61	Ubuntu 13.04	gcc 4.7.3
62	Linux 3.8	gcc 4.7.3
63	Linux 3.4 under Citrix XenServer 6.2	gcc 4.8.0
64	Linux 3.4 under Citrix XenServer 6.2	gcc 4.8.0
65	Linux 3.4 under VMware ESXi 5.1	gcc 4.8.0
66	Linux 3.4 under VMware ESXi 5.1	gcc 4.8.0
67	Linux 3.4 under Microsoft Windows 2012 HyperV	gcc 4.8.0
68	Linux 3.4 under Microsoft Windows 2012 HyperV	gcc 4.8.0
69	Apple iOS 6.0	gcc 4.2.1
70	Apple iOS 6.0	gcc 4.2.1
71	PexOS 1.0 under vSphere	gcc 4.6.3
72	PexOS 1.0 under vSphere	gcc 4.6.3
73	Linux 2.6	gcc 4.4.1
74	AcanOS 1.0	gcc 4.6.2
75	AcanOS 1.0	gcc 4.6.2
76	AcanOS 1.0	gcc 4.5.3
77	FreeBSD 8.4	gcc 4.2.1
78	FreeBSD 9.1	gcc 4.2.1

79	FreeBSD 9.1	gcc 4.2.1
80	ArbOS 5.3	gcc 4.1.2
81	ArbOS 5.3	gcc 4.1.2
82	Linux ORACLESP 2.6	gcc 4.4.5
83	Linux ORACLESP 2.6	gcc 4.4.5
84	FreeBSD 9.2	gcc 4.2.1
85	FreeBSD 9.2	gcc 4.2.1
86	FreeBSD 10.0	clang 3.3
87	FreeBSD 10.0	clang 3.3
88	FreeBSD 8.4	gcc 4.2.1
89	VMware Horizon Workspace 2.1 x86 under vSphere	gcc 4.5.1
90	VMware Horizon Workspace 2.1 x86 under vSphere	gcc 4.5.1
91	QNX on ARMv4	gcc 4.3.3
92	Apple iOS 7.1 64bit on ARMv8	clang 5.1
93	Apple iOS 7.1 64bit on ARMv8	clang 5.1
94	TSLinux 2.4	gcc 4.3.2
95	iOS 8.1 64bit	clang600.0.56
96	iOS 8.1 64bit	clang600.0.56
97	VxWorks 6.9	gcc 4.3.3
98	iOS 8.1 32bit	clang600.0.56
99	iOS 8.1 32bit	clang600.0.56
100	Android 5.0	gcc 4.9
101	Android 5.0	gcc 4.9
102	Android 5.0 64bit	gcc 4.9
103	Android 5.0 64bit	gcc 4.9
104	Android 4.4	gcc 4.8.5
105	Android 4.4	gcc 4.8.5

Table C - Compilers