



pqm4: Testing and Benchmarking NIST PQC on ARM Cortex-M4

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A tropical sunset scene with palm trees silhouetted against a bright orange and yellow sky. The sun is low on the horizon, reflecting on the ocean. A white text box with a black border is centered in the image.

[pi kju ɛm fɔr]

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 - Do schemes even fit in limited RAM + flash?
 - Are schemes efficient on small ARMs?
 - What is the overhead of masking?



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– everyone, always



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- ▶ STM32F4DISCOVERY
 - ARM Cortex-M4
 - 32-bit, ARMv7E-M
 - 192 KiB RAM, 168 MHz
- ▶ PQM4: test and optimize on the Cortex-M4
 - github.com/mupq/pqm4



- ▶ They are cheap ($< \$30$)



Rationale for using STM32F4DISCOVERY boards

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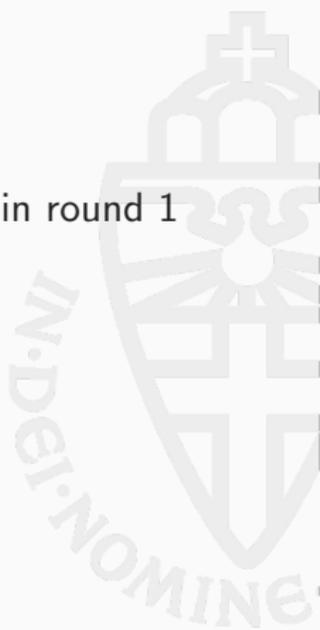


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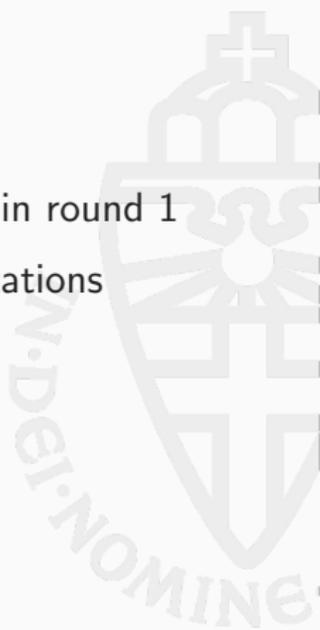
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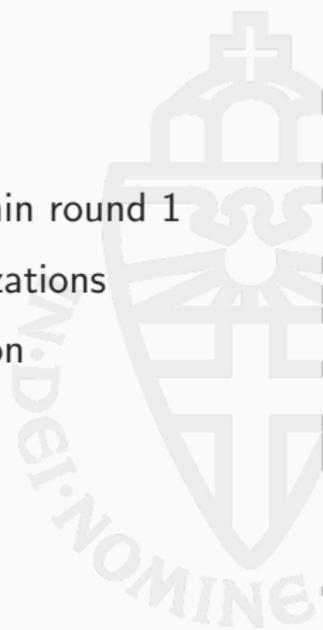
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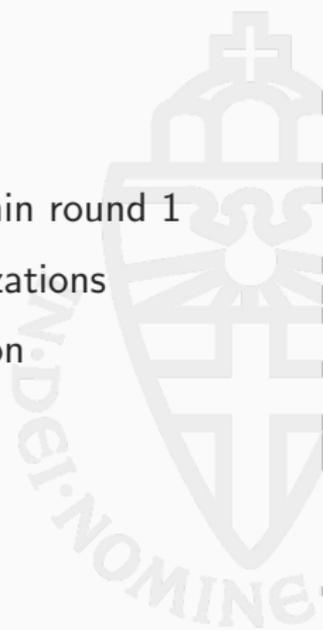
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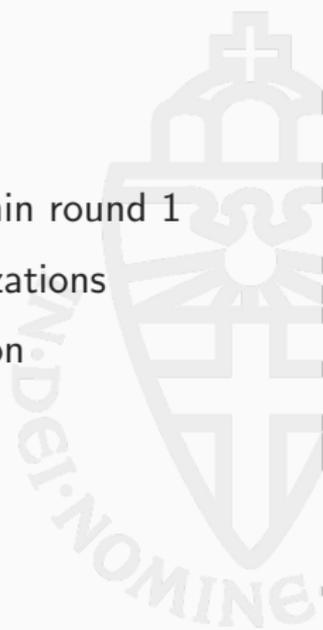
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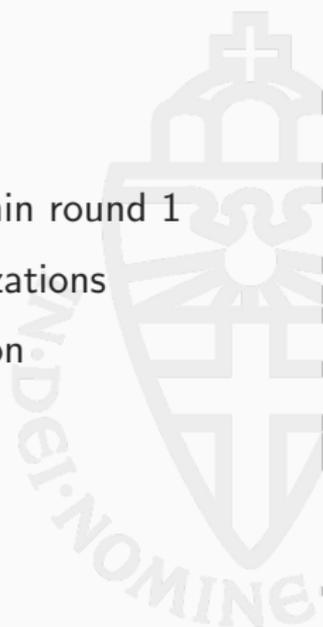
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 - Our students know how to work with them



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- `m4`: Optimized using ARMv7E-M assembly

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Schemes included in pqm4– KEMs

	reference	optimized	
BIKE	\times_{Lib}	—	
Classic McEliece	\times_{Key}	—	
CRYSTALS-Kyber	✓	✓	[BKS19]
Frodo-KEM	✓	✓	[BFM ⁺ 18]
HQC	\times_{Lib}	—	
LAC	✓	—	
LEDAcrypt	\times_{RAM}	WIP	
NewHope	✓	✓	[AJS16]
NTRU	✓	✓	[KRS19]
NTRU Prime	✓	—	
NTS-KEM	\times_{Key}	—	
ROLLO	\times_{Lib}	—	
Round5	✓	✓	Round5 team
RQC	\times_{Lib}	—	
SABER	✓	✓	[KRS19]
SIKE	✓	—	
ThreeBears	✓	✓	ThreeBears team

\times_{Key} : keys too large \times_{RAM} : implementation uses too much RAM

\times_{Lib} : available implementations depend on external libraries

Schemes included in pqm4– Signatures

	reference	optimized	
CRYSTALS-Dilithium	✓	✓	[GKOS18, RSGCB19]
FALCON	✗ _{RAM}	✓	Falcon team
GeMSS	✗ _{Key}	—	
LUOV	✓	—	
MQDSS	✗ _{RAM}	—	
Picnic	✗ _{RAM}	—	
qTESLA	✓	—	
Rainbow	✗ _{Key}	—	
SPHINCS+	✓	—	

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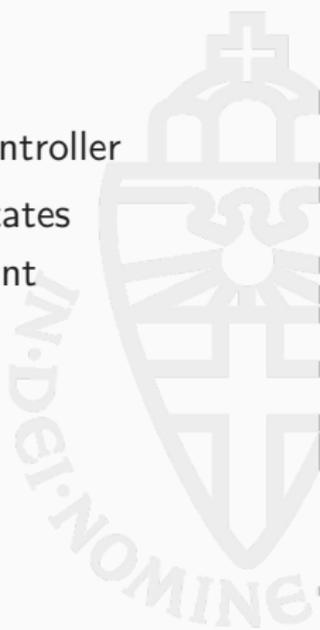


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- We use the hardware RNG of our platform
- Most schemes only sample seed, so speed doesn't matter

- ▶ Submission packages often come with different implementations of SHA-2, SHA-3, or AES

¹<https://github.com/XKCP/XKCP>

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Benchmarking: Fast Hashing

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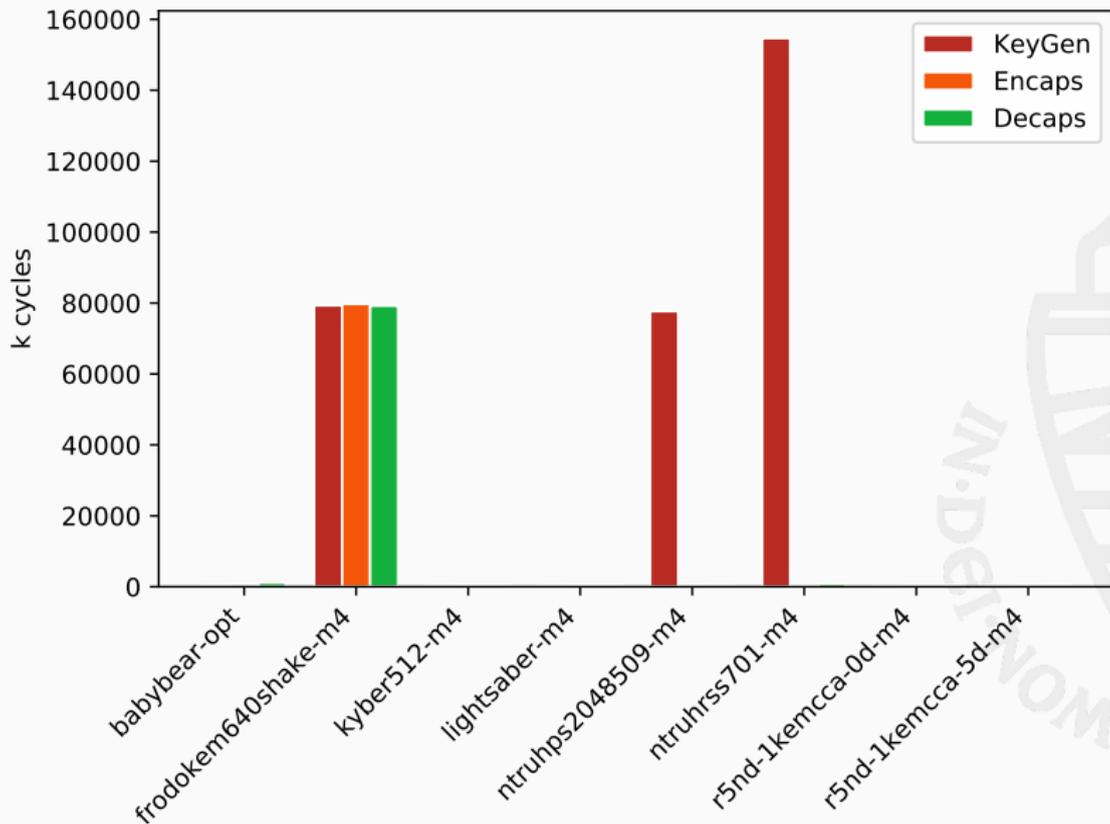
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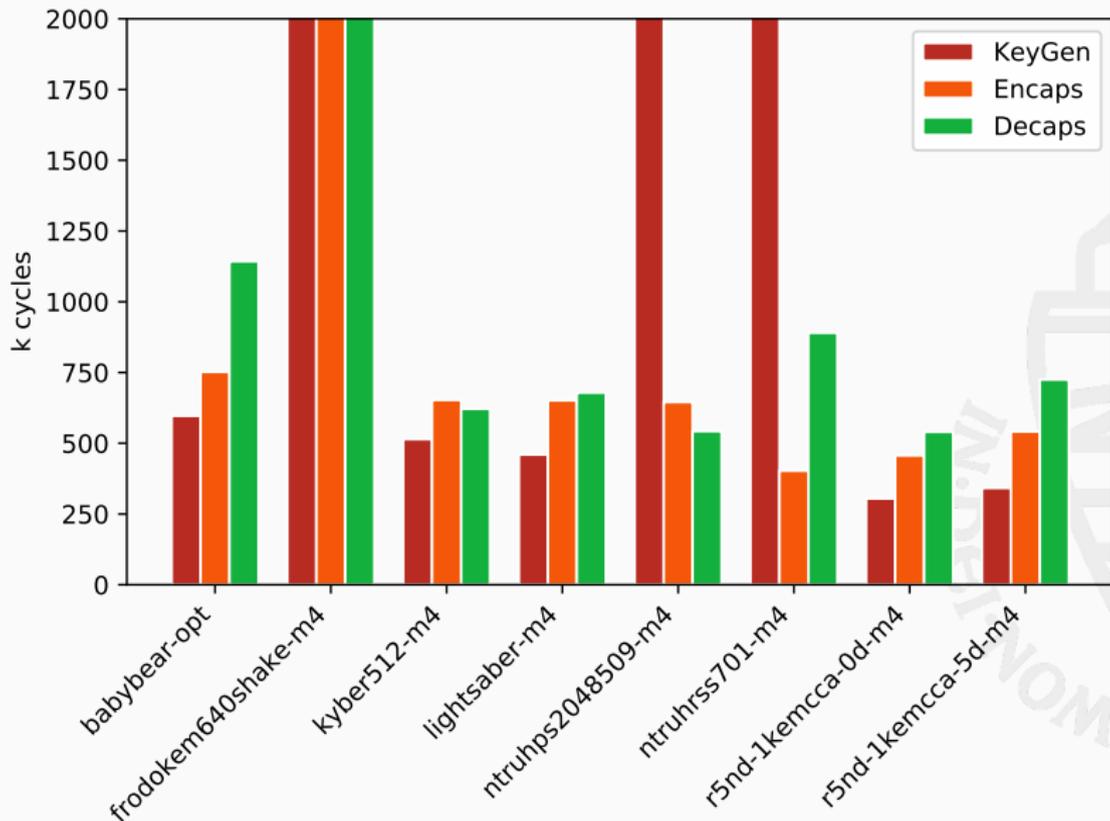
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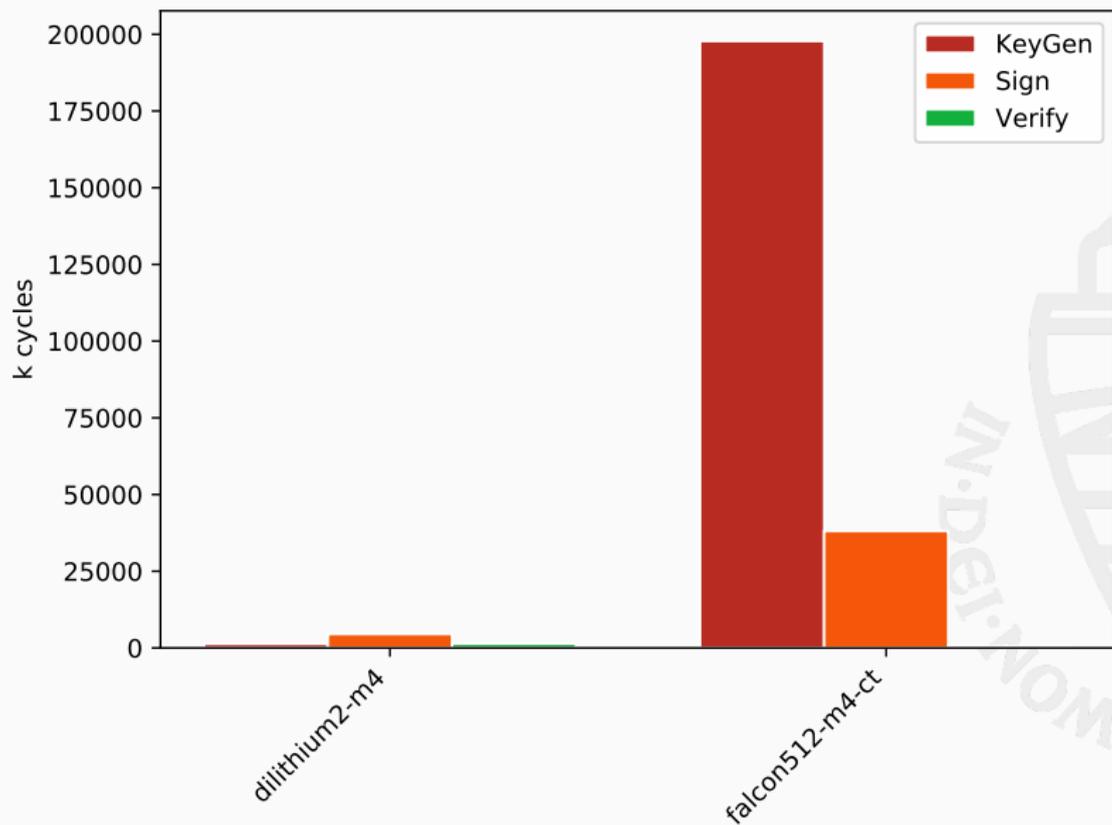
KEM Speed



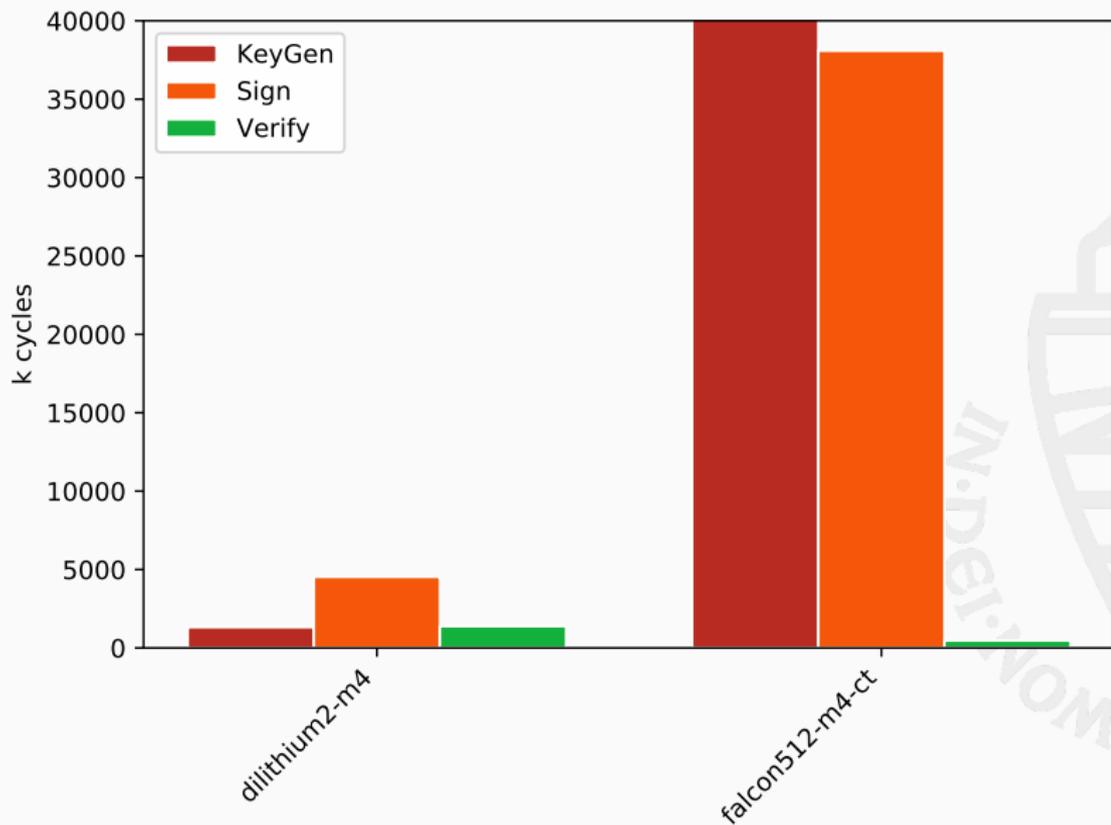
KEM Speed (2)



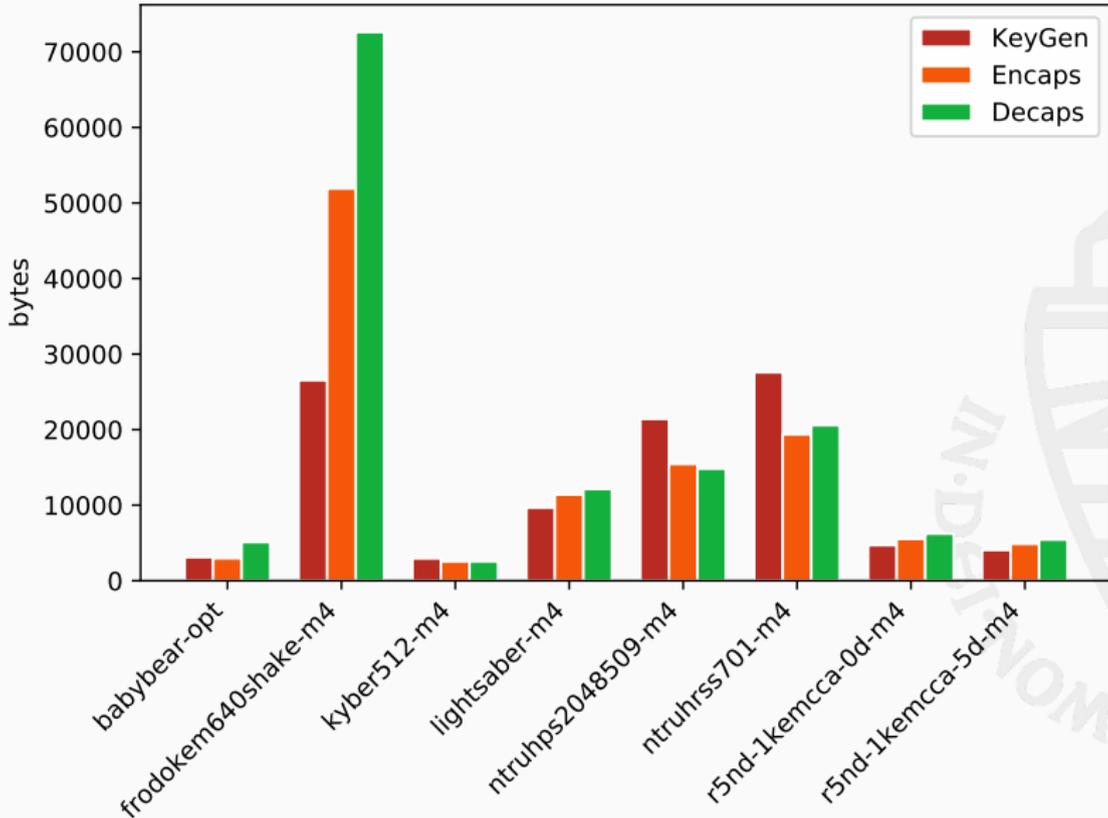
Signature Speed



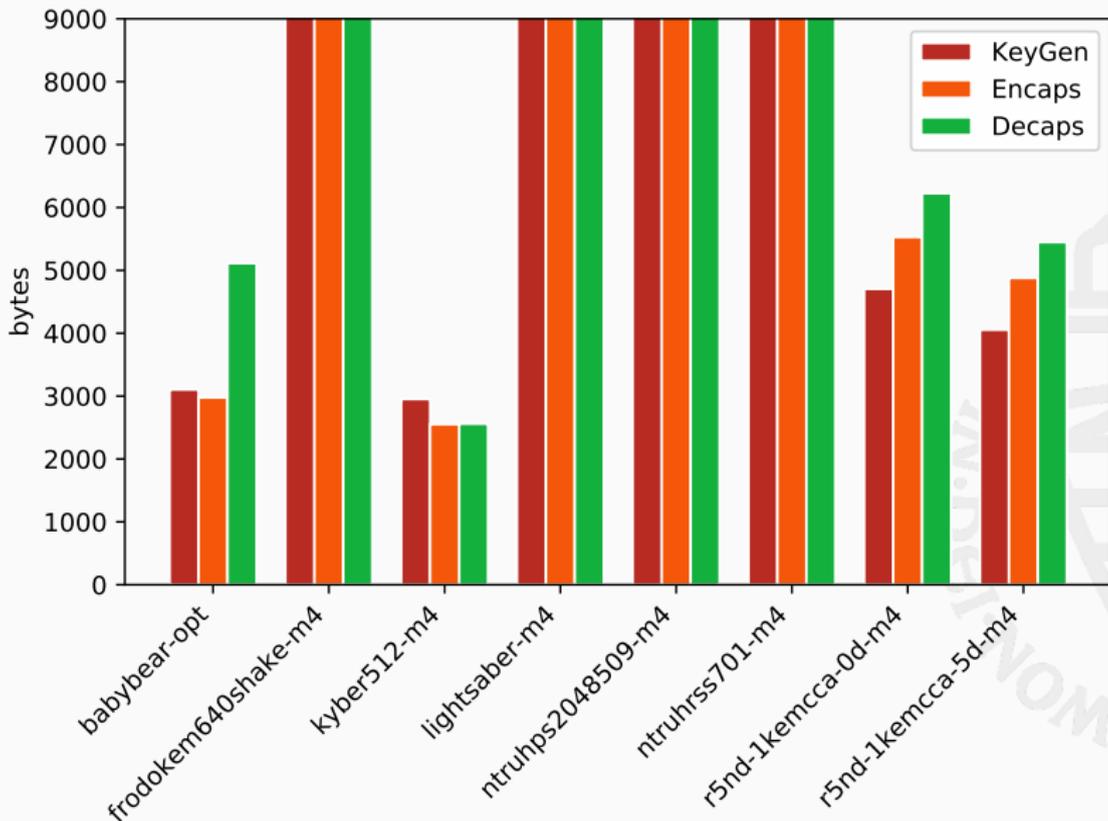
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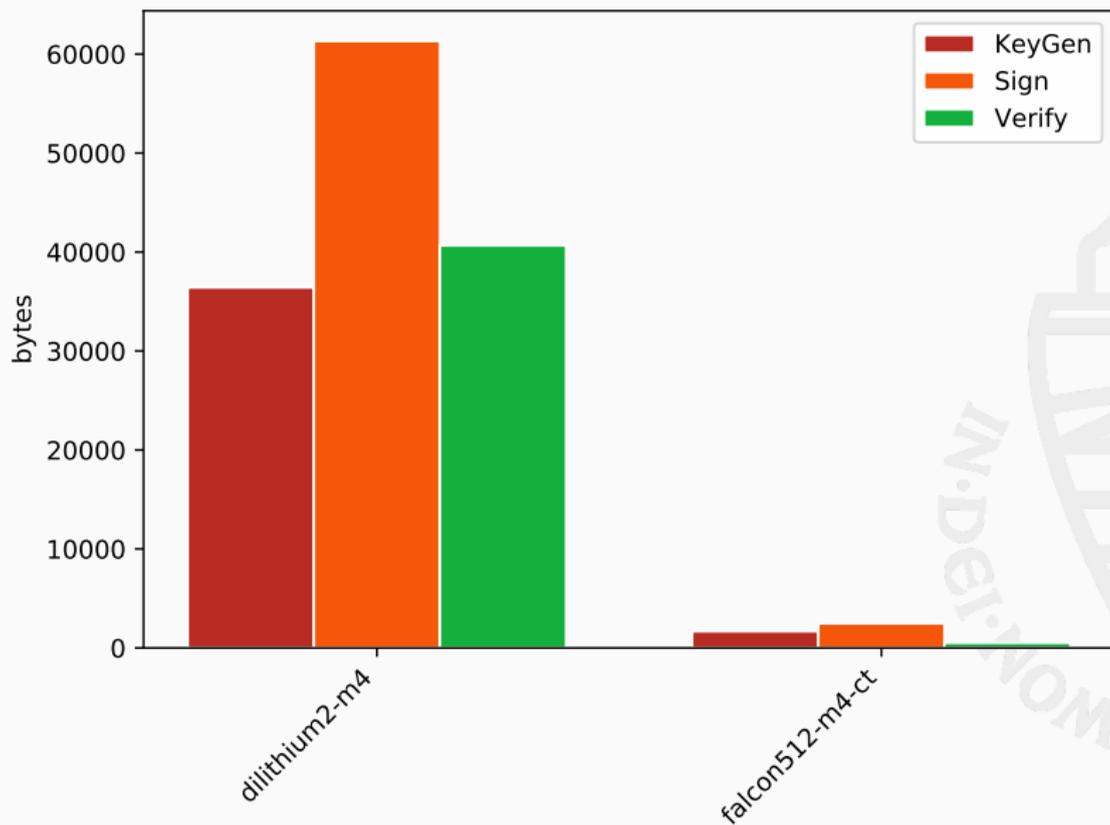
KEM RAM consumption



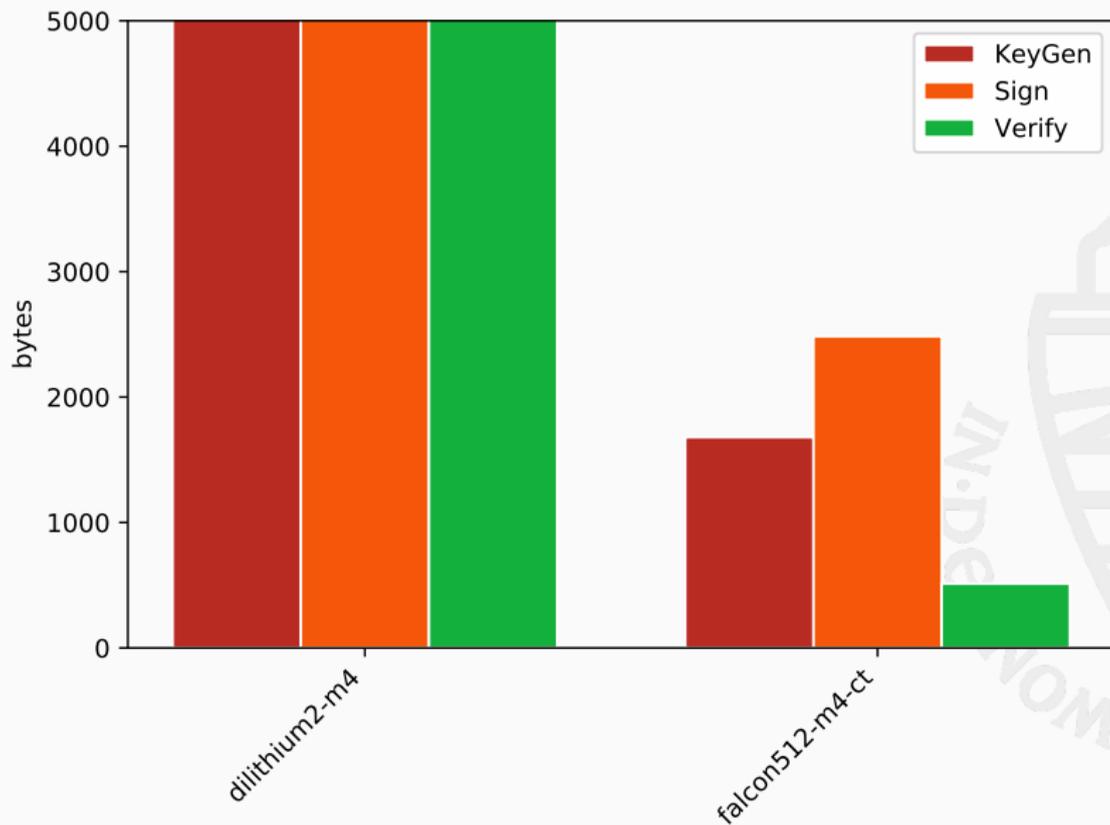
KEM RAM consumption (2)



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- ▶ BIKE, HQC, ROLLO, RQC use OpenSSL/NTL/GMP
 - needs to be replaced to make it work



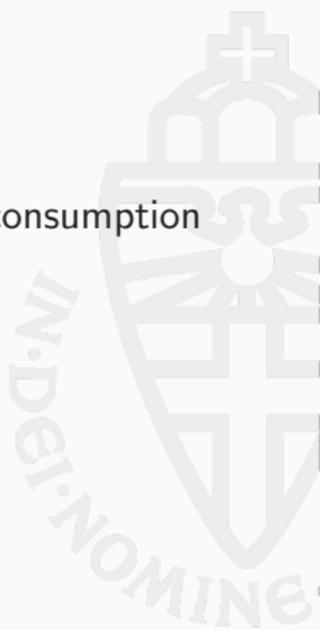
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 - But Kyber, NTRU, Saber, ThreeBears very close

<https://github.com/mupq/pqm4>

slides and paper available at kannwischer.eu

Thank you!



-  Erdem Alkim, Philipp Jakubeit, and Peter Schwabe.
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