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<td>SP800-90A Rev. 1</td>
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<td>SP800-131A Rev. 1</td>
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<td>Transitions: Recommendation for Transitioning the Use of Cryptographic Algorithms and Key Lengths, November 2015</td>
</tr>
<tr>
<td>SP800-133</td>
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### 1.2 Abbreviations

<table>
<thead>
<tr>
<th>Abbreviation</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ANS.1</td>
<td>Abstract Syntax Notation One. A notation for describing data structures.</td>
</tr>
<tr>
<td>AES</td>
<td>The Advanced Encryption Standard. The AES is intended to be issued as a FIPS standard and will replace DES. In January 1997 the AES initiative was announced and in September 1997 the public was invited to propose suitable block ciphers as candidates for the AES. NIST is looking for a cipher that will remain secure well into the next century. NIST selected Rijndael as the AES algorithm.</td>
</tr>
<tr>
<td>AES_CCM</td>
<td>AES Counter mode with Cipher block chaining-Message authentication code as documented in NIST SP800-38C.</td>
</tr>
<tr>
<td>AES_GCM</td>
<td>AES Galois counter mode as documented in NIST SP800-38D.</td>
</tr>
<tr>
<td>AES_KW</td>
<td>AES Key Wrap mode as documented in NIST SP800-38F.</td>
</tr>
<tr>
<td>AES_KWP</td>
<td>AES Key Wrap with Padding mode as documented in NIST SP800-38F.</td>
</tr>
<tr>
<td>AES_XTS</td>
<td>AES XOR Encrypt XOR (XEX) tweakable block cipher with ciphertext stealing mode as documented in NIST SP800-38E.</td>
</tr>
<tr>
<td>Camellia</td>
<td>A 128 bit block cipher developed by NTT.</td>
</tr>
<tr>
<td>CMAC</td>
<td>Cipher-based Message Authentication Code, as documented in NIST SP800-38B.</td>
</tr>
<tr>
<td>CMVP</td>
<td>Cryptographic Module Validation Program; a joint effort between US NIST and Communications Security Establishment (CSE) of Canada.</td>
</tr>
<tr>
<td>CSE</td>
<td>Communications Security Establishment of Canada.</td>
</tr>
<tr>
<td>CPACF</td>
<td>CP (central processor) assist for Cryptographic Functions.</td>
</tr>
<tr>
<td>Crypto</td>
<td>Cryptographic capability/functionality</td>
</tr>
<tr>
<td><strong>DES</strong></td>
<td>The Data Encryption Standard, an encryption block cipher defined and endorsed by the U.S. government in 1977 as an official standard; the details can be found in the latest official FIPS (Federal Information Processing Standards) publication concerning DES. It was originally developed at IBM. DES has been extensively studied since its publication and is the most well-known and widely used cryptosystem in the world.</td>
</tr>
<tr>
<td><strong>DH</strong></td>
<td>Diffie-Hellman key agreement protocol (also called exponential key agreement) was developed by Diffie and Hellman in 1976 and published in the ground-breaking paper “New Directions in Cryptography”. The protocol allows two users to exchange a secret key over an insecure medium without any prior secrets.</td>
</tr>
<tr>
<td><strong>DSA</strong></td>
<td>The Digital Signature Algorithm (DSA) was published by NIST in the Digital Signature Standard (DSS).</td>
</tr>
<tr>
<td><strong>ECC</strong></td>
<td>Elliptic Curve Cryptography. A potentially faster and more secure replacement for prime field based asymmetric algorithms such as RSA and Diffie-Hellman.</td>
</tr>
<tr>
<td><strong>ECDH</strong></td>
<td>Elliptic Curve Diffie-Hellman.</td>
</tr>
<tr>
<td><strong>ECDSA</strong></td>
<td>Elliptic Curve Digital Signature Algorithm.</td>
</tr>
<tr>
<td><strong>ICC</strong></td>
<td>IBM Crypto for C-language.</td>
</tr>
<tr>
<td><strong>MD2</strong></td>
<td>MD2, MD4, and MD5 are message-digest algorithms developed by Rivest. They are meant for digital signature applications where a large message has to be &quot;compressed&quot; in a secure manner before being signed with the private key. All three algorithms take a message of arbitrary length and produce a 128-bit message digest. While the structures of these algorithms are somewhat similar, the design of MD2 is quite different from that of MD4 and MD5 and MD2 was optimized for 8-bit machines, whereas MD4 and MD5 were aimed at 32-bit machines. Description and source code for the three algorithms can be found as Internet RFCs 1319 - 1321.</td>
</tr>
<tr>
<td><strong>MDC2</strong></td>
<td>A seldom used hash algorithm developed by IBM.</td>
</tr>
<tr>
<td><strong>NIST</strong></td>
<td>(The) National Institute of Standards and Technology; NIST is a non-regulatory federal agency within the U.S. Commerce Department's Technology Administration. NIST’s mission is to develop and promote measurement, standards, and technology to enhance productivity, facilitate trade, and improve the quality of life. NIST oversees the Cryptographic Module Validation Program.</td>
</tr>
<tr>
<td><strong>OpenSSL</strong></td>
<td>A collaborative effort to develop a robust, commercial-grade, full-featured and Open Source toolkit implementing the Secure Socket Layer (SSL) and Transport Layer Security (TLS) protocols.</td>
</tr>
<tr>
<td><strong>PKCS#1</strong></td>
<td>A standard that describes a method for encrypting data using the RSA public-key crypto system.</td>
</tr>
<tr>
<td><strong>PRNG</strong></td>
<td>Pseudo-Random number generator. Essentially a sequence generator which, if the internal state is unknown, is unpredictable and has good distribution characteristics.</td>
</tr>
<tr>
<td>Algorithm</td>
<td>Description</td>
</tr>
<tr>
<td>-----------</td>
<td>-------------</td>
</tr>
<tr>
<td>RC2</td>
<td>A variable key-size block cipher designed by Rivest for RSA Data Security. &quot;RC&quot; stands for &quot;Ron's Code&quot; or &quot;Rivest's Cipher.&quot; It is faster than DES and is designed as a &quot;drop-in&quot; replacement for DES. It can be made more secure or less secure than DES against exhaustive key search by using appropriate key sizes. It has a block size of 64 bits and is about two to three times faster than DES in software. The algorithm is confidential and proprietary to RSA Data Security. RC2 can be used in the same modes as DES.</td>
</tr>
<tr>
<td>RC4</td>
<td>A stream cipher designed by Rivest for RSA Data Security. It is a variable key-size stream cipher with byte-oriented operations.</td>
</tr>
<tr>
<td>RSA</td>
<td>A public-key cryptosystem for both encryption and authentication; it was invented in 1977 by Ron Rivest, Adi Shamir, and Leonard Adleman.</td>
</tr>
<tr>
<td>SHA-1</td>
<td>The Secure Hash Algorithm, the algorithm specified in the Secure Hash Standard (SHS), was developed by NIST and published as a federal information processing standard. SHA-1 was a revision to SHA that was published in 1994. The revision corrected an unpublished flaw in SHA.</td>
</tr>
<tr>
<td>SHA-2</td>
<td>A set of hash algorithms intended as an upgrade to SHA-1. It includes SHA-224, SHA-256, SHA-384, SHA-512, SHA-512/224, SHA-512/256.</td>
</tr>
<tr>
<td>SHA-3</td>
<td>A set of hash algorithms, including SHA3-224, SHA3-256, SHA3-384, SHA3-512, SHAKE128 and SHAKE256.</td>
</tr>
<tr>
<td>Triple-DES</td>
<td>Based on the DES standard; the plaintext is, in effect, encrypted three times. Triple-DES (TDEA), as specified in SP 800-67, is recognized as a FIPS approved algorithm.</td>
</tr>
<tr>
<td>TRNG</td>
<td>True Random Number Generator. A random number generator using an entropy source. May have worse distribution characteristics than a PRNG, but its output cannot be predicted even with knowledge of its previous state.</td>
</tr>
</tbody>
</table>
2 Introduction

This document is a non-proprietary FIPS 140-2 Security Policy for the IBM Crypto for C versions 8.6.0.0 and 8.6.1.0 (ICC) cryptographic module. It contains a specification of the rules under which the module must operate and describes how this module meets the requirements as specified in FIPS PUB 140-2 (Federal Information Processing Standards Publication 140-2) for a security level 1 multi-chip standalone software module.

The table below shows the security level claimed for each of the eleven sections that comprise the FIPS 140-2 standard.

<table>
<thead>
<tr>
<th>FIPS 140-2 Section</th>
<th>Security Level</th>
</tr>
</thead>
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<tr>
<td>1 Cryptographic Module Specification</td>
<td>1</td>
</tr>
<tr>
<td>2 Cryptographic Module Ports and Interfaces</td>
<td>1</td>
</tr>
<tr>
<td>3 Roles, Services and Authentication</td>
<td>1</td>
</tr>
<tr>
<td>4 Finite State Model</td>
<td>1</td>
</tr>
<tr>
<td>5 Physical Security</td>
<td>N/A</td>
</tr>
<tr>
<td>6 Operational Environment</td>
<td>1</td>
</tr>
<tr>
<td>7 Cryptographic Key Management</td>
<td>1</td>
</tr>
<tr>
<td>8 EMI/EMC</td>
<td>1</td>
</tr>
<tr>
<td>9 Self Tests</td>
<td>1</td>
</tr>
<tr>
<td>10 Design Assurance</td>
<td>1</td>
</tr>
<tr>
<td>11 Mitigation of Other Attacks</td>
<td>N/A</td>
</tr>
</tbody>
</table>

This document is intended to be part of the package of documents that are submitted for FIPS validation. It is intended for the following people:

- Developers working on the release
- Product Verification
- Documentation
- Product and Development Managers
3 Cryptographic Module Definition

The ICC cryptographic module is implemented in the C programming language. It is packaged as a dynamic (shared) library usable by applications written in a language that supports C language linking conventions (e.g., C, C++, Java, Assembler, etc.) for use on commercially available operating systems. The ICC allows these applications to access cryptographic functions using an Application Programming Interface (API) provided through an ICC import library and based on the API defined by the OpenSSL group.

The software provided to the customer consists of:

- **ICC static stub**: static library (object code) that is linked into the customer’s application and communicates with the Crypto Module. It includes the C headers (source code) containing the API prototypes and other definitions needed for linking the static library. This static library is not part of the cryptographic module.

- **ICC shared library** (libicclib84.dll for Windows, libicclib084.so for the rest): shared library (executable code) containing proprietary code needed to meet FIPS and functional requirements not provided by OpenSSL (e.g., TRNG, PRNG, self-tests, startup/shutdown), the OpenSSL cryptographic library and the zlib used for NRBG entropy estimation. This shared library constitutes the cryptographic module.

- **ICCSIG.txt file**: contains the signature file used for integrity tests. This file is not part of the cryptographic module (that is, it is not within the logical boundary).

The cryptographic module takes advantage of the hardware cryptographic accelerator features supported by the testing platforms that are part of the operational environment, as shown in the following table.

<table>
<thead>
<tr>
<th>Processor</th>
<th>Processor Algorithm Acceleration (PAA) functions utilized by the module</th>
<th>Algorithms affected in the Cryptographic Module</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel® Xeon®</td>
<td>AES-NI</td>
<td>AES</td>
</tr>
<tr>
<td>SPARC T4</td>
<td>SPARC</td>
<td>SHA-1, SHA-2, AES</td>
</tr>
</tbody>
</table>

*Table 1 - Processor Algorithm Acceleration (PAA) functions*

The following table presents the operational environments on which versions 8.6.0.0 and 8.6.1.0 of the cryptographic module were tested and validated. Each operational environment includes the hardware platform, the processor and the operating system. Each row of the table also includes the corresponding version of the module.
Table 2 - Tested platforms

As outlined in G.5 of the Implementation Guidance for FIPS 140-2 [FIPS140-2-IG], the module maintains its compliance on other operating systems, provided that:

- the operating system meets the operational environment requirements at the module’s level of validation, and runs in a single-user mode;
- the module does not require modification to run in the new environment.

CMVP makes no statement as to the correct operation of the module or the security strengths of the generated keys when so ported if the specific operational environment is not listed on the validation certificate.

### 3.1 Cryptographic Module Boundary

The relationship between ICC and IBM applications is shown in the following diagram. ICC comprises a static stub linked into the IBM application which binds the API functions with the shared library containing the cryptographic functionality.
- **IBM Application** - The IBM application using ICC. This contains the application code, and the ICC static stub.

- **IBM Application code** - The program using ICC to perform cryptographic functions.

- **ICC Static stub** - Linked into the calling application to bind the API with the implementation of the cryptographic services in the shared library.

- **ICC shared library** - This contains proprietary code needed to meet FIPS requirements and cryptographic services not provided by OpenSSL, a statically linked copy of zlib used for TRNG entropy estimation, and a statically linked copy of the OpenSSL cryptographic library.
The logical boundary of the cryptographic module consists of the ICC shared library bounded by the dashed red line in the figure. The signature used for the integrity check of the ICC during its initialization is contained in the file ICCSIG.txt (not shown in the figure). This file is not considered within the logical boundary.

The physical boundary of the cryptographic module is defined to be the enclosure of the computer that runs the ICC software.
4 FIPS 140-2 Specifications

4.1 Ports and Interfaces

The ICC meets the requirements of a multi-chip standalone module. Since the ICC is a software module, its interfaces are defined in terms of the API that it provides:

- Data Input Interface is defined as the input data parameters of those API functions that accept, as their arguments, data to be used or processed by the module.
- The return value or arguments of appropriate types, data generated or otherwise processed by the API functions to the caller constitute Data Output Interface.
- Control Input Interface is comprised by the API function ICC_Init (used to initiate the context handle of the module), the API function ICC_Attach (used to bind the entry point of the API functions with their implementation in the shared library), the API functions used to control the operation of the module, and configuration parameters and environment variables used to provide alternative values before the module has been initialized.
- Status Output Interface is defined as the API function ICC_GetStatus that provides information about the status of the module. The function may be called once the context of the module has been obtained.

4.2 Roles, Services and Authentication

4.2.1 Roles and Authentication

The ICC assumes the following two roles: Crypto-Officer role and User role (there is no Maintenance Role). The Operating System (OS) provides functionality to require any user to be successfully authenticated prior to using any system services. However, the Module does not support user identification or authentication that would allow for distinguishing users between the two supported roles. Only a single operator assuming a particular role may operate the Module at any particular moment in time. The OS authentication mechanism must be enabled to ensure that none of the Module’s services are available to users who do not assume an authorized role.

The Module does not identify nor authenticate any user (in any role) that is accessing the Module. The roles are implicitly assumed by the services that are requested as follows:

- **Crypto Officer** - any entity that can install and initialize the Module.
- **User** - any entity that can access services implemented in the Module as listed in Table 3 and Table 4.

4.2.2 Services and Cryptographic Algorithms

An operator is implicitly assumed in the User or Crypto-Officer role based upon the
operations chosen. If an operator installs and/or initializes the Module, then the operator is in the Crypto-Officer role. Otherwise, the operator is in the User role.

The module enters the FIPS mode of operation (the approved mode) implicitly after initialization. Once operational, the module assumes implicitly the mode of operation depending on the services requested.

The following table shows the services and algorithms allowed in FIPS mode of operation. Requesting these services will put the module in FIPS mode of operation.

<table>
<thead>
<tr>
<th>Service</th>
<th>Algorithms</th>
<th>Role</th>
<th>Keys, CSPs and access</th>
</tr>
</thead>
<tbody>
<tr>
<td>Symmetric encryption and decryption</td>
<td>AES</td>
<td>User</td>
<td>AES key</td>
</tr>
<tr>
<td></td>
<td>Triple-DES</td>
<td>User</td>
<td>Triple-DES key</td>
</tr>
<tr>
<td>DSA signature verification</td>
<td>DSA</td>
<td>User</td>
<td>DSA public key</td>
</tr>
<tr>
<td>ECDSA Key Pair Generation</td>
<td>ECDSA, DRBG</td>
<td>User</td>
<td>ECDSA public and private keys</td>
</tr>
<tr>
<td>ECDSA public key validation</td>
<td>ECDSA</td>
<td>User</td>
<td>ECDSA key material</td>
</tr>
<tr>
<td>ECDSA Signature Generation and Verification</td>
<td>ECDSA</td>
<td>User</td>
<td>ECDSA private and public keys</td>
</tr>
<tr>
<td>RSA Key Generation</td>
<td>RSA, DRBG</td>
<td>User</td>
<td>RSA public and private keys</td>
</tr>
<tr>
<td>RSA Signature Generation and Verification</td>
<td>RSA</td>
<td>User</td>
<td>RSA public and private keys</td>
</tr>
<tr>
<td>Key wrapping</td>
<td>AES</td>
<td>User</td>
<td>AES key</td>
</tr>
<tr>
<td>Key encapsulation</td>
<td>RSA</td>
<td>User</td>
<td>RSA public and private keys</td>
</tr>
<tr>
<td>Diffie-Hellman Key Agreement</td>
<td>KAS FFC</td>
<td>User</td>
<td>Diffie-Hellman domain parameters</td>
</tr>
<tr>
<td>EC Diffie-Hellman Key Agreement</td>
<td>KAS ECC, ECC CDH primitive</td>
<td>User</td>
<td>EC Diffie-Hellman public and private keys</td>
</tr>
<tr>
<td>Message digest</td>
<td>SHA-1, SHA-224, SHA-256, SHA-384, SHA-512</td>
<td>User</td>
<td></td>
</tr>
<tr>
<td></td>
<td>SHA3-224, SHA3-256, SHA3-384, SHA3-512</td>
<td>User</td>
<td></td>
</tr>
<tr>
<td>Message authentication code (MAC)</td>
<td>HMAC with SHA-1, SHA-224, SHA-256, SHA-384, SHA-512</td>
<td>User</td>
<td>HMAC key</td>
</tr>
<tr>
<td></td>
<td>HMAC with SHA3-224, SHA3-256, SHA3-384, SHA3-512</td>
<td>User</td>
<td>HMAC key</td>
</tr>
<tr>
<td></td>
<td>CMAC with AES</td>
<td>User</td>
<td>AES key</td>
</tr>
</tbody>
</table>

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<table>
<thead>
<tr>
<th>Service</th>
<th>Algorithms</th>
<th>Role</th>
<th>Keys, CSPs and access</th>
</tr>
</thead>
<tbody>
<tr>
<td>Random number generation</td>
<td>CMAC with Triple-DES SP800-90A DRBG</td>
<td>User</td>
<td>Triple-DES key</td>
</tr>
<tr>
<td>Get Status</td>
<td>n/a</td>
<td>User</td>
<td>Entropy input string, Internal state</td>
</tr>
<tr>
<td>On demand self-tests</td>
<td>n/a</td>
<td>User</td>
<td></td>
</tr>
<tr>
<td>Zeroization</td>
<td>n/a</td>
<td>User</td>
<td></td>
</tr>
<tr>
<td>Module installation and configuration</td>
<td>n/a</td>
<td>Crypto Officer</td>
<td></td>
</tr>
</tbody>
</table>

Table 3 – Services in FIPS mode of operation

The following table shows the services and algorithms not allowed in FIPS mode of operation. Requesting these services will implicitly put the module in non-FIPS mode of operation.

In addition, requesting any of these services using a processor capability mask not allowed by this security policy, as specified in item 4 of section 4.3.2, will also put the module in non-FIPS mode of operation.

<table>
<thead>
<tr>
<th>Service</th>
<th>Algorithms</th>
<th>Role</th>
<th>Keys, CSPs and access</th>
</tr>
</thead>
<tbody>
<tr>
<td>Symmetric encryption and decryption</td>
<td>DES, CAST, Camellia, Blowfish, RC4, RC2</td>
<td>User</td>
<td>Symmetric key</td>
</tr>
<tr>
<td></td>
<td>2-key Triple-DES</td>
<td>User</td>
<td>Triple-DES key</td>
</tr>
<tr>
<td>DSA Key/Parameter Generation</td>
<td>DSA</td>
<td>User</td>
<td>DSA public and private keys</td>
</tr>
<tr>
<td>DSA Signature Generation</td>
<td>DSA</td>
<td>User</td>
<td>DSA private key</td>
</tr>
<tr>
<td>DSA Signature Verification</td>
<td>DSA using keys disallowed by [SP800-131A]</td>
<td>User</td>
<td>DSA public key</td>
</tr>
<tr>
<td>ECDSA Key Pair Generation</td>
<td>ECDSA using keys disallowed by [SP800-131A]</td>
<td>User</td>
<td>ECDSA private and public keys</td>
</tr>
<tr>
<td>ECDSA Signature Generation</td>
<td>ECDSA using keys or SHS disallowed by [SP800-131A]</td>
<td>User</td>
<td>ECDSA private key</td>
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<tr>
<td>RSA Key Generation</td>
<td>RSA using keys disallowed by [SP800-131A]</td>
<td>User</td>
<td>RSA public and private keys</td>
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<tr>
<td>RSA Signature Generation and Verification</td>
<td>RSA using keys or SHS disallowed by [SP800-131A]</td>
<td>User</td>
<td>RSA public and private keys</td>
</tr>
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</table>
The following table shows the FIPS approved cryptographic algorithms with their associated modes, key sizes and usage in cryptographic services. Each algorithm specifies the CAVS certificate for each of the operational environments (OE) on which it was tested (please refer to Table 2 for a description of the corresponding OE id) and whether the module was set or unset to take advantage of the processor algorithm acceleration (PAA) capabilities.

<table>
<thead>
<tr>
<th>CAVP Cert#</th>
<th>Algorithm</th>
<th>Standard</th>
<th>Mode / Method</th>
<th>Key size</th>
<th>Use</th>
</tr>
</thead>
</table>
| Tested on OE 1:  
  • PAA: #4732  
  • no PAA: #4733 | AES       | [FIPS197], [SP800-38A] | ECB, CBC, OFB, CFB1, CFB8, CFB128, CTR | 128, 192 and 256 bits | Data Encryption and Decryption |
| Tested on OE 2:  
  • PAA: #4734  
  • no PAA: #4736 |          | [SP800-38B] | CMAC         | 128, 192 and 256 bits | MAC Generation and Verification |
| Tested on OE 3:  
  • PAA: #4730  
  • no PAA: #4731 |          | [SP800-38C] | CCM          | 128, 192 and 256 bits | Data Encryption and Decryption |
|                | [SP800-38E] | XTS      | 128 and 256 bits | Data Encryption and Decryption for Data Storage |
|                | [SP800-38D] | GCM      | 128, 192 and 256 bits | Data Encryption and Decryption |

Table 4 - Services in non-FIPS mode of operation
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<tr>
<th><strong>CAVP Cert#</strong></th>
<th><strong>Algorithm</strong></th>
<th><strong>Standard</strong></th>
<th><strong>Mode / Method</strong></th>
<th><strong>Key size</strong></th>
<th><strong>Use</strong></th>
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<td>• PAA: #4737</td>
<td>Triple-DES</td>
<td>[SP800-38F]</td>
<td>KW</td>
<td>128, 192 and 256 bits</td>
<td>Key Wrapping and Unwrapping</td>
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<td>• no PAA: #4738</td>
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<td>• PAA: #2514</td>
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<td>[SP800-67]</td>
<td>CBC, ECB, CFB64, OFB</td>
<td>192 bits (168 bits are key bits and the rest are parity bits)</td>
<td>Data Encryption and Decryption</td>
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<td>• PAA: #2516</td>
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<td>CMAC</td>
<td>192 bits (168 bits are key bits and the rest are parity bits)</td>
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<td>• PAA: #1264</td>
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<td>SHA-1, SHA-224, SHA-256, SHA-384, SHA-512</td>
<td>L=1024, N=160 L=2048, N=224 L=2048, N=256 L=3072, N=256</td>
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<td>Key Pair Generation</td>
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<td>Mode / Method</td>
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<td>Mode / Method</td>
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<td>Digital Signature Generation</td>
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<td>EC Diffie-Hellman Key Agreement</td>
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<th>Mode / Method</th>
<th>Key size</th>
<th>Use</th>
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<td>[FIPS202]</td>
<td>SHA3-224, SHA3-256, SHA3-384, SHA3-512</td>
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The following algorithms are non-approved but allowed algorithms in FIPS mode of operation.

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<th>Mode / Method</th>
<th>Key size</th>
<th>Use</th>
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<td>Message authentication code</td>
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<td>[FIPS198-1]</td>
<td>SHA-1, SHA-224, SHA-256, SHA-384, SHA-512</td>
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<td>SHA-1, SHA-224, SHA-256, SHA-384, SHA-512</td>
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<td>[FIPS198-1]</td>
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<td>Tested on OE 2:</td>
<td>DRBG</td>
<td>[SP800-90A]</td>
<td>HMAC_DRBG HMAC using SHA-1, SHA-224, SHA-256, SHA-384, SHA-512 with/without PR</td>
<td>n/a</td>
<td>Random Number Generation</td>
</tr>
<tr>
<td>Tested on OE 3:</td>
<td>DRBG</td>
<td>[SP800-90A]</td>
<td>HMAC_DRBG HMAC using SHA-1, SHA-224, SHA-256, SHA-384, SHA-512 with/without PR</td>
<td>n/a</td>
<td>Random Number Generation</td>
</tr>
<tr>
<td>Tested on OE 4:</td>
<td>DRBG</td>
<td>[SP800-90A]</td>
<td>HMAC_DRBG HMAC using SHA-1, SHA-224, SHA-256, SHA-384, SHA-512 with/without PR</td>
<td>n/a</td>
<td>Random Number Generation</td>
</tr>
</tbody>
</table>

Table 5 - Approved Algorithms in FIPS mode of operation
<table>
<thead>
<tr>
<th>Algorithm</th>
<th>Key size</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>RSA key encapsulation with encryption and decryption primitives</td>
<td>2048 bits or greater</td>
<td>Key transport; allowed in [FIPS140-2-IG] D.9</td>
</tr>
<tr>
<td>Diffie-Hellman</td>
<td>2048 bits or greater</td>
<td>Key agreement; allowed in [FIPS140-2-IG] D.8</td>
</tr>
<tr>
<td>EC Diffie-Hellman</td>
<td>P-224, P-256, P-384, P-521</td>
<td>Key agreement; allowed in [FIPS140-2-IG] D.8</td>
</tr>
</tbody>
</table>

*Tested on OE 1:
- PAA: [CVL#1371](#)
- no PAA: [CVL#1372](#)*

*Tested on OE 2:
- PAA: [CVL#1373](#)
- no PAA: [CVL#1374](#)*

*Tested on OE 3:
- PAA: [CVL#1369](#)
- no PAA: [CVL#1370](#)*

*Tested on OE 4:
- PAA: [CVL#1375](#)
- no PAA: [CVL#1376](#)*

<table>
<thead>
<tr>
<th>Algorithm</th>
<th>Key size</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>MD5</td>
<td></td>
<td>According to [SP800-52], MD5 is allowed to be used in TLS versions 1.0 and 1.1 as the hash function used in the PRF, as defined in [RFC2246] and [RFC4346].</td>
</tr>
<tr>
<td>SHA-1</td>
<td></td>
<td>According to [SP800-52], SHA-1 is allowed in digital signatures on ephemeral parameters in TLS.</td>
</tr>
<tr>
<td>TRNG</td>
<td></td>
<td>The module obtains entropy data from this NDRNG to seed the DRBG.</td>
</tr>
</tbody>
</table>

*Table 6 – non-approved but allowed algorithms in FIPS mode of operation*

The table below shows the non-approved algorithms; using these algorithms will implicitly put the module in non-FIPS mode of operation.

<table>
<thead>
<tr>
<th>Algorithm</th>
<th>Key size</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>AES</td>
<td></td>
<td>Use of a processor capability mask not allowed by this security policy, as specified in item 4 of section 4.3.2.</td>
</tr>
<tr>
<td>Triple-DES</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DSA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Algorithm</td>
<td>Key size</td>
<td>Comments</td>
</tr>
<tr>
<td>---------------------------</td>
<td>----------------------------------------------</td>
<td>--------------------------------------------------------------------------</td>
</tr>
<tr>
<td>ECDSA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RSA</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SHA-1, SHA-2</td>
<td></td>
<td></td>
</tr>
<tr>
<td>SHA-3</td>
<td></td>
<td></td>
</tr>
<tr>
<td>HMAC</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DRBG</td>
<td></td>
<td></td>
</tr>
<tr>
<td>DSA Key/Parameter Generation</td>
<td>L=512, N=160; L=1024, N=160; L=2048, N=224; L=2048, N=256; L=3072, N=256</td>
<td>Algorithm not tested under the CAVP</td>
</tr>
<tr>
<td>DSA Signature Generation</td>
<td>L=512, N=160; L=1024, N=160; L=2048, N=224; L=2048, N=256; L=3072, N=256</td>
<td>Algorithm not tested under the CAVP</td>
</tr>
<tr>
<td>DSA Signature Verification</td>
<td>L=512, N=160</td>
<td>Key sizes disallowed by [SP800-131A]</td>
</tr>
<tr>
<td>ECDSA Key Pair Generation</td>
<td>P-192, K-163, B-163</td>
<td>Key sizes disallowed by [SP800-131A]</td>
</tr>
<tr>
<td>ECDSA Signature Generation</td>
<td>P-192, K-163, B-163</td>
<td>Key sizes disallowed by [SP800-131A]</td>
</tr>
<tr>
<td>RSA Key Generation</td>
<td>1024 bits</td>
<td>Key sizes disallowed by [SP800-131A]</td>
</tr>
<tr>
<td>RSA Signature Generation</td>
<td>1024 bits</td>
<td>Key sizes disallowed by [SP800-131A]</td>
</tr>
<tr>
<td>RSA Key Encapsulation</td>
<td>1024 bits</td>
<td>Key sizes disallowed by [SP800-131A]</td>
</tr>
<tr>
<td>SHA-1 in Signature Generation</td>
<td></td>
<td>SHA-1 is disallowed by [SP800-131A], except when signature generation is used for ephemeral parameters in TLS as stated in Table 6.</td>
</tr>
<tr>
<td>Diffie-Hellman (DH)</td>
<td>1024 bits</td>
<td>Key sizes do not meet [SP800-131A]</td>
</tr>
<tr>
<td>EC Diffie-Hellman (ECDH)</td>
<td>P-192, K-163, B-163</td>
<td>Key sizes disallowed by [SP800-131A]</td>
</tr>
<tr>
<td>DES</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>Algorithm</td>
<td>Key size</td>
<td>Comments</td>
</tr>
<tr>
<td>---------------------------</td>
<td>----------</td>
<td>-------------------------------</td>
</tr>
<tr>
<td>CAST</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>Camellia</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>Blowfish</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>RC4</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>RC2</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>MD2</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>MD4</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>MD5</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>Password Based Encryption</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>HMAC-MD5</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>MDC2</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>RIPEMD</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>chacha20</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>chacha20-poly1305</td>
<td></td>
<td>Non-approved algorithm</td>
</tr>
<tr>
<td>Key Derivation Function (KDF)</td>
<td></td>
<td>Algorithm not tested under the CAVP</td>
</tr>
<tr>
<td>SP800-108</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Table 7 - Algorithms only allowed in non-FIPS mode of operation

4.2.3 Access Rights within Services

An operator performing a service within any role can read/write cryptographic keys and critical security parameters (CSP) only through the invocation of a service by use of the Cryptographic Module API. Each service within each role can only access the cryptographic keys and CSPs that the service’s API defines. The following cases exist:

- A cryptographic key or CSP is provided to an API as an input parameter; this indicates read/write access to that cryptographic key or CSP.
- A cryptographic key or CSP is returned from an API as a return value; this indicates read access to that cryptographic key or CSP.

The details of the access to cryptographic keys and CSPs for each service are indicated in the rightmost column of Table 3 and Table 4. The indicated access rights apply to the role that invokes the service.

4.2.4 Operational Rules and Assumptions

The following operational rules must be followed by any user of the cryptographic module:
1. The Module is to be used by a single human operator at a time and may not be actively shared among operators at any period of time.

2. The OS authentication mechanism must be enabled in order to prevent unauthorized users from being able to access system services.

3. All keys entered into the module must be verified as being legitimate and belonging to the correct entity by software running on the same machine as the module.

4. In case the module’s power is lost and then restored, the keys used for the AES GCM encryption/decryption shall be re-distributed. The GCM is used in the context of TLS version 1.2 or higher. The mechanism for IV generation is compliant with RFC 5288.

5. The AES algorithm in XTS mode can be only used for the cryptographic protection of data on storage devices, as specified in [SP800-38E]. The length of a single data unit encrypted with the XTS-AES shall not exceed $2^{20}$ AES blocks (16MB of data).

6. To meet the requirement in [FIPS140-2-IG] A.9, the module implements a check to ensure that the two AES keys used in the XTS-AES algorithm are not identical.

7. Data encryption using the same three-key Triple-DES key shall not exceed $2^{28}$ Triple-DES blocks (2GB of data), in accordance to [SP800-67] and [FIPS140-2-IG] IG A.13.

8. Since the ICC runs on a general-purpose processor all main data paths of the computer system will contain cryptographic material. The following items need to apply relative to where the ICC will execute:
   - Virtual (paged) memory must be secure (local disk or a secure network)
   - The system bus must be secure.
   - The disk drive that ICC is installed on must be in a secure environment.

9. The above rules must be upheld at all times in order to ensure continued system security and FIPS 140-2 mode compliance after initial setup of the validated configuration. If the module is removed from the above environment, it is assumed not to be operational in the validated mode until such time as it has been returned to the above environment and re-initialized by the user to the validated condition.

**NOTE:** It is the responsibility of the Crypto-Officer to configure the operating system to operate securely and ensure that only a single operator may operate the Module at any particular moment in time.

The services provided by the Module to a User are effectively delivered through the use
of appropriate API calls. When a client process attempts to load an instance of the Module into memory, the Module runs an integrity test and a number of cryptographic functionality self-tests. If all the tests pass successfully, the Module makes a transition to the “Operational” state, where the API calls can be used by the client to obtain desired cryptographic services. Otherwise, the Module enters to “Error” state and returns an error to the calling application. When the Module is in “Error” state, no services are available, and all of data input and data output except the status information are inhibited.

4.3 Operational Environment

Along with the conditions stated in section 4.2.4 (“Operational Rules and Assumptions”), the criteria below must be followed in order to achieve and maintain a FIPS 140-2 mode of operation:

4.3.1 Assumptions

None.

4.3.2 Installation and Initialization

The following steps must be performed to install and initialize the module for operating in a FIPS 140-2 compliant manner:

1. The operating system must be configured to operate securely and to prevent remote login. This is accomplished by disabling all services (within the Administrative tools) that provide remote access (e.g., – ftp, telnet, ssh, and server) and disallowing multiple operators to log in at once.

2. The operating system must be configured to allow only a single user. This is accomplished by disabling all user accounts except the administrator. This can be done through the Computer Management window of the operating system.

3. Before the module initialization, the user has a choice to configure the TRNG alternatives and the DRBG algorithm to use. This can be set using global setting ICC_TRNG’ and ‘ICC_RANDOM_GENERATOR’ respectively.

4. The use of the OpenSSL environment variable to alter the value of the processor capability (OpenSSL capability masks) is strictly prohibited. The only options allowed are either to leave the PAA capability enabled, or disable the PAA using the ICC_CAPABILITY_MASK property filled with all zeroes. Please refer to the user guidance for configuring this property.

5. The module is initialized automatically when the shared library is loaded in the calling application process space. The module executes the power-up self-tests (POST) and, if they are successful, the module enters the FIPS mode of operation. The calling application must include the following calling sequence to have access to the cryptographic services:
4.4 Cryptographic Key Management

4.4.1 Implemented Algorithms
The ICC cryptographic module supports the algorithms (and modes, as applicable) listed in section 4.2.2.

4.4.2 Random Number Generation
ICC employs a Deterministic Random Bit Generator (DRBG) based on [SP800-90A] for the creation of asymmetric keys. In addition, the module provides a Random Number Generation service to calling applications.

The default algorithm is Hash_DRBG using SHA-256, but another algorithm from the Hash_DRBG, HMAC_DRBG and CTR_DRBG algorithms (see Table 5 for the complete list) can be also configured (see section 4.3.2).

ICC allows for multiple entropy sources to instantiate and reseed the DRBG's, software derived, and where available, hardware RBG's. Entropy processing is as in draft SP800-90B, SP800-90C. The DRBG uses a True Random Number Generator (TRNG) to establish the initial state of the DRBG and to reseed the engine after a certain amount of time.

The TRNG's all extract noise from some source assumed to provide entropy, test to guarantee that entropy level of this noise source is at least 0.5 bits/bit, then HMAC compress and retest to guarantee that the output is better than 0.5 bits/bit. The minimum guaranteed entropy of the raw entropy source (i.e. 1 bit from a timer sample) is guaranteed to be least 0.5 bits per bit before and after HMAC compression.

In addition to the default TRNG, ICC offers multiple TRNG designs all providing the same 0.5 bits/bit entropy guarantee.

The DRBG seed and nonce are of the same length (440 bits each for HMAC-SHA256) and obtained from separate and independent calls to the TRNG. Since the DRBG is internalized by 440 bit of entropy data ((440+440)*0.5 = 440), the DRBG supports 256 bits of effective security strength in its output.

4.4.3 Key Generation
Key generation has a dependency on the SP800-90A DRBG. The SP800-90 DRBG is used to generate RSA, DSA, ECDSA DH and ECDH key pairs.

In FIPS mode, RSA key generation is carried out in accordance with the algorithms described in FIPS 186-4. ECDSA key generation is carried out in accordance with the
algorithms described in FIPS 186-4 and ANSI X9.62. All key generation methods implemented in this module are compliant to SP 800-133.

The ICC module provides PKCS#1v1.5 and PSS compatible algorithms for processing signatures (creating and verifying) the function of which is available as specified in the API's in this document. These algorithms are also available for encryption and decryption where it is used as PKCS#1v1.5 compatible.

In addition, there is a set of lower level interfaces for encryption and decryption where the algorithm can be used as PKCS#1v1.5 compatible but it also allows other types of padding operations to be used. See RSA encryption functions for the definition of the functions and for the list of padding modes.

4.4.4 Key Establishment
The ICC uses in FIPS mode of operation the following key establishment methodologies:

- Diffie-Hellman (DH) key agreement, providing between 112 and 256 bits of security strength.
- Elliptic Curve Diffie-Hellman (ECDH) key agreement, providing between 112 and 256 bits of security strength.
- RSA key encapsulation, providing between 112 and 256 bits of security strength, respectively.
- AES key wrapping, providing between 128 and 256 bits of security strength.

4.4.5 Key Entry and Output
Keys are entered into and output from the ICC module in electronic form through the data input and output interface (i.e. API function parameters). The ICC module does not support manual key entry or intermediate key generation key output. In addition, the ICC module does not produce key output in plaintext format outside its physical boundary.

4.4.6 Key Storage
The module does not provide any long-term key storage and no keys are ever stored on the hard disk.

The only exception is the RSA public key used for integrity test, which is stored in the crypto module and relies on the operating system for protection.

4.4.7 Key Zeroization
ICC modifies the default OpenSSL scrubbing code to zero objects instead of filling with pseudo random data and adds explicit testing for zeroization.

Key zeroization services are performed via the following API functions.
Table 8 - Key Zeroization

<table>
<thead>
<tr>
<th>Key Zeroization Services</th>
<th>API functions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Clean up memory locations used by low-level arithmetic functions</td>
<td>ICC_BN_clear_free()</td>
</tr>
<tr>
<td>Clean up symmetric cipher context</td>
<td>ICC_EVP_CIPHER_CTX_free()</td>
</tr>
<tr>
<td>Clean up RSA context</td>
<td>ICC_RSA_free()</td>
</tr>
<tr>
<td>Clean up DSA context</td>
<td>ICC_DSA_free()</td>
</tr>
<tr>
<td>Clean up Diffie-Hellman context</td>
<td>ICC_DH_free()</td>
</tr>
<tr>
<td>Clean up asymmetric key contexts</td>
<td>ICC_EVP_PKEY_free()</td>
</tr>
<tr>
<td>Clean up HMAC context</td>
<td>ICC_HMAC_CTX_free()</td>
</tr>
<tr>
<td>Clean up ECDSA and ECDH contexts</td>
<td>ICC_EC_KEY_free()</td>
</tr>
<tr>
<td>Clean up CMAC context</td>
<td>ICC_CMAC_CTX_free()</td>
</tr>
<tr>
<td>Clean up AES-GCM context</td>
<td>ICC_AES_GCM_CTX_free()</td>
</tr>
<tr>
<td>Clean up RNG context</td>
<td>ICC_RNG_CTX_free()</td>
</tr>
</tbody>
</table>

It is the calling application’s responsibility to appropriately utilize the provided zeroization methods (i.e. API functions) as listed in the table above to clean up involved cryptographic contexts before they are released.

4.5 Self-Tests

The ICC module implements a number of self-tests to check proper functioning of the module. This includes power-up self-tests (which are also callable on demand) and conditional self-tests.

Self-tests are automatically invoked by the module during power-up from the default entry point (DEP) of the shared library. The self-test can also be initiated by calling the function ICC_SelfTest, which returns the operational status of the module (after the self-tests are run) and an error code with description of the error (if applicable).

When the module is performing self-tests, no API functions are available and no data output is possible until the self-tests are successfully completed. After the power-up tests are successfully completed, the module turns to FIPS mode of operation. Requesting any services from Table 4 will implicitly put the module in the non-FIPS mode of operation.

4.5.1 Show Status

The status of the ICC module can be obtained using the Get Status service through the following API function:

- ICC_GetStatus: shows the state of the ICC module
The function can be called anytime once the module is initialized with the ICC_Init() and ICC_Attach API functions.

4.5.2 Startup Tests

The module performs self-tests automatically when it is loaded. Self-tests can also be requested on demand through the API function ICC_SelfTest.

Whenever the startup tests are initiated the module performs the following; if any of these tests fail, the module enters the error state:

- **Integrity Test**: the ICC uses an integrity test which uses a 2048-bit CAVS-validated RSA public key (PKCS#1v1.5) and SHA-256 hashing. This RSA public key is stored inside the shared library and relies on the operating system for protection.

- **Cryptographic algorithm tests**: Known Answer Tests for encryption and decryption are performed for the following FIPS approved and allowed algorithms:
  - Triple-DES – CBC
  - AES – CBC
  - AES_GCM
  - AES_CCM
  - AES_XTS

One way known answer tests are performed for the following FIPS approved algorithms:
  - SHA-1
  - SHA-256
  - SHA-512
  - SHA3-512
  - HMAC with SHA-1
  - HMAC with SHA-256
  - HMAC with SHA3-512
  - CMAC with AES

Known Answer Tests for signature generation and verification are performed on the following algorithms:
  - RSA signature generation with 2048-bit modulus
  - RSA signature verification with 2048-bit modulus
  - DSA signature verification with 2048-bit modulus
  - ECDSA signature generation with P-384
  - ECDSA signature verification with P-384
  - ECDSA signature generation with B-233
  - ECDSA signature verification with B-233

Other Known Answer Tests:
4.5.3 Conditional Tests

**Pair-wise consistency tests for public and private key generation:** the consistency of the keys is tested by the calculation and verification of a digital signature. If the digital signature cannot be verified, the test fails. Pair-wise consistency tests are performed on the following algorithms:

- ECDSA
- RSA

**Continuous RNG tests:** the module implements Continuous RNG tests as follows:

**SP800-90A DRBG**

- The SP800-90A DRBG generates a minimum of 8 bytes per request. If less than 8 bytes are requested, the rest of the bytes is discarded and the next request will generate new random data.

- The first 8 bytes of every request is compared with the last 8 bytes requested, if the bytes match an error is generated.

- For the first request made to any instantiation of a SP800-90A DRBG, two internal 8 byte cycles are performed.

- The SP800-90A DRBG performs known answer tests when first instantiated and health checks at intervals as specified in the standard.

**True Random Number Generator (TRNG)**

- A non-deterministic RNG (NDRNG) is used to seed the RNG. Every time a new seed or n bytes is required (either to initialize the RNG, reseed the RNG periodically or reseed the RNG by user’s demand), the cryptographic module performs a comparison between the SHA-256 message digest using the new seed and the previously calculated digest. If the values match, the TRNG generates a new stream of bytes until the continuous RNG test passes.

4.5.4 Severe Errors

When severe errors are detected (e.g., self-test failure or a conditional test failure) then all security related functions shall be disabled and no partial data is exposed through the data output interface. The only way to transition from the error state to an operational
state is to reinitialize the cryptographic module (from an uninitialized state). The error state can be retrieved via the Get Status service (see section 4.5.1).

4.6 Mitigation of Other Attacks

The cryptographic module is not designed to mitigate any specific attacks.
5 API Functions

The module API functions are fully described in the *IBM Crypto for C (ICC) Design Document*. The following list enumerates the API functions supported.

- ICC_EC_GROUP_set_asn1_flag
- ICC_EVP_CIPHER_CTX_flags
- ICC_EVP_CIPHER_CTX_set_flags
- ICC_GetStatus
- ICC_Init
- ICC_Attach
- ICC_Cleanup
- ICC_SelfTest
- ICC_GenerateRandomSeed
- ICC_OBJ_nid2sn
- ICC_EVP_get_digestbyname
- ICC_EVP_get_cipherbyname
- ICC_EVP_MD_CTX_new
- ICC_EVP_MD_CTX_free
- ICC_EVP_MD_CTX_init
- ICC_EVP_MD_CTX_clean
- ICC_EVP_MD_CTX_copy
- ICC_EVP_MD_type
- ICC_EVP_MD_size
- ICC_EVP_MD_block_size
- ICC_EVP_MD_CTX_md
- ICC_EVP_Digestinit
- ICC_EVP_DigestUpdate
- ICC_EVP_DigestFinal
- ICC_EVP_CIPHER_CTX_new
- ICC_EVP_CIPHER_CTX_free
- ICC_EVP_CIPHER_CTX_init
- ICC_EVP_CIPHER_CTX_cleanup
- ICC_EVP_CIPHER_CTX_set_key_length
- ICC_EVP_CIPHER_CTX_set_padding
- ICC_EVP_CIPHER_block_size
- ICC_EVP_CIPHER_key_length
- ICC_EVP_CIPHER_iv_length
- ICC_EVP_CIPHER_type
- ICC_EVP_CIPHER_CTX_cipher
- ICC_DES_random_key
- ICC_DES_set_odd_parity
- ICC_EVP_encryptInit
- ICC_EVP_encryptUpdate
- ICC_EVP_encryptFinal
- ICC_EVP_decryptInit
- ICC_EVP_decryptUpdate
- ICC_EVP_decryptFinal
- ICC_EVP_sealInit
- ICC_EVP_sealUpdate
- ICC_EVP_sealFinal
- ICC_EVP_signInit
- ICC_EVP_signUpdate
- ICC_EVP_signFinal
- ICC_EVP_verifyInit
- ICC_EVP_verifyUpdate
- ICC_EVP_verifyFinal
- ICC_EVP_ENCODE_CTX_new
- ICC_EVP_ENCODE_CTX_free
- ICC_EVP_ENCODE_CTX_init
- ICC_EVP_ENCODE_CTX_clean
- ICC_EVP_ENCODE_CTX_set_key_length
- ICC_EVP_ENCODE_CTX_set_padding
- ICC_EVP_encryptInit
- ICC_EVP_encryptUpdate
- ICC_EVP_encryptFinal
- ICC_EVP_decryptInit
- ICC_EVP_decryptUpdate
- ICC_EVP_decryptFinal
- ICC_EVP_sealInit
- ICC_EVP_sealUpdate
- ICC_EVP_sealFinal
- ICC_EVP_signInit
- ICC_EVP_signUpdate
- ICC_EVP_signFinal
- ICC_EVP_verifyInit
- ICC_EVP_verifyUpdate
- ICC_EVP_verifyFinal
- ICC_EVP_ENCODE_CTX_new
- ICC_EVP_ENCODE_CTX_free
- ICC_EVP_ENCODE_CTX_init
- ICC_EVP_ENCODE_CTX_clean
- ICC_EVP_ENCODE_CTX_set_key_length
- ICC_EVP_ENCODE_CTX_set_padding
- ICC_EVP_encryptInit
- ICC_EVP_encryptUpdate
- ICC_EVP_encryptFinal
- ICC_EVP_decryptInit
- ICC_EVP_decryptUpdate
- ICC_EVP_decryptFinal
- ICC_EVP_sealInit
- ICC_EVP_sealUpdate
- ICC_EVP_sealFinal
- ICC_EVP_signInit
- ICC_EVP_signUpdate
- ICC_EVP_signFinal
- ICC_EVP_verifyInit
- ICC_EVP_verifyUpdate
- ICC_EVP_verifyFinal
- ICC_EVP_ENCODE_CTX_new
- ICC_EVP_ENCODE_CTX_free
- ICC_EVP_ENCODE_CTX_init
- ICC_EVP_ENCODE_CTX_clean
- ICC_EVP_ENCODE_CTX_set_key_length
- ICC_EVP_ENCODE_CTX_set_padding
- ICC_EVP_encryptInit
- ICC_EVP_encryptUpdate
- ICC_EVP_encryptFinal
- ICC_EVP_decryptInit
- ICC_EVP_decryptUpdate
- ICC_EVP_decryptFinal
- ICC_EVP_sealInit
- ICC_EVP_sealUpdate
- ICC_EVP_sealFinal
- ICC_EVP_signInit
- ICC_EVP_signUpdate
- ICC_EVP_signFinal
- ICC_EVP_verifyInit
- ICC_EVP_verifyUpdate
- ICC_EVP_verifyFinal

ICC_EVP_DecodeInit
ICC_EVP_DecodeUpdate
ICC_EVP_DecodeFinal
ICC_RAND_bytes
ICC_RAND_seed
ICC_EVP_PKEY_decrypt
ICC_EVP_PKEY_encrypt
ICC_EVP_PKEY_new
ICC_EVP_PKEY_free
ICC_EVP_PKEY_size
ICC_RSA_new
ICC_RSA_generate_key
ICC_RSA_check_key
ICC_EVP_PKEY_set1_RSA
ICC_EVP_PKEY_get1_RSA
ICC_RSA_free
ICC_RSA_private_encrypt
ICC_RSA_private_decrypt
ICC_RSA_public_encrypt
ICC_RSA_public_decrypt
ICC_i2d_RSAPrivateKey
ICC_i2d_RSAPublicKey
ICC_d2i/PrivateKey
ICC_d2i/PublicKey
ICC_EVP_PKEY_set1_DH
ICC_EVP_PKEY_get1_DH
ICC_DH_new
ICC_DH_new_generate_key
ICC_DH_check
ICC_DH_free
ICC_DH_size
ICC_DH_compute_key
ICC_DH_generate_parameters
ICC_DH_get_PublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSA_dup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_DSA_dup
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
ICC_DSA_verify
ICC_DSA_size
ICC_DSA_new
ICC_DSA_free
ICC_DSA_generate_key
ICC_DSA_generate_parameters
ICC_i2d_DSAPrivateKey
ICC_d2i_DSAPrivateKey
ICC_i2d_DSAPublicKey
ICC_d2i_DSAPublicKey
ICC_id2_DHparams
ICC_d2i_DHparams
ICC_EVP_PKEY_set1_DSA
ICC_EVP_PKEY_get1_DSA
ICC_DSADup_DH
ICC_DSA_sign
IBM Crypto for C versions 8.6.0.0 and 8.6.1.0

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ICC_RSA_blinding_off
ICC_EVP_CIPHER_CTX_ctrl
ICC_RSA_size
ICC_BN_CTX_new
ICC_BN_CTX_free
ICC_BN_mod_exp
ICC_HMAC_CTX_new
ICC_HMAC_CTX_free
ICC_HMAC_Init
ICC_HMAC_Update
ICC_HMAC_Final
ICC_BN_div
ICC_d2i_DSA_PUBKEY
ICC_i2d_DSA_PUBKEY
ICC_ECDSA_SIG_new
ICC_ECDSA_SIG_free
ICC_i2d_ECDSA_SIG
ICC_d2i_ECDSA_SIG
ICC_ECDSA_sign
ICC_ECDSA_verify
ICC_ECDSA_size
ICC_EVP_PKEY_set1_EC_KEY
ICC_EVP_PKEY_get1_EC_KEY
ICC_EC_KEY_new_by_curve_name
ICC_EC_KEY_new
ICC_EC_KEY_free
ICC_EC_KEY_generate_key
ICC_EC_KEY_get0_group
ICC_EC_METHOD_get_field_type
ICC_EC_POINT_new
ICC_EC_POINT_free
ICC_EC_POINT_get_affine_coordinates_GFp
ICC_EC_POINT_set_affine_coordinates_GFp
ICC_EC_POINT_get_affine_coordinates_GF2m
ICC_EC_POINT_set_affine_coordinates_GF2m
ICC_EC_POINT_is_on_curve
ICC_EC_POINT_is_at_infinity
ICC_EC_KEY_check_key
ICC_EC_POINT_mul
ICC_EC_GROUP_get_order
ICC_EC_POINT_dup
ICC_PKCS5_pbe_set
ICC_PKCS5_pbe2_set
ICC_X509_ALGOR_free
ICC_OBJ_txt2nid
ICC_EVP_EncodeBlock
ICC_EVP_DecodeBlock
ICC_CMAC_CTX_new
ICC_CMAC_CTX_free
ICC_CMAC_CTX_ctrl
ICC_CMAC_Init
ICC_CMAC_Update
ICC_CMAC_Final
ICC_AES_GCM_CTX_new
ICC_AES_GCM_CTX_free
ICC_AES_GCM_CTX_ctrl
ICC_AES_GCM_Init
ICC_AES_GCM_EncryptUpdate
ICC_AES_GCM_DecryptUpdate  ICC_EC_GROUP_free
ICC_AES_GCM_EncryptFinal  ICC_EC_KEY_set_group
ICC_AES_GCM_DecryptFinal  ICC_EC_KEY_dup
ICC_AES_GCM_GenerateIV  ICC_SP800_38F_KW
ICC_AES_GCM_GenerateIV_NIST  ICC_SP800_108_get_KDFbyname
ICC_GHASH  ICC_SP800_108_KDF
ICC_AES_CCM_Encrypt  ICC_DSA_SIG_new
ICC_AES_CCM_Decrypt  ICC_DSA_SIG_free
ICC_get_RNGbyname  ICC_d2i_DSA_SIG
ICC_RNG_CTX_new  ICC_i2d_DSA_SIG
ICC_RNG_CTX_free  ICC_RSA_X931_derive_ex
ICC_RNG_CTX_Init  ICC_Init
ICC_RNG_Generate  ICC_lib_init
ICC_RNG_ReSeed  ICC_lib_cleanup
ICC_RNG_CTX_ctrl  ICC_MemCheck_start
ICC_RSA_sign  ICC_MemCheck_stop
ICC_RSA_verify  ICC_EC_GROUP_set_asn1_flag
ICC_EC_GROUP_get_degree  ICC_OPENSSL_cpuid_override
ICC_EC_GROUP_get_curve_GFp  ICC_OPENSSL_cpuid
ICC_EC_GROUP_get_curve_GF2m  ICC_EVP_CIPHER_CTX_flags
ICC_EC_GROUP_get0_generator  ICC_EVP_CIPHER_CTX_set_flags
ICC_i2o_ECPublicKey  ICC_OPENSSL_HW_rand
ICC_o2i_ECPublicKey  ICC_OPENSSL_rdtscX
ICC_BN_cmp  ICC_EVP_CIPHER_CTX_copy
ICC_BN_add  ICC_BN_is_prime_fasttest_ex
ICC_BN_sub  ICC_BN_mod_mul
ICC_EVP_PKCS82PKCS8  ICC_EVP_PKEY2PKCS8
ICC_PKCS8_PRIV_KEY_INFO_free  ICC_PKCS8_PRIV_KEY_INFO
ICC_d2i_PKCS8_PRIV_KEY_INFO  ICC_i2d_PKCS8_PRIV_KEY_INFO
ICC_i2d_ECPKParameters  ICC_d2i_ECPKParameters
ICC_i2d_ECPKParameters