

# Amazon Linux 2 Kernel Crypto API Cryptographic Module

**Software Module Version: 1.0** 

# FIPS 140-2 Non-Proprietary Security Policy

**Document Version: 1.3** 

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### **1** Introduction

This document is the non-proprietary FIPS 140-2 Security Policy for version 1.0 of the Amazon Linux 2 Kernel Crypto API Cryptographic Module. It contains the security rules under which the module must be operated and describes how this module meets the requirements as specified in FIPS 140-2 (Federal Information Processing Standards Publication 140-2) for a Security Level 1 module.

#### **1.1** Purpose of the Security Policy

There are three major reasons that a security policy is needed:

- It is required for FIPS 140 2 validation,
- It allows individuals and organizations to determine whether a cryptographic module, as implemented, satisfies the stated security policy, and
- It describes the capabilities, protection and access rights provided by the cryptographic module, allowing individuals and organizations to determine whether it will meet their security requirements.

#### **1.2 Target Audience**

This document is part of the package of documents that are submitted for FIPS 140 2 conformance validation of the module. It is intended for the following audience:

- Developers.
- FIPS 140-2 testing lab.
- The Cryptographic Module Validation Program (CMVP).
- Customers using or considering integration of Amazon Linux 2 Kernel Crypto API Cryptographic Module.

#### 1.3 How this Security Policy was Prepared

The vendor has provided the non-proprietary Security Policy of the cryptographic module, which was further consolidated into this document by atsec information security together with other vendor-supplied documentation. In preparing the Security Policy document, the laboratory formatted the vendor-supplied documentation for consolidation without altering the technical statements therein contained. The further refining of the Security Policy document was conducted iteratively throughout the conformance testing, wherein the Security Policy was submitted to the vendor, who would then edit, modify, and add technical contents. The vendor would also supply additional documentation, which the laboratory formatted into the existing Security Policy, and resubmitted to the vendor for their final editing.

## 2 Cryptographic Module Specification

### 2.1 Module Overview

The Amazon Linux 2 Kernel Crypto API Cryptographic Module (hereafter referred to as the "module") is a software module supporting FIPS 140-2 Approved cryptographic algorithms. The module provides a C language application program interface (API) for use by other processes that require cryptographic functionality.

#### 2.2 FIPS 140-2 Validation Scope

Table 1 shows the security level claimed for each of the eleven sections that comprise the FIPS 140-2 standard.

|                                | Level                                     |     |
|--------------------------------|---|-----|
| 1                              | Cryptographic Module Specification        | 1   |
| 2                              | Cryptographic Module Ports and Interfaces | 1   |
| 3                              | Roles and Services and Authentication     | 1   |
| 4                              | Finite State Machine Model                | 1   |
| 5                              | Physical Security                         | N/A |
| 6                              | Operational Environment                   | 1   |
| 7                              | Cryptographic Key Management              | 1   |
| 8                              | EMI/EMC                                   | 1   |
| 9                              | Self-Tests                                | 1   |
| 10                             | Design Assurance                          | 1   |
| 11 Mitigation of Other Attacks |   | N/A |
| Over                           | 1   |     |

Table 1: FIPS 140-2 Security Requirements.

### 2.3 Definition of the Cryptographic Module

The Amazon Linux 2 Kernel Crypto API Cryptographic Module is defined as a Multi-chip Standalone module per the requirements within FIPS 140-2. The logical cryptographic boundary of the module consists of the static kernel binary, the sha512hmac application, their respective integrity HMAC files, and the kernel loadable components, which are delivered through the Amazon Linux 2 yum core repositories (ID amz2-core/2/x86\_64 and amz2-core/2/aarch64) from the RPM file with version kernel-4.14.146-119.123.amzn2.x86\_64 for the x86\_64 platform and kernel-4.14.146-119.123.amzn2.aarch64 for the aarch64 platform:

- kernel loadable components: x86\_64: /lib/modules/4.14.146-119.123.amzn2.x86\_64/kernel/crypto/\*.ko aarch64: /lib/modules/4.14.146-120.123.amzn2.aarch64 /kernel/crypto/\*.ko
- kernel loadable components: x86\_64: /lib/modules/4.14.146-119.123.amzn2.x86\_64/kernel/arch/x86/crypto/\*.ko aarch64: /lib/modules/4.14.146-120.123.amzn2.aarch64/kernel/arch/x86/crypto/\*.ko

- static kernel binary (vmlinuz): x86\_64: /boot/vmlinuz-4.14.146-119.123.amzn2.x86\_64 aarch64: /boot/vmlinuz-4.14.146-120.123.amzn2.aarch64
- static kernel binary (vmlinuz) HMAC file: x86\_64: /boot/.vmlinuz-4.14.146-119.123.amzn2.x86\_64.hmac aarch64: /boot/vmlinuz-4.14.146-120.123.amzn2.aarch64.hmac
- sha512hmac binary file for performing the integrity checks: usr/bin/sha512hmac
- sha512hmac binary HMAC file: /usr/lib64/hmaccalc/sha512hmac.hmac

The module instantiation is provided by the dracut-fips RPM package with the file versions:

x86\_x64:

dracut-fips-033-535.amzn2.1.3.x86\_64.rpm

aarch64:

dracut-fips-033-535.amzn2.1.3.aarch64.rpm

The AES-NI configuration of the kernel is provided by the dracut-fips RPM package with the file versions:

x86\_64: dracut-fips-aesni-033-535.amzn2.1.3.x86\_64.rpm

aarch64:

dracut-fips-aesni-033-535.amzn2.1.3.aarch64.rpm

The module shall be instantiated by the dracut-fips package with the RPM file version specified above. The dracut-fips RPM package is only used for the installation and instantiation of the module. This code is not active when the module is operational and does not provide any services to users interacting with the module. Therefore, the dracut-fips RPM package is outside the module's logical boundary.

The bound module Amazon Linux 2 NSS Cryptographic Module with FIPS 140-2 Certificate #4565 (hereafter referred to as the "NSS bound module" or "NSS module") is also required for the Kernel Crypto API to operate. The NSS bound module provides the HMAC-SHA2-512 algorithm used by the sha512hmac binary file to verify the integrity of both the sha512hmac file and the vmlinuz (static kernel binary). This HMAC-SHA2-512 algorithm from the bound module is otherwise not exposed to a user of the Amazon Linux 2 Kernel Crypto API Cryptographic Module.

Figure 1 shows the logical block diagram of the module executing in memory on the host system.



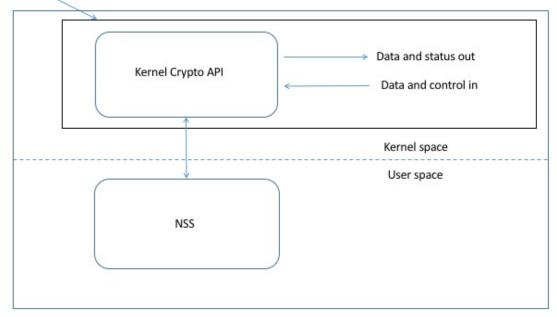


Figure 1: Logical cryptographic boundary.

### 2.4 Definition of the Physical Cryptographic Boundary

The physical cryptographic boundary of the module is defined as the hard enclosure of the host system on which the module runs. Figure 2 depicts the hardware block diagram. The physical hard enclosure is indicated by the dashed colored line. No components are excluded from the requirements of FIPS 140-2.

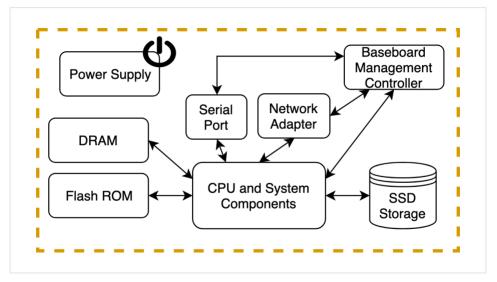


Figure 2: Hardware block diagram.

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### **2.5 Tested Environments**

The module was tested on the environments/platforms listed in Table 2. The tested operational environment is not a virtualized cloud service, and was controlled such that the laboratory had full and exclusive access to the environment and module during the testing procedures.

| Operating<br>System | Processor   | Hardware  |  |  |
|---------------------|---|---|--|--|
| Amazon Linux 2      | Intel Xeon E5-2686 x86_64<br>bits with PAA                                      | Amazon EC2 i3.metal<br>512 GiB system memory<br>13.6 TiB SSD storage + 8 GiB SSD boot disk<br>25 Gbps Elastic Network Adapter |  |  |
| Amazon Linux 2      | Intel Xeon E5-2686 x86_64<br>bits without PAA                                   | Amazon EC2 i3.metal<br>512 GiB system memory<br>13.6 TiB SSD storage + 8 GiB SSD boot disk<br>25 Gbps Elastic Network Adapter |  |  |
| Amazon Linux 2      | Graviton 2 aarch64 bit with<br>PAA (i.e., Neon and Crypto<br>Extension (EC))    | Amazon EC2 c6g.metal<br>128 GiB system memory<br>200 GiB SSD storage<br>25 Gbps Elastic Network Adapter                       |  |  |
| Amazon Linux 2      | Graviton 2 aarch64 bit<br>without PAA (i.e., Neon and<br>Crypto Extension (EC)) | Amazon EC2 c6g.metal<br>128 GiB system memory<br>200 GiB SSD storage<br>25 Gbps Elastic Network Adapter                       |  |  |

### 2.6 Modes of Operation

The module supports two modes of operation.

- In "**FIPS mode**" (the Approved mode of operation), only approved or allowed security functions with sufficient security strength are offered by the module.
- In "**non-FIPS mode**" (the non-Approved mode of operation), non-approved security functions are offered by the module.

The module enters the operational mode after Power-On Self-Tests (POST) succeed. Once the module is operational, the mode of operation is implicitly assumed depending on the security function invoked and the security strength<sup>1</sup> of the cryptographic keys chosen for the service.

If the POST or the Conditional Tests fail (Section 9), the module goes into the error state. The status of the module can be determined by the availability of the module. If the module is available, then it had passed all self-tests. If the module is unavailable, it is because any self-test failed, and the module has transitioned to the error state.

Keys and Critical Security Parameters (CSPs) used or stored in FIPS mode shall not be used in non-FIPS mode, and vice versa.

<sup>1</sup> See Section 5.6.1 in [SP800-57] for a definition of "security strength".

### **3 Module Ports and Interfaces**

As a Software module, the module does not have physical ports. The operator can only interact with the module through the API provided by the module. Thus, the physical ports within the physical boundary are interpreted to be the physical ports of the hardware platform on which the module runs and are directed through the logical interfaces provided by the software.

The logical interfaces are the API through which applications request services and receive output data through return values or modified data referenced by pointers. Table 3 summarizes the logical interfaces.

| Logical Interface | Description   |  |  |  |  |
|-------------------|---|--|--|--|--|
| Data Input        | API input parameters from kernel system calls, AF_ALG type socket.  |  |  |  |  |
| Data Output       | API output parameters from kernel system calls, AF_ALG type socket.   |  |  |  |  |
| Control Input     | API function calls, API input parameters for control from kernel system calls, AF_ALG type socket, kernel command line. |  |  |  |  |
| Status Output     | API return codes, AF_ALG type socket, kernel logs.  |  |  |  |  |
| Power Input       | N/A   |  |  |  |  |

## 4 Roles, Services and Authentication

### 4.1 Roles

The module supports the following roles:

- User role: performs all services (in both FIPS mode and non-FIPS mode of operation), except module installation and configuration. This role is assumed by the calling application accessing the module.
- **Crypto Officer (CO) role**: performs module installation and configuration.

The User and Crypto Officer roles are implicitly assumed depending on the service requested.

### 4.2 Services

The module provides services to calling applications that assume the user role, and human users assuming the Crypto Officer role. Table 4 and Table 5 depict all services, which are described with more detail in the user documentation.

The tables use the following convention when specifying the access permissions that the service has for each CSP or key.

- Create (C): the service can create a new CSP.
- **Read (R)**: the service can read the CSP.
- Update (U): the service can write a new value to the CSP.
- Zeroize (Z): the service can zeroize the CSP.
- **N/A**: the service does not access any CSP or key during its operation.

For the "Role" column, U indicates the User role, and CO indicates the Crypto Officer role. An X marks which role has access to that service.

Note: The bound NSS module does not offer any service to a user of the Amazon Linux 2 Kernel Crypto API Cryptographic Module.

#### 4.2.1 Services in the FIPS-Approved Mode of Operation

Table 4 provides a full description of FIPS Approved services and the non-Approved but Allowed services provided by the module in the FIPS-approved mode of operation and lists the roles allowed to invoke each service.

| Service                                  | Service Description and  | Role |    | Keys and CSPs              | Access |
|--|--|------|----|----------------------------|--------|
|  | Algorithms   |      | со |                            | Types  |
| Symmetric<br>Encryption/Decry            | Encrypts or decrypts a block<br>of data using AES                  | Х    |    | AES Keys                   | R      |
| ption                                    | Encrypts or decrypts a block<br>of data using 3-Key Triple-<br>DES |      |    | Triple-DES Keys            |        |
| RSA Digital<br>Signature<br>Verification | Verify signature operations<br>for RSA PKCS#1v1.5                  | Х    |    | RSA public/private<br>keys | R      |
| Key Wrapping                             | Encrypt and decrypt a key using RSA primitives                     | Х    |    | RSA public/private<br>keys | R      |
|  | Wrap and unwrap keys with:   | Х    |    | AES keys                   | R      |

 Table 4: Services in the FIPS-approved mode of operation.

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| Service                                 | Service Description and<br>Algorithms  | Ro      | le   | Keys and CSPs                                 | Access  |
|---|--|---------|------|---|---------|
| Service                                 |  | User    | СО   | Reys and CSFS                                 | Types   |
|   | <ul> <li>AES-KW</li> <li>AES-CCM</li> <li>AES-GCM</li> <li>AES (ECB, CBC, CTR)<br/>and HMAC</li> </ul>   |         |      |   |         |
| Message<br>Authentication<br>Code (MAC) | Authenticate and verify<br>authentication of data using<br>HMAC-SHA-1, HMAC-SHA2-<br>224, HMAC-SHA2-256, HMAC-<br>SHA2-384, HMAC-SHA2-512<br>HMAC-SHA3-224, HMAC-<br>SHA3-256, HMAC-SHA3-384,<br>HMAC-SHA3-512 | X       |      | HMAC Key                                      | R       |
|   | Authenticate and verify<br>authentication of data using<br>CMAC with AES   | Х       |      | AES Key                                       | R       |
|   | Authenticate and verify<br>authentication of data using<br>CMAC with Triple-DES  | X       |      | Triple-DES Key                                | R       |
| Message Digest                          | Hash a block of data with:<br>SHA-1, SHA2-224, SHA2-256,<br>SHA2-384, SHA2-512<br>SHA3-224, SHA3-256, SHA3-<br>384, SHA3-512   | X       |      | None  | N/A     |
| Random Number<br>Generation             | Generate random numbers<br>based on the SP 800-90A<br>DRBG   | X       |      | Entropy input<br>string and internal<br>state | C, R, U |
|   | Other FIPS-relate  | ed Serv | ices |   | -       |
| Show Status                             | Show status of the module state  | X       |      | None  | N/A     |
| Self-Tests                              | Initiate power-on self-tests   | Х       |      | None  | N/A     |
| Zeroization                             | Zeroize all critical security parameters   | Х       |      | All keys and CSPs                             | Z       |
| Module<br>Installation                  | Installation of the module   |         | Х    | None  | N/A     |
| Module<br>Configuration                 | Configuration of the module  |         | Х    | None  | N/A     |

### 4.2.2 Services in the Non-FIPS-Approved Mode of Operation

Table 5 presents the services only available in non-FIPS-approved mode of operation.

| Service   | Service Description<br>and Algorithms                      | Role |    | Keys  | Access  |
|---|--|------|----|---|---------|
| Service   |  | User | со | . Keys  | Types   |
| AES<br>encryption/decryption                      | XTS block chaining mode                                    | X    |    | 192-bit AES key                                 | C, R, U |
|   | CTS block chaining mode                                    |      |    | 128/192/256-bit<br>key                          |         |
|   | OFB block chaining mode                                    |      |    |   |         |
|   | GCM with external IV generation                            |      |    |   |         |
| Triple-DES<br>encryption/decryption               | 2-key Triple-DES<br>encryption/decryption                  | Х    |    | 112-bit Triple-<br>DES key                      | C, R, U |
| HMAC MAC<br>generation                            | Keyed message digest                                       | X    |    | HMAC key<br>smaller than<br>112 bits            | C, R, U |
| CBC-MAC MAC generatoin                            | CBC-MAC operation<br>outside the CCM<br>operation          | X    |    | Triple-DES or<br>AES key                        | C, R, U |
| GHASH message<br>digest generation                | GHASH operation<br>outside the GCM<br>operation            | X    |    | AES key   | C, R, U |
| RSA operations                                    | Encrypts or decrypts<br>using non-Approved<br>RSA key size | Х    |    | RSA<br>public/private<br>key                    | C, R, U |
|   | Sign or verify<br>primitives                               |      |    |   |         |
| Diffie-Hellman shared secret computation          | Shared secret<br>computation<br>KAS FFC                    | X    |    | Diffie-Hellman<br>public/private<br>keys        | C, R, U |
|   |  |      |    | Shared secret                                   | C, R, U |
| EC Diffie-Hellman<br>shared secret<br>computation | Shared secret<br>computation KAS ECC                       | X    |    | EC Diffie-<br>Hellman<br>public/private<br>keys | R       |

Table 5: Services in the non-FIPS approved mode of operation.

### 4.3 Algorithms

The module implements cryptographic algorithms that are used by the services provided by the module. The cryptographic algorithms that are approved to be used in the FIPS mode of operation are tested and validated by the CAVP.

The module provides assembler implementations for AES, Triple-DES and SHS; support for the AVX2, AVX, AESNI and SSSE3 instruction sets from the CPU (per the tested operational

environment in Table 2) for AES and SHS; CLMUL for the GHASH algorithm used in the GCM block mode; and C-language generic implementations for the rest of the algorithms.

Table 6, Table 7 and Table 8 present the cryptographic algorithms in specific modes of operation. These tables include the CAVP certificates for different implementations, the algorithm name, respective standards, the available modes and key sizes wherein applicable, and usage. Information from certain columns may be applicable to more than one row.

#### 4.3.1 **FIPS-Approved**

Table 6 lists the cryptographic algorithms that are approved to be used in the FIPS mode of operation. Appendix B brings a list of the names contained in the algorithm certificates (e.g., aesni\_iiv), which refer to the specific algorithm implementations, and their description.

Note: some algorithms for which a CAVP certificate was issued may be not implemented by the module as FIPS approved algorithms.

| Algorithm | Standard                 | Mode/Meth<br>od                 | Key size                 | Use                                | CAVP Cert#   |
|-----------|--------------------------|---------------------------------|--------------------------|------------------------------------|--|
| AES       | [FIPS197]<br>[SP800-38A] | CBC, CTR <sup>2</sup> ,<br>ECB, | 128, 192 and<br>256 bits | Data Encryption<br>and Decryption  | A3671 (DH_C)<br>A3672 (ECDH_C)<br>A3673 (C_C)<br>A3675 (IIV_C_C)<br>A3676 (CTI_C)<br>A3676 (CTI_C)<br>A3677 (IIV_CTI_C)<br>A3678 (ARM64_CE)<br>A3679<br>(ARM_64_CE_C)<br>A3680<br>(IIV_ARM64_CE_C)<br>C911 (aesasm)<br>C912 (aesasm-iiv) <sup>5</sup><br>C913 (aesgen)<br>C914 (aesgen_iiv) <sup>5</sup><br>C915 (aesni)<br>C917 (aesni_iiv) |
|           | [FIPS197]<br>[SP800-38B] | CMAC                            | 128, 192 and<br>256 bits | MAC Generation<br>and Verification | A3673 (C_C)<br>A3676 (CTI_C)<br>A3678 (ARM64_CE)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)  |

Table 6: FIPS-approved cryptographic algorithms.

<sup>2</sup> AES-CTR tested for encryption only.

| Algorithm | Standard                 | Mode/Meth<br>od                     | Key size                 | Use                               | CAVP Cert#   |
|-----------|--------------------------|-------------------------------------|--------------------------|-----------------------------------|--|
|           | [FIPS197]<br>[SP800-38C] | ССМ                                 | 128, 192 and<br>256 bits | Data Encryption<br>and Decryption | A3673 (C_C)<br>A3676 (CTI_C)<br>A3678 (ARM64_CE)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)  |
|           | [FIPS197]<br>[SP800-38D] | GCM<br>(internal IV,<br>mode 8.2.2) | 128, 192 and<br>256 bits | Data Encryption<br>and Decryption | A3675 (IIV_C_C)<br>A3677 (IIV_CTI_C)<br>A3680<br>(IIV_ARM64_CE_C)<br>C912 (aesasm-iiv) <sup>6</sup><br>C914 (aesgen_iiv) <sup>6</sup><br>C917 (aesni_iiv) <sup>3</sup> |
|           |                          | GCM<br>(external IV)                | 128, 192 and<br>256 bits | Data Decryption                   | A3673 (C_C)<br>A3676 (CTI_C)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)  |
|           |                          | GMAC                                | 128, 192 and<br>256 bits | Message<br>Authentication<br>Code | A3673 (C_C)<br>A3676 (CTI_C)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)  |
|           | [FIPS197]<br>[SP800-38E] | XTS                                 | 128 and 256<br>bits      | Data Encryption<br>and Decryption | A3673 (C_C)<br>A3676 (CTI_C)<br>A3678 (ARM64_CE)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)  |

<sup>3</sup> AES-GCM tested for encryption only.

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| Algorithm       | Standard                 | Mode/Meth<br>od  | Key size                 | Use                         | CAVP Cert#  |
|-----------------|--------------------------|--|--------------------------|-----------------------------|---|
|                 | [FIPS197]<br>[SP800-38F] | ĸw   | 128, 192 and<br>256 bits | Key Wrapping                | A3673 (C_C)<br>A3676 (CTI_C)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)   |
| DRBG [SP800-904 | [SP800-90A]              | Hash_DRBG<br>SHA-1,<br>SHA2-256,<br>SHA2-384,<br>SHA2-512<br>with/without<br>PR  | n/a                      | Random Number<br>Generation | A3671 (DH_C)<br>A3672 (ECDH_C)<br>A3673 (C_C)<br>A3676 (CTI_C)<br>C921 (shassse3) <sup>4</sup><br>C920 (shagen)<br>C919 (shaavx2)<br>C918 (shaavx) <sup>8</sup> |
|                 |                          | HMAC_DRB<br>G SHA-1,<br>SHA2-256,<br>SHA2-384,<br>SHA2-512<br>with/without<br>PR | n/a                      | Random Number<br>Generation | A3671 (DH_C)<br>A3672 (ECDH_C)<br>A3673 (C_C)<br>A3676 (CTI_C)<br>C921 (shassse3) <sup>8</sup><br>C920 (shagen)<br>C919 (shaavx2)<br>C918 (shaavx) <sup>8</sup> |
|                 |                          | CTR_DRBG<br>AES128,<br>AES192,<br>AES256<br>with DF,<br>with/without<br>PR       | n/a                      | Random Number<br>Generation | A3671 (DH_C)<br>A3672 (ECDH_C)<br>A3673 (C_C)<br>A3676 (CTI_C)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)                       |

<sup>4</sup> The SHASSSE3 and SHAAVX implementations do not include SHA2-224, SHA2-384, HMAC-SHA2-224, HMAC-SHA2-384, Hash\_DRBG with SHA2-384 and HMAC\_DRBG with SHA2-384.

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| Algorithm  | Standard                  | Mode/Meth<br>od   | Key size               | Use                                | CAVP Cert#   |
|------------|---------------------------|---|------------------------|------------------------------------|--|
| НМАС       | [FIPS198-1]               | SHA-1,<br>SHA2-224,<br>SHA2-256,<br>SHA2-384,<br>SHA2-512 | 112 bits or<br>greater | Message<br>Authentication<br>Code  | A3671 (DH_C)<br>A3672 (ECDH_C)<br>A3673 (C_C)<br>A3678 (ARM64_CE)<br>A3681<br>(ARM64_ASM)<br>C921 (shassse3) <sup>8</sup><br>C920 (shagen)<br>C919 (shaavx2)<br>C918 (shaavx) <sup>8</sup> |
|            |                           | SHA3-224,<br>SHA3-256,<br>SHA3-384,<br>SHA3-512           |                        |                                    | A3674 (SHA_C_C)<br>C923  |
| RSA        | [FIPS186-4]               | PKCS#1<br>v1.5 with<br>SHA2-256,<br>SHA2-512              | 2048 and<br>3072 bits  | Signature<br>Verification          | A3673 (C_C)<br>C923<br>C921 (shassse3)<br>C920 (shagen)<br>C919 (shaavx2)<br>C918 (shaavx)   |
| SHS        | [FIPS180-4]               | SHA-1,<br>SHA2-224,<br>SHA2-256,<br>SHA2-384,<br>SHA2-512 | N/A                    | Message Digest                     | A3671 (DH_C)<br>A3672 (ECDH_C)<br>A3673 (C_C)<br>A3678 (ARM64_CE)<br>A3681<br>(ARM64_ASM)<br>C921 (shassse3) <sup>8</sup><br>C920 (shagen)<br>C919 (shaavx2)<br>C918 (shaavx) <sup>8</sup> |
| SHA-3      | [FIPS202]                 | SHA3-224,<br>SHA3-256,<br>SHA3-384,<br>SHA3-512           | N/A                    | Message Digest                     | A3674 (SHA3_C_C)<br>C923   |
| Triple-DES | [SP800-67]<br>[SP800-38A] | ECB, CBC,<br>CTR,   | 192 bits               | Data Encryption<br>and Decryption  | A3673 (C_C)<br>C923  |
|            | [SP800-67]<br>[SP800-38B] | СМАС  | 192 bits               | MAC Generation<br>and Verification |  |

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| Algorithm | Standard    | Mode/Meth<br>od   | Key size              | Use          | CAVP Cert#  |
|-----------|-------------|-------------------|-----------------------|--------------|---|
| KTS       | [SP800-38F] | AES-KW            | 128, 192, 256<br>bits | Key Wrapping | A3673 (C_C)<br>A3676 (CTI_C)<br>A3679<br>(ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)   |
|           |             | AES-[GCM,<br>CCM] | 128, 192, 256<br>bits | Key Wrapping | A3673 (C_C)<br>A3675 (IIV_C_C)<br>A3676 (CTI_C)<br>A3677 (IIV_CTI_C)<br>A3678 (ARM64_CE)<br>A3679<br>(ARM64_CE_C)<br>A3680<br>(IIV_ARM64_CE_C)<br>C911 (aesasm)<br>C913 (aesgen)<br>C915 (aesni)<br>C917 (aesni_iiv) <sup>6</sup><br>C912 (aesasm-iiv) <sup>6</sup><br>C914 (aesgen_iiv) <sup>6</sup> |

| Algorithm | Standard    | Mode/Meth<br>od                    | Key size               | Use                               | CAVP Cert#   |
|-----------|-------------|------------------------------------|------------------------|-----------------------------------|--|
|           |             | AES-[CBC,<br>CTR, ECB]<br>and HMAC | 128, 192, 256<br>bits  |                                   | A3671 (DH_C)<br>A3672 (ECDH_C)<br>A3673 (C_C)<br>A3675 (IIV_C_C)<br>A3676 (CTI_C)<br>A3676 (CTI_C)<br>A3677 (IIV_CTI_C)<br>A3678 (ARM64_CE)<br>A3679<br>(ARM_64_CE_C)<br>A3680<br>(IIV_ARM64_CE_C)<br>C917 (aesni_iiv)<br>C911 (aesasm)<br>C912 (aesasm-iiv) <sup>5</sup><br>C913 (aesgen)<br>C914 (aesgen_iiv) <sup>5</sup><br>C915 (aesni)<br>C921 (shassse3) <sup>8</sup><br>C920 (shagen)<br>C919 (shaavx2)<br>C918 (shaavx) |
|           | Alg         | orithms used                       | a from the bou         | IND NSS Module                    |  |
| НМАС      | [FIPS198-1] | SHA2-512                           | 112 bits or<br>greater | Message<br>Authentication<br>Code | A3741<br>C803  |

#### 4.3.3 Non-Approved-but-Allowed

Table 7 lists the non-Approved-but-Allowed cryptographic algorithms provided by the module that are allowed to be used in the FIPS mode of operation.

| Algorithm | Usage  |
|-----------|--|
| RSA       | Encrypt/Decrypt with keys between 2048 bits up to 15,360 or more |
| NDRNG     | Used for seeding NIST SP 800-90A DRBG.                           |

#### 4.3.4 Non-Approved

Table 8 lists the cryptographic algorithms that are not allowed to be used in the FIPS mode of operation. Use of any of these algorithms (and corresponding services in Table 5) will implicitly switch the module to the non-Approved mode.

| Algorithm         | Usage  |
|-------------------|--|
| AES               | XTS with 192-bit keys<br>CTS with 128/192/256-bit keys<br>OFB with 128/192/256-bit keys<br>GCM encryption with external IV generation with 128/192/256-bit<br>keys   |
| Triple-DES        | 2-key Triple-DES   |
| НМАС              | HMAC with keys smaller than 112 bits   |
| CBC-MAC           | CBC-MAC not part of CCM  |
| GHASH             | GHASH not part of GCM  |
| Diffie-Hellman    | Shared secret computation  |
| EC Diffie-Hellman | Shared secret computation  |
| RSA               | Encrypt/Decrypt with keys smaller than 2048 bits<br>Sign/Verify primitive operation (i.e., the computation of the hash of<br>the message if performed externally to the primitive, and the hash<br>must be provided to the primitive) for any key size |

#### Table 8: Non-FIPS approved cryptographic algorithms.

#### 4.4 Operator Authentication

The module does not support operator authentication mechanisms. The role of the operator is implicitly assumed based on the service requested.

## **5** Physical Security

The module is comprised of software only and thus this Security Policy does not claim any physical security.

## 6 Operational Environment

### 6.1 Applicability

The module operates in a modifiable operational environment per FIPS 140-2 Security Level 1 specifications. The module runs on the Amazon Linux 2 operating system executing on the hardware specified in Section 2.5.

### 6.2 Policy

The operating system is restricted to a single operator mode of operation (i.e., concurrent operators are explicitly excluded by the operating system).

The application that makes calls to the modules is the single user of the module, even when the application is serving multiple clients.

## 7 Cryptographic Key Management

Table 9 summarizes the keys and other CSPs that are used by the cryptographic services implemented in the module. All CSPs are zeroized when the cypher handle is freed (Section 7.4).

| Name                               | Name Use  |   | Entry and<br>Output                    |
|------------------------------------|---|---|--|
| AES Key<br>Triple-DES Key          | Encryption, decryption.<br>MAC generation and<br>verification for CMAC.   | N/A   | Entered via API<br>input<br>parameter. |
| НМАС Кеу                           | MAC generation and verification   |   | No output.                             |
| RSA public and private key         | Key Wrapping  | N/A   |  |
| Entropy input string               | Entropy input strings used in seeds to the DRBG.  | Obtained from NDRNG.  | N/A                                    |
| DRBG Internal state (V, C,<br>Key) | V and key are used<br>internally by HMAC and<br>CTR DRBGs. V and C are<br>used internally by HASH<br>DRBG. Used to generate<br>random bits. | Derived from<br>Entropy input as<br>defined in NIST<br>SP 800-90A |  |

Table 9: Lifecycle of keys and other Critical Security Parameters (CSPs).

#### 7.1 Random Number Generation

The module provides a DRBG compliant with [SP800-90A] for random number generation. The DRBG implements a Hash\_DRBG, CTR\_DRBG, and HMAC\_DRBG mechanisms and performs the health tests for the SP800-90A DRBG as defined per Section 11.3 of SP800-90A. The DRBG is initialized during module initialization and seeded from the in-kernel NDRNG. The NDRNG is within the module's physical boundary but outside the module's logical boundary. The NDRNG provides at least 256 bits of entropy to the DRBG.

The module performs continuous random number generator tests (CRNGT) on the output of NDRNG to ensure that consecutive random numbers do not repeat.

### 7.2 Key Generation

The module does not support key generation, manual key entry or intermediate key generation output. In addition, the module does not produce key output outside its physical boundary. The keys can be entered or output from the module in plaintext form via API parameters, to and from the calling application only.

### 7.3 Key/CSP Storage

All keys and CSPs keys are provided to the module by the calling process and are destroyed when released by the appropriate API function calls. The module does not perform persistent storage of keys.

### 7.4 Key/CSP Zeroization

The application is responsible for calling the appropriate destruction functions from the Kernel Crypto API. The destruction functions then overwrite the memory occupied by keys with zeros and deallocates the memory with the free() call. In case of abnormal termination, the keys in physical

memory are overwritten by the Linux kernel before the physical memory is allocated to another process.

### 7.5 Key Establishment

The module provides AES key wrapping per [SP800-38F] and RSA key wrapping or encapsulation as allowed by [FIPS140-2\_IG] D.9.

The module provides approved key transport methods as permitted by IG D.9. These key transport methods are provided either by using an approved key wrapping algorithm, an approved authenticated encryption mode, or a combination method of AES encryption and HMAC-SHA-1 or HMAC-SHA-2.

Table 6 and Table 7 specify the key sizes allowed in the FIPS mode of operation. According to "Table 2: Comparable strengths" in [SP800-57], the key sizes of AES key wrapping and RSA key wrapping provide the following security strengths:

- AES-KW key wrapping; key establishment methodology provides between 128 and 256 bits of encryption strength.
- AES-GCM and AES-CCM authenticated encryption key wrapping: key establishment methodology provides between 128 and 256 bits of encryption strength.
- Combination method of approved AES block mode (CTR, CBC, ECB) and message authentication code (HMAC-SHA-1/SHA-2) key wrapping: key establishment methodology provides between 128 and 256 bits of encryption strength.
- RSA key wrapping: key establishment methodology provides between 112 and 256 bits of encryption strength.

### 8 Electromagnetic Interference/Electromagnetic Compatibility (EMI/EMC)

The test platforms listed in Table 2 have been tested and found to conform to the EMI/EMC requirements specified by 47 Code of Federal Regulations, FCC PART 15, Subpart B, Unintentional Radiators, Digital Devices, Class A (i.e., Business use). These devices are designed to provide reasonable protection against harmful interference when the devices are operated in a commercial environment.

## 9 Self-Tests

#### 9.1 Power-Up Self-Tests

The module performs power-up tests when the module is loaded into memory, without operator intervention. Power-up tests ensure that the module is not corrupted and that the cryptographic algorithms work as expected.

While the module is executing the power-up tests, services are not available, and input and output are inhibited. The module will not return the control to the calling application until the power-up tests are completed successfully.

The module verifies its integrity through the following mechanisms:

- All kernel object (\*.ko) files are signed with a RSA and SHA2-512. Before these kernel
  objects are loaded into memory, the module performs RSA signature verification by using
  the RSA public key from the X.509 certificates that are compiled into the module's binary. If
  the signature cannot be verified, the kernel panics to indicate that the test fails and the
  module enters the error state.
- The integrity of the static kernel is ensured with the HMAC-SHA2-512 value stored in the .hmac file that was computed at build time. At run time, the module invokes the sha512hmac utility to calculate the HMAC value of the static kernel binary file, and then compares it with the pre-stored one. If the two HMAC values do not match, the kernel panics to indicate that the test fails, and the module enters the error state.
- The Integrity of the sha512hmac utility (i.e., /usr/bin/sha512hmac) is ensured with the HMAC-SHA2-512 value stored in the .hmac file (i.e. /usr/bin/.sha512hmac.hmac) that was computed at build time. At run time, the utility itself calculates the HMAC value of the utility, and then compares it with the pre-stored one. If the two HMAC values do not match, the kernel panics to indicate that the test fails, and the module enters the error state. The HMAC-SHA2-512 algorithm is provided by the bound NSS module and is KAT tested before the NSS module makes itself available to the sha512hmac application.

Both the RSA signature verification and HMAC-SHA2-512 algorithms are approved algorithms implemented in the Kernel Crypto API module and the bound NSS module.

Table 10 details the self-tests that are performed on the FIPS-approved cryptographic algorithms supported in the FIPS-approved mode of operation, using the Known-Answer Tests<sup>9</sup> (KATs) and Pairwise Consistency Tests (PCTs). For algorithms that have more than one implementation in the module (per Table 6), the module performs the self-tests independently for each of these implementations.

| Algorithm  | Test  |
|------------|---|
| AES        | <ul> <li>KAT (CBC) with 128/192/256-bit keys, encryption and decryption</li> <li>KAT (GCM) with 128/192/256-bit key, encryption and decryption</li> <li>KAT (CMAC) with 128/256-bit keys, encryption</li> <li>KAT (XTS) with 128/256-bit keys, encryption and decryption</li> </ul> |
| Triple-DES | <ul> <li>KAT (CBC) with 192-bit key, encryption and decryption</li> <li>KAT (CMAC) with 192-bit key, encryption</li> </ul>  |

#### Table 10: Self-tests.

<sup>9</sup> The module also implements Diffie-Hellman and EC Diffie-Hellman "Z" computation KATs. These algorithms are non-approved for this module.

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| Algorithm        | Test  |
|------------------|---|
| RSA              | <ul> <li>KAT RSA PKCS#1v1.5 signature verification with 2048-bit key and using<br/>SHA2-256 and SHA2-512</li> </ul> |
|                  | KAT RSA encryption and decryption primitives with 2048-bit key  |
| DRBG             | • KAT (CTR) using AES-128/256 with DF, without PR   |
|                  | KAT (HMAC) using SHA2-256 with and without PR   |
|                  | KAT (Hash) using SHA2-256 with and without PR   |
| НМАС             | • KAT HMAC-SHA-1  |
|                  | • KAT HMAC-SHA2-224   |
|                  | • KAT HMAC-SHA2-256   |
|                  | KAT HMAC-SHA2-384   |
|                  | KAT HMAC-SHA2-512   |
|                  | KAT HMAC-SHA3-224   |
|                  | KAT HMAC-SHA3-256   |
|                  | KAT HMAC-SHA3-384     KAT HMAC SHA3-512   |
|                  | KAT HMAC-SHA3-512   |
| SHS              | KAT SHA-1   |
|                  | • KAT SHA2-224  |
|                  | • KAT SHA2-256  |
|                  | • KAT SHA2-384  |
|                  | • KAT SHA2-512  |
| SHA3             | • KAT SHA3-224  |
|                  | • KAT SHA3-256  |
|                  | • KAT SHA3-384  |
|                  | • KAT SHA3-512  |
| Module Integrity | HMAC-SHA2-512 provided by Amazon Linux 2 NSS Crypto Module  |

### 9.2 Conditional Self-Tests

Conditional tests are performed during operational state of the module when the respective crypto functions are used. If any of the conditional tests fails, module transitions to error state.

Table 11 lists the conditional self-tests performed by the functions.

| Table 11: Conditional self-te | ests. |
|-------------------------------|-------|
|-------------------------------|-------|

| Algorithm | Test  |
|-----------|---|
| NDRNG     | Continuous Random Number Generator Test (CRNGT) |

### 9.3 On-Demand self-tests

The module provides the Self-Test service to perform self-tests on demand. On demand self-tests can be invoked by powering-off and reloading the module. This service performs the same

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cryptographic algorithm tests executed during power-up. During the execution of the on-demand self-tests, cryptographic services are not available, and no data output or input is possible.

### **10** Guidance

This section provides guidance for the Crypto Officer and the User to maintain proper use of the module per FIPS 140-2 requirements.

#### 10.1 Crypto-Officer Guidance

The RPM files containing the FIPS validated module referenced in Section 2.3 must be installed according to this guidance.

The bound NSS module shall be installed according to its own documentation and Security Policy.

For proper operation of the in-module integrity verification, the prelink shall be disabled. This can be done by setting PRELINKING=no in the /etc/sysconfig/prelink configuration file. If the libraries were already prelinked, the prelink shall be undone on all the system files using the 'prelink -u -a' command.

To configure the operating environment to support FIPS perform the following steps:

1. Install the dracut-fips package.

x86\_64:

# yum install dracut-fips-033-535.amzn2.1.3.x86\_64.rpm

aarch64:

# yum install dracut-fips-033-535.amzn2.1.3.aarch64.rpm

2. Recreate the INITRAMFS image.

# dracut -f

After regenerating the initramfs, the Crypto Officer shall append the following string to the kernel command line by changing the setting in the boot loader:

fips=1

If /boot or /boot/efi resides on a separate partition, the kernel parameter boot=<partition of /boot or /boot/efi> must be supplied. The partition can be identified with the command "df /boot" or "df /boot/efi".

#### For example:

| \$ df /boot |           |       |           |      |       |        |
|-------------|-----------|-------|-----------|------|-------|--------|
| Filesystem  | 1K-blocks | Used  | Available | Use% | Mount | ted on |
| /dev/sda1   | 233191    | 30454 | 19029     | 96   | 14%   | /boot  |

The partition of /boot is located on /dev/sda1 in the example above. Therefore, the following string needs to be appended to the kernel command line:

"boot=/dev/sda1"

When supporting other formats such as boot=UUID/LABEL, please refer to the FIPS section of the 'dracut.cmdline' man page.

Reboot to apply these settings. After the reboot, the file /proc/sys/crypto/fips\_enabled will contain 1. If the file does not exist or does not contain "1", the operational environment is not configured to support FIPS and the module will not operate as a FIPS validated module.

After performing the configuration described above, the Crypto Officer shall proceed for module installation with the version of the RPM package listed in Section 2.3. The integrity of the RPM is automatically verified during the installation of the modules and the Crypto Officer shall not install the RPM file if the RPM tool indicates an integrity error

#### 10.2 User Guidance

For detailed description of the Linux Kernel Crypto API, please refer to the user documentation [KC API Architecture].

In order to run in FIPS mode, the module must be operated using the FIPS Approved services, with their corresponding FIPS Approved and FIPS allowed cryptographic algorithms provided in this Security Policy (see section 4.2). In addition, key sizes must comply with [SP800-131A].

#### 10.2.1 AES GCM IV

In case the module's power is lost and then restored, the key used for the AES-GCM encryption or decryption shall be redistributed.

When a GCM IV is used for encryption, the module complies with IG A.5 Scenario 2 [FIPS140-2\_IG], in which the GCM IV is generated internally at its entirety randomly.

When a GCM IV is used for decryption, the responsibility for the IV generation lies with the party that performs the AES-GCM encryption therefore there is no restriction on the IV generation.

#### 10.2.2 Triple-DES Data Encryption

Data encryption using the same three-key Triple-DES key shall not exceed 2<sup>16</sup> Triple-DES (64-bit) blocks, in accordance to [SP800-67] and IG A.13 in [FIPS140-2-IG]. The user of the module is responsible for ensuring the module's compliance with this requirement.

#### 10.2.3 AES-XTS

The AES algorithm in XTS mode can be only used for the cryptographic protection of data on storage devices, as specified in [SP800-38E]. In addition, the length of a single data unit encrypted with the XTS-AES shall not exceed 2<sup>20</sup> AES blocks, that is, 16 MiB of data.

In addition, to meet the requirement in [FIPS140-2\_IG] A.9, the module implements a check to ensure that the two AES keys used in XTS-AES algorithm are not identical.

Note: AES-XTS shall be used with 128 and 256-bit keys only. AES-XTS with 192-bit keys is not an Approved service.

#### 10.2.4 Key Usage and Management

In general, a single key shall be used for only one purpose (e.g., encryption, integrity, authentication, key wrapping, random bit generation, or digital signatures) and be disjoint between the modes of operations of the module. Thus, if the module is switched between its FIPS mode and non-FIPS mode or vice versa, the following procedures shall be observed:

- The DRBG engine shall be reseeded.
- CSPs and keys shall not be shared between security functions of the two different modes.

#### **10.3 Handling Self-Test Errors**

When the module fails any self-test, it will panic the kernel and the operating system will not load. Errors occurred during the self-tests transition the module into the error state. The only way to recover from this error state is to reboot the system. If the failure persists, the module must be reinstalled by the Crypto Officer following the instructions as specified in section 10.1.

The kernel dumps self-test success and failure messages into the kernel message ring buffer. The user can use **dmesg** to read the contents of the kernel ring buffer. The format of the ring buffer (dmesg) output for self-test status is:

alg: self-tests for %s (%s) passed

Typical messages are similar to "alg: self-tests for xts(aes) (xts(aes)) passed" for each algorithm/sub-algorithm type.

The only way to recover from the error state is to reload the module and restart the application. If failures persist, the module must be reinstalled.

### **11** Mitigation of Other Attacks

The module does not implement mitigation of other attacks.

## Appendix A. Glossary and Abbreviations

|         | -  |
|---------|--|
| AES     | Advanced Encryption Standard                                   |
| AES-NI  | Advanced Encryption Standard New Instructions                  |
| API     | Application Program Interface                                  |
| CAVP    | Cryptographic Algorithm Validation Program                     |
| CAVS    | Cryptographic Algorithm Validation System                      |
| CBC     | Cipher Block Chaining  |
| ССМ     | Counter with Cipher Block Chaining-Message Authentication Code |
| CMAC    | Cipher-based Message Authentication Code                       |
| CMVP    | Cryptographic Module Validation Program                        |
| CRNGT   | Continuous Random Number Generator Test                        |
| CSP     | Critical Security Parameter                                    |
| CTR     | Counter Mode   |
| DES     | Data Encryption Standard                                       |
| DF      | Derivation Function  |
| DRBG    | Deterministic Random Bit Generator                             |
| ECB     | Electronic Code Book   |
| EMI/EMC | Electromagnetic Interference/Electromagnetic Compatibility     |
| FCC     | Federal Communications Commission                              |
| FIPS    | Federal Information Processing Standards Publication           |
| GCM     | Galois Counter Mode  |
| HMAC    | Hash Message Authentication Code                               |
| IG      | Implementation Guidance  |
| KAT     | Known Answer Test  |
| KW      | Key Wrap   |
| MAC     | Message Authentication Code                                    |
| NIST    | National Institute of Science and Technology                   |
| NDRNG   | Non-Deterministic Random Number Generator                      |
| PAA     | Processor Algorithm Acceleration                               |
| РСТ     | Pair-wise Consistency Test                                     |
| PR      | Prediction Resistance  |
| RSA     | Rivest, Shamir, Addleman                                       |
| SHA     | Secure Hash Algorithm  |
| SHS     | Secure Hash Standard   |
| SSSE3   | Supplemental Streaming SIMD Extensions 3                       |
| XTS     | XEX-based Tweaked-codebook mode with ciphertext Stealing       |
|         |  |

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### **Appendix B. Algorithm Implementations**

Table 12 describes the names utilized in the algorithm certificates and the implementations to which they refer for the test platforms. For instance, CAVP Cert. C918 refers to shaavx, which is the SHA algorithms implemented using AVX instructions.

| Cert. | Name       | Description  |
|-------|------------|--|
| C918  | shaavx     | SHA implementation using AVX instructions                    |
| C919  | shaavx2    | SHA implementation using AVX2 instructions                   |
| C920  | shagen     | SHA implementation using generic C                           |
| C921  | shassse3   | SHA implementation using SSSE3 instructions                  |
| C911  | aesasm     | AES implementation using assembler, and GCM with external IV |
| C912  | aesasm_iiv | AES implementation using assembler, and GCM with internal IV |
| C913  | aesgen     | AES implementation using C, and GCM with external IV         |
| C914  | aesgen_iiv | AES implementation using C, and GCM with internal IV         |
| C915  | aesni      | AES implementation with AESNI, and GCM with external IV      |
| C917  | aesni_iiv  | AES implementation using AESNI, and GCM with internal IV     |
| C923  | TDES       | C implementations  |

*Table 12: Algorithm implementations and their names in the CAVP certificates.* 

## **Appendix C. References**

| FIPS140-2              | FIPS PUB 140-2 - Security Requirements For Cryptographic Modules<br>May 2001   |
|------------------------|--|
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| PKCS#1                 | Public Key Cryptography Standards (PKCS) #1: RSA Cryptography<br>Specifications Version 2.1<br>February 2003<br>http://www.ietf.org/rfc/rfc3447.txt  |
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