

# Sandy2x: Fastest Curve25519 Implementation Ever

Tung Chou

Technische Universiteit Eindhoven, The Netherlands

June 12, 2015

# X25519 and Ed25519

## X25519

- ECDH scheme
- public keys and shared secrets are points on the Montgomery curve

$$y^2 = x^3 + 486662x^2 + x$$

over  $\mathbb{F}_{2^{255}-19}$

- by Bernstein, 2006

## Ed25519

- signature scheme
- public keys and (part of) signatures are points on the twisted Edwards curve

$$-x^2 + y^2 = 1 - 121665/121666x^2y^2$$

over  $\mathbb{F}_{2^{255}-19}$

- by Bernstein, Duif, Lange, Schwabe, and Yang, 2011

# The big multiplier

# The big multiplier

- used in all papers about ECC speeds on Intel microarchitectures

# The big multiplier

- used in all papers about ECC speeds on Intel microarchitectures
- $64 \times 64 \rightarrow 128$ -bit multiplication in one instruction (`mul`)

# The big multiplier

- used in all papers about ECC speeds on Intel microarchitectures
- $64 \times 64 \rightarrow 128$ -bit multiplication in one instruction (`mul`)
- (This talk focuses on Sandy Bridge/Ivy Bridge)

The radix- $2^{51}$  representation for  $\mathbb{F}_{2^{255}-19}$

The radix- $2^{51}$  representation for  $\mathbb{F}_{2^{255}-19}$

$$f = f_0 + f_1 2^{51} + f_2 2^{102} + f_3 2^{153} + f_4 2^{204}$$



## The radix- $2^{51}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned} f &= f_0 + f_1 2^{51} + f_2 2^{102} + f_3 2^{153} + f_4 2^{204} \\ g &= g_0 + g_1 2^{51} + g_2 2^{102} + g_3 2^{153} + g_4 2^{204} \end{aligned}$$

## The radix- $2^{51}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned} f &= f_0 + f_1 2^{51} + f_2 2^{102} + f_3 2^{153} + f_4 2^{204} \\ g &= g_0 + g_1 2^{51} + g_2 2^{102} + g_3 2^{153} + g_4 2^{204} \end{aligned}$$

$$\begin{aligned} h_0 &= f_0 g_0 + 19 f_1 g_4 + 19 f_2 g_3 + 19 f_3 g_2 + 19 f_4 g_1 \\ h_1 &= f_0 g_1 + f_1 g_0 + 19 f_2 g_4 + 19 f_3 g_3 + 19 f_4 g_2 \\ h_2 &= f_0 g_2 + f_1 g_1 + f_2 g_0 + 19 f_3 g_4 + 19 f_4 g_3 \\ h_3 &= f_0 g_3 + f_1 g_2 + f_2 g_1 + f_3 g_0 + 19 f_4 g_4 \\ h_4 &= f_0 g_4 + f_1 g_3 + f_2 g_2 + f_3 g_1 + f_4 g_0 \end{aligned}$$

## The radix- $2^{51}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned} f &= f_0 + f_1 2^{51} + f_2 2^{102} + f_3 2^{153} + f_4 2^{204} \\ g &= g_0 + g_1 2^{51} + g_2 2^{102} + g_3 2^{153} + g_4 2^{204} \end{aligned}$$

$$\begin{aligned} h_0 &= f_0 g_0 + 19 f_1 g_4 + 19 f_2 g_3 + 19 f_3 g_2 + 19 f_4 g_1 \\ h_1 &= f_0 g_1 + f_1 g_0 + 19 f_2 g_4 + 19 f_3 g_3 + 19 f_4 g_2 \\ h_2 &= f_0 g_2 + f_1 g_1 + f_2 g_0 + 19 f_3 g_4 + 19 f_4 g_3 \\ h_3 &= f_0 g_3 + f_1 g_2 + f_2 g_1 + f_3 g_0 + 19 f_4 g_4 \\ h_4 &= f_0 g_4 + f_1 g_3 + f_2 g_2 + f_3 g_1 + f_4 g_0 \end{aligned}$$

- 25 multiplication instructions + overhead.

## The radix- $2^{51}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned} f &= f_0 + f_1 2^{51} + f_2 2^{102} + f_3 2^{153} + f_4 2^{204} \\ g &= g_0 + g_1 2^{51} + g_2 2^{102} + g_3 2^{153} + g_4 2^{204} \end{aligned}$$

$$\begin{aligned} h_0 &= f_0 g_0 + 19 f_1 g_4 + 19 f_2 g_3 + 19 f_3 g_2 + 19 f_4 g_1 \\ h_1 &= f_0 g_1 + f_1 g_0 + 19 f_2 g_4 + 19 f_3 g_3 + 19 f_4 g_2 \\ h_2 &= f_0 g_2 + f_1 g_1 + f_2 g_0 + 19 f_3 g_4 + 19 f_4 g_3 \\ h_3 &= f_0 g_3 + f_1 g_2 + f_2 g_1 + f_3 g_0 + 19 f_4 g_4 \\ h_4 &= f_0 g_4 + f_1 g_3 + f_2 g_2 + f_3 g_1 + f_4 g_0 \end{aligned}$$

- 25 multiplication instructions + overhead.
- some *carries* required.

## A small multiplier

## A small multiplier

- a 2-way vectorized multiplier

## A small multiplier

- a 2-way vectorized multiplier
- $32 \times 32 \rightarrow 64$ -bit multiplications in one instruction (`vpmuludq`)

# A small multiplier

- a 2-way vectorized multiplier
- $32 \times 32 \rightarrow 64$ -bit multiplications in one instruction (`vpmuludq`)
- usage:

$$(a_0 b_0, a_1 b_1) = (a_0, a_1) \times (b_0, b_1)$$



The radix- $2^{25.5}$  representation for  $\mathbb{F}_{2^{255}-19}$

# The radix- $2^{25.5}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned} f &= f_0 + f_1 2^{26} + f_2 2^{51} + f_3 2^{77} + f_4 2^{102} + f_5 2^{128} + f_6 2^{153} + f_7 2^{179} + f_8 2^{204} + f_9 2^{230} \\ g &= g_0 + g_1 2^{26} + g_2 2^{51} + g_3 2^{77} + g_4 2^{102} + g_5 2^{128} + g_6 2^{153} + g_7 2^{179} + g_8 2^{204} + g_9 2^{230} \end{aligned}$$

# The radix- $2^{25.5}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned}
 f &= f_0 + f_1 2^{26} + f_2 2^{51} + f_3 2^{77} + f_4 2^{102} + f_5 2^{128} + f_6 2^{153} + f_7 2^{179} + f_8 2^{204} + f_9 2^{230} \\
 g &= g_0 + g_1 2^{26} + g_2 2^{51} + g_3 2^{77} + g_4 2^{102} + g_5 2^{128} + g_6 2^{153} + g_7 2^{179} + g_8 2^{204} + g_9 2^{230}
 \end{aligned}$$

$$\begin{aligned}
 h_0 &= f_0 g_0 + 38 f_1 g_9 + 19 f_2 g_8 + 38 f_3 g_7 + 19 f_4 g_6 + 38 f_5 g_5 + 19 f_6 g_4 + 38 f_7 g_3 + 19 f_8 g_2 + 38 f_9 g_1 \\
 h_1 &= f_0 g_1 + f_1 g_0 + 19 f_2 g_9 + 19 f_3 g_8 + 19 f_4 g_7 + 19 f_5 g_6 + 19 f_6 g_5 + 19 f_7 g_4 + 19 f_8 g_3 + 19 f_9 g_2 \\
 h_2 &= f_0 g_2 + 2 f_1 g_1 + f_2 g_0 + 38 f_3 g_9 + 19 f_4 g_8 + 38 f_5 g_7 + 19 f_6 g_6 + 38 f_7 g_5 + 19 f_8 g_4 + 38 f_9 g_3 \\
 h_3 &= f_0 g_3 + f_1 g_2 + f_2 g_1 + f_3 g_0 + 19 f_4 g_9 + 19 f_5 g_8 + 19 f_6 g_7 + 19 f_7 g_6 + 19 f_8 g_5 + 19 f_9 g_4 \\
 h_4 &= f_0 g_4 + 2 f_1 g_3 + f_2 g_2 + 2 f_3 g_1 + f_4 g_0 + 38 f_5 g_9 + 19 f_6 g_8 + 38 f_7 g_7 + 19 f_8 g_6 + 38 f_9 g_5 \\
 h_5 &= f_0 g_5 + f_1 g_4 + f_2 g_3 + f_3 g_2 + f_4 g_1 + f_5 g_0 + 19 f_6 g_9 + 19 f_7 g_8 + 19 f_8 g_7 + 19 f_9 g_6 \\
 h_6 &= f_0 g_6 + 2 f_1 g_5 + f_2 g_4 + 2 f_3 g_3 + f_4 g_2 + 2 f_5 g_1 + f_6 g_0 + 38 f_7 g_9 + 19 f_8 g_8 + 38 f_9 g_7 \\
 h_7 &= f_0 g_7 + f_1 g_6 + f_2 g_5 + f_3 g_4 + f_4 g_3 + f_5 g_2 + f_6 g_1 + f_7 g_0 + 19 f_8 g_9 + 19 f_9 g_8 \\
 h_8 &= f_0 g_8 + 2 f_1 g_7 + f_2 g_6 + 2 f_3 g_5 + f_4 g_4 + 2 f_5 g_3 + f_6 g_2 + 2 f_7 g_1 + f_8 g_0 + 38 f_9 g_9 \\
 h_9 &= f_0 g_9 + f_1 g_8 + f_2 g_7 + f_3 g_6 + f_4 g_5 + f_5 g_4 + f_6 g_3 + f_7 g_2 + f_8 g_1 + f_9 g_0
 \end{aligned}$$

# The radix- $2^{25.5}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned}
 f &= f_0 + f_1 2^{26} + f_2 2^{51} + f_3 2^{77} + f_4 2^{102} + f_5 2^{128} + f_6 2^{153} + f_7 2^{179} + f_8 2^{204} + f_9 2^{230} \\
 g &= g_0 + g_1 2^{26} + g_2 2^{51} + g_3 2^{77} + g_4 2^{102} + g_5 2^{128} + g_6 2^{153} + g_7 2^{179} + g_8 2^{204} + g_9 2^{230}
 \end{aligned}$$

$$\begin{aligned}
 h_0 &= f_0 g_0 + 38 f_1 g_9 + 19 f_2 g_8 + 38 f_3 g_7 + 19 f_4 g_6 + 38 f_5 g_5 + 19 f_6 g_4 + 38 f_7 g_3 + 19 f_8 g_2 + 38 f_9 g_1 \\
 h_1 &= f_0 g_1 + f_1 g_0 + 19 f_2 g_9 + 19 f_3 g_8 + 19 f_4 g_7 + 19 f_5 g_6 + 19 f_6 g_5 + 19 f_7 g_4 + 19 f_8 g_3 + 19 f_9 g_2 \\
 h_2 &= f_0 g_2 + 2 f_1 g_1 + f_2 g_0 + 38 f_3 g_9 + 19 f_4 g_8 + 38 f_5 g_7 + 19 f_6 g_6 + 38 f_7 g_5 + 19 f_8 g_4 + 38 f_9 g_3 \\
 h_3 &= f_0 g_3 + f_1 g_2 + f_2 g_1 + f_3 g_0 + 19 f_4 g_9 + 19 f_5 g_8 + 19 f_6 g_7 + 19 f_7 g_6 + 19 f_8 g_5 + 19 f_9 g_4 \\
 h_4 &= f_0 g_4 + 2 f_1 g_3 + f_2 g_2 + 2 f_3 g_1 + f_4 g_0 + 38 f_5 g_9 + 19 f_6 g_8 + 38 f_7 g_7 + 19 f_8 g_6 + 38 f_9 g_5 \\
 h_5 &= f_0 g_5 + f_1 g_4 + f_2 g_3 + f_3 g_2 + f_4 g_1 + f_5 g_0 + 19 f_6 g_9 + 19 f_7 g_8 + 19 f_8 g_7 + 19 f_9 g_6 \\
 h_6 &= f_0 g_6 + 2 f_1 g_5 + f_2 g_4 + 2 f_3 g_3 + f_4 g_2 + 2 f_5 g_1 + f_6 g_0 + 38 f_7 g_9 + 19 f_8 g_8 + 38 f_9 g_7 \\
 h_7 &= f_0 g_7 + f_1 g_6 + f_2 g_5 + f_3 g_4 + f_4 g_3 + f_5 g_2 + f_6 g_1 + f_7 g_0 + 19 f_8 g_9 + 19 f_9 g_8 \\
 h_8 &= f_0 g_8 + 2 f_1 g_7 + f_2 g_6 + 2 f_3 g_5 + f_4 g_4 + 2 f_5 g_3 + f_6 g_2 + 2 f_7 g_1 + f_8 g_0 + 38 f_9 g_9 \\
 h_9 &= f_0 g_9 + f_1 g_8 + f_2 g_7 + f_3 g_6 + f_4 g_5 + f_5 g_4 + f_6 g_3 + f_7 g_2 + f_8 g_1 + f_9 g_0
 \end{aligned}$$

- 100 multiplication instructions + overhead; 50 per multiplication.
- some *carries* required.

# The radix- $2^{25.5}$ representation for $\mathbb{F}_{2^{255}-19}$

$$\begin{aligned}
 f &= f_0 + f_1 2^{26} + f_2 2^{51} + f_3 2^{77} + f_4 2^{102} + f_5 2^{128} + f_6 2^{153} + f_7 2^{179} + f_8 2^{204} + f_9 2^{230} \\
 g &= g_0 + g_1 2^{26} + g_2 2^{51} + g_3 2^{77} + g_4 2^{102} + g_5 2^{128} + g_6 2^{153} + g_7 2^{179} + g_8 2^{204} + g_9 2^{230}
 \end{aligned}$$

$$\begin{aligned}
 h_0 &= f_0 g_0 + 38 f_1 g_9 + 19 f_2 g_8 + 38 f_3 g_7 + 19 f_4 g_6 + 38 f_5 g_5 + 19 f_6 g_4 + 38 f_7 g_3 + 19 f_8 g_2 + 38 f_9 g_1 \\
 h_1 &= f_0 g_1 + f_1 g_0 + 19 f_2 g_9 + 19 f_3 g_8 + 19 f_4 g_7 + 19 f_5 g_6 + 19 f_6 g_5 + 19 f_7 g_4 + 19 f_8 g_3 + 19 f_9 g_2 \\
 h_2 &= f_0 g_2 + 2 f_1 g_1 + f_2 g_0 + 38 f_3 g_9 + 19 f_4 g_8 + 38 f_5 g_7 + 19 f_6 g_6 + 38 f_7 g_5 + 19 f_8 g_4 + 38 f_9 g_3 \\
 h_3 &= f_0 g_3 + f_1 g_2 + f_2 g_1 + f_3 g_0 + 19 f_4 g_9 + 19 f_5 g_8 + 19 f_6 g_7 + 19 f_7 g_6 + 19 f_8 g_5 + 19 f_9 g_4 \\
 h_4 &= f_0 g_4 + 2 f_1 g_3 + f_2 g_2 + 2 f_3 g_1 + f_4 g_0 + 38 f_5 g_9 + 19 f_6 g_8 + 38 f_7 g_7 + 19 f_8 g_6 + 38 f_9 g_5 \\
 h_5 &= f_0 g_5 + f_1 g_4 + f_2 g_3 + f_3 g_2 + f_4 g_1 + f_5 g_0 + 19 f_6 g_9 + 19 f_7 g_8 + 19 f_8 g_7 + 19 f_9 g_6 \\
 h_6 &= f_0 g_6 + 2 f_1 g_5 + f_2 g_4 + 2 f_3 g_3 + f_4 g_2 + 2 f_5 g_1 + f_6 g_0 + 38 f_7 g_9 + 19 f_8 g_8 + 38 f_9 g_7 \\
 h_7 &= f_0 g_7 + f_1 g_6 + f_2 g_5 + f_3 g_4 + f_4 g_3 + f_5 g_2 + f_6 g_1 + f_7 g_0 + 19 f_8 g_9 + 19 f_9 g_8 \\
 h_8 &= f_0 g_8 + 2 f_1 g_7 + f_2 g_6 + 2 f_3 g_5 + f_4 g_4 + 2 f_5 g_3 + f_6 g_2 + 2 f_7 g_1 + f_8 g_0 + 38 f_9 g_9 \\
 h_9 &= f_0 g_9 + f_1 g_8 + f_2 g_7 + f_3 g_6 + f_4 g_5 + f_5 g_4 + f_6 g_3 + f_7 g_2 + f_8 g_1 + f_9 g_0
 \end{aligned}$$

- 100 multiplication instructions + overhead; 50 per multiplication.
- some *carries* required.

Sandy2x sets new speed records by using the vectorized multiplier.

## Performance results

	SB cycles	IB cycles	reference
X25519 public-key generation	54 346	52 169	<b>Sandy2x</b>
	61 828	57 612	[A. Moon]
	194 165	182 876	[Ed25519]
X25519 shared secret computation	156 995	159 128	<b>Sandy2x</b>
	194 036	182 708	[Ed25519]
Ed25519 public-key generation	57 164	54 901	<b>Sandy2x</b>
	63 712	59 332	[A. Moon]
	64 015	61 099	[Ed25519]
Ed25519 sign	63 526	59 949	<b>Sandy2x</b>
	67 692	62 624	[A. Moon]
	72 444	67 284	[Ed25519]
Ed25519 verification	205 741	198 406	<b>Sandy2x</b>
	227 628	204 376	[A. Moon]
	222 564	209 060	[Ed25519]

- Andrew Moon “floodyberry”,  
<https://github.com/floodyberry/ed25519-donna>

# Why is vectorization better?

Ports

# Why is vectorization better?

## Ports

- one each SB/IB core there are 6 ports: Port 0,1,5 are for arithmetic.



# Why is vectorization better?

## Ports

- one each SB/IB core there are 6 ports: Port 0,1,5 are for arithmetic.
- each instruction is decomposed into *microoperations* ( $\mu$ ops)

# Why is vectorization better?

## Ports

- one each SB/IB core there are 6 ports: Port 0,1,5 are for arithmetic.
- each instruction is decomposed into *microoperations* ( $\mu$ ops)
  - mul: 2  $\mu$ ops, handled by Port 0 and 1.

# Why is vectorization better?

## Ports

- one each SB/IB core there are 6 ports: Port 0,1,5 are for arithmetic.
- each instruction is decomposed into *microoperations* ( $\mu$ ops)
  - `mul`: 2  $\mu$ ops, handled by Port 0 and 1.
  - `vpmuludq`: 1  $\mu$ op, handled by Port 0.

# Why is vectorization better?

## Ports

- one each SB/IB core there are 6 ports: Port 0,1,5 are for arithmetic.
- each instruction is decomposed into *microoperations* ( $\mu$ ops)
  - `mul`: 2  $\mu$ ops, handled by Port 0 and 1.
  - `vpmuludq`: 1  $\mu$ op, handled by Port 0.
  - `vpaddq`: 1  $\mu$ op, handled by either Port 1 or Port 5.

# Why is vectorization better?

## Ports

- one each SB/IB core there are 6 ports: Port 0,1,5 are for arithmetic.
- each instruction is decomposed into *microoperations* ( $\mu$ ops)
  - `mul`: 2  $\mu$ ops, handled by Port 0 and 1.
  - `vpmuludq`: 1  $\mu$ op, handled by Port 0.
  - `vpaddq`: 1  $\mu$ op, handled by either Port 1 or Port 5.
- port utilization gives a lower bound of cycle count

# Why is vectorization better?

Using the vectorized multiplier

- 109 `vpmuludq` + 95 `vpaddq`

# Why is vectorization better?

Using the vectorized multiplier

- 109 `vpmuludq` + 95 `vpaddq`
- lower bound: 109 cycles (dominated by Port 0)

# Why is vectorization better?

Using the vectorized multiplier

- 109 `vpmuludq` + 95 `vpaddq`
- lower bound: 109 cycles (dominated by Port 0)
- actual cycle count 112 cycles (**56 cycles** per multiplication)



# Why is vectorization better?

Using the vectorized multiplier

- 109 `vpmuludq` + 95 `vpaddq`
- lower bound: 109 cycles (dominated by Port 0)
- actual cycle count 112 cycles (**56 cycles** per multiplication)

Using the serial multiplier

- 25 `mul` + 4 `imul` + 20 `add` + 20 `adc`

# Why is vectorization better?

Using the vectorized multiplier

- 109 `vpmuludq` + 95 `vpaddq`
- lower bound: 109 cycles (dominated by Port 0)
- actual cycle count 112 cycles (56 cycles per multiplication)

Using the serial multiplier

- 25 `mul` + 4 `imul` + 20 `add` + 20 `adc`
- lower bound:  $(25 \cdot 2 + 4 + 20 + 20 \cdot 2)/3 = 38$

# Why is vectorization better?

Using the vectorized multiplier

- 109 `vpmuludq` + 95 `vpaddq`
- lower bound: 109 cycles (dominated by Port 0)
- actual cycle count 112 cycles (**56 cycles** per multiplication)

Using the serial multiplier

- 25 `mul` + 4 `imul` + 20 `add` + 20 `adc`
- lower bound:  $(25 \cdot 2 + 4 + 20 + 20 \cdot 2)/3 = 38$
- actual cycle count is much larger: **52 cycles**

# Why is vectorization better?

Using the vectorized multiplier

- 109 `vpmuludq` + 95 `vpaddq`
- lower bound: 109 cycles (dominated by Port 0)
- actual cycle count 112 cycles (**56 cycles** per multiplication)

Using the serial multiplier

- 25 `mul` + 4 `imul` + 20 `add` + 20 `adc`
- lower bound:  $(25 \cdot 2 + 4 + 20 + 20 \cdot 2)/3 = 38$
- actual cycle count is much larger: **52 cycles**
- `perf-stat` shows that the core fails to distribute the  $\mu$ ops equally over the ports

# Why is vectorization better?

More reasons

# Why is vectorization better?

More reasons

- carries take more cycles when using the serial multiplier

	$M^-$	$M$
serial	52	68
vectorized	56	69.5

# Why is vectorization better?

## More reasons

- carries take more cycles when using the serial multiplier

	$M^-$	$M$
serial	52	68
vectorized	56	69.5

- batched squarings are faster

# Why is vectorization better?

## More reasons

- carries take more cycles when using the serial multiplier

	$M^-$	$M$
serial	52	68
vectorized	56	69.5

- batched squarings are faster
- instruction interleaving hides cost for addition/subtraction



# Why is vectorization better?

## More reasons

- carries take more cycles when using the serial multiplier

	$M^-$	$M$
serial	52	68
vectorized	56	69.5

- batched squarings are faster
- instruction interleaving hides cost for addition/subtraction
- constant-time table lookups are faster with vector instructions

# The importance of small constant

# The importance of small constant

- consider using  $\mathbb{F}_{2^{255}-c}$

# The importance of small constant

- consider using  $\mathbb{F}_{2^{255}-c}$
- consider computation of  $(f - g)^2$ , which is one step of the Montgomery ladder

# The importance of small constant

- consider using  $\mathbb{F}_{2^{255}-c}$
- consider computation of  $(f - g)^2$ , which is one step of the Montgomery ladder
- $f, g$  has limbs of upper bound  $\approx 2^{26}$

# The importance of small constant

- consider using  $\mathbb{F}_{2^{255}-c}$
- consider computation of  $(f - g)^2$ , which is one step of the Montgomery ladder
- $f, g$  has limbs of upper bound  $\approx 2^{26}$
- $h = f - g$  has limbs of upper bound  $\approx 3 \cdot 2^{26}$

# The importance of small constant

- consider using  $\mathbb{F}_{2^{255}-c}$
- consider computation of  $(f - g)^2$ , which is one step of the Montgomery ladder
- $f, g$  has limbs of upper bound  $\approx 2^{26}$
- $h = f - g$  has limbs of upper bound  $\approx 3 \cdot 2^{26}$
- for  $(f - g)^2$  we need  $c \cdot h_6^2$ 
  - $c < 22$ : multiply by  $c \rightarrow$  multiply by  $h_6$
  - $c \geq 22$ : depends on  $c$ ; can be nasty

# Ending Remarks

Messages of this talk:



# Ending Remarks

Messages of this talk:

- Vectorization should be considered on recent Intel microarchitectures.

# Ending Remarks

Messages of this talk:

- Vectorization should be considered on recent Intel microarchitectures.
- The actual size of  $c$  in the prime  $2^b - c$  is important.

# Ending Remarks

Messages of this talk:

- Vectorization should be considered on recent Intel microarchitectures.
- The actual size of  $c$  in the prime  $2^b - c$  is important.

Code and slides can be found on

- <https://sites.google.com/a/crypto.tw/blueprint/>